

#### 1 Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol	Parameter	Value	Unit		
V <sub>RRM</sub>	Repetitive peak reverse voltage	200	V		
1	Average forward current $\delta$ = 0.5, square wave	SMA	T <sub>L</sub> = 145 °C	1	_
I <sub>F(AV)</sub>		DO-41	T <sub>L</sub> = 130 °C		Α
leau	SMA t = 10 ma signapidal	t <sub>n</sub> = 10 ms sinusoidal	40	Α	
IFSM	Surge non repetitive forward current $p = 10 \text{ ms sinusoidal}$ $p = 10 \text{ ms sinusoidal}$				
T <sub>stg</sub>	Storage temperature range	-65 to +175	°C		
Tj	Operating junction temperature	+175	°C		

Table 2. Thermal resistance parameter

Symbol		Max. value	Unit		
P., a.s.	Junction to lead		SMA	30	°C/W
$R_{th(j-l)}$	Junction to lead	Lead length = 10 mm	DO-41	50	C/VV

For more information, please refer to the following application note:

• AN5088 : Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics

	Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
	I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 25 °C	$V_R = V_{RRM}$	-		1	μA
			T <sub>j</sub> = 125 °C	VR - VRRM	-	1	25	
	V <sub>F</sub> <sup>(2)</sup>	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>E</sub> = 1 A	-		0.97	V
			T <sub>j</sub> = 125 °C	it = 1 \(\text{\tin}\text{\tint{\text{\tint{\text{\tin}\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\ti}}\tint{\text{\text{\tin}\tint{\text{\text{\texitt{\text{\text{\texi}\text{\text{\texit{\tin\tint{\text{\texit{\texi{\text{\ti}\tint{\ti}\tint{\titil\titit{\texi}\tilit{\tiint{\texit{\texi}\tint{\tint}\tint{\tint}\tint	-	0.68	0.78	<b>V</b>

- 1. Pulse test:  $t_p$  = 5 ms,  $\delta$  < 2%
- 2. Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2%

To evaluate the conduction losses, use the following equation:

$$P = 0.65 \times I_{F(AV)} + 0.130 \times I_{F^{2}(RMS)}$$

For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses on a power diode

DS2448 - Rev 6 page 2/12



# Table 4. Dynamic characteristics ( $T_j$ = 25 °C unless otherwise stated)

Symbol	Parameters	Test conditions		Тур.	Max.	Unit
t <sub>rr</sub>	Reverse recovery time	$I_F = 0.5 \text{ A}, I_{rr} = 0.25 \text{ A}, I_R = 1 \text{ A}$	-	12	20	ns
t <sub>fr</sub>	Forward recovery time	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/ms}, V_{FR} = 1.1 V_{F(max.)}$	-	50		ns
V <sub>FP</sub>	Forward recovery voltage	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}$	-	1.8		V

DS2448 - Rev 6 page 3/12



#### 1.1 Characteristics (curves)

Figure 1. Average forward power dissipation versus average forward current (SMA)

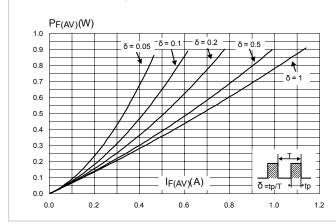


Figure 2. Average forward power dissipation versus average forward current (DO-41)

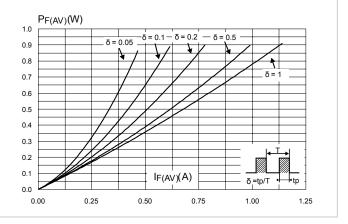


Figure 3. Average forward current versus ambient temperature ( $\delta$  = 0.5) (SMA)

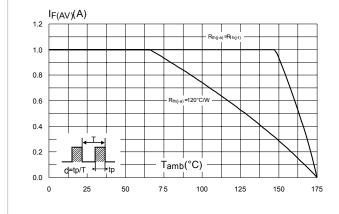
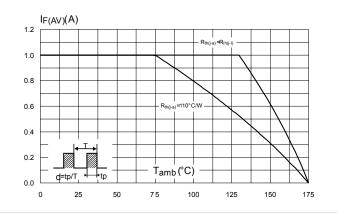


Figure 4. (DO-41)Average forward current versus ambient temperature ( $\delta$  = 0.5) (DO-41)



DS2448 - Rev 6 page 4/12



Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration (epoxy printed circuit board, e(Cu) =  $35 \mu m$ ,recommended pad layout) (SMA)

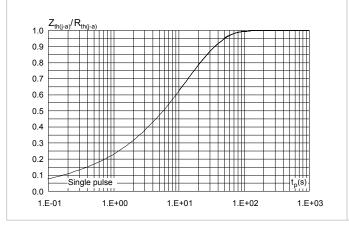


Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration (DO-41)

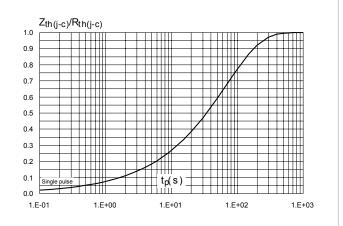


Figure 7. Forward voltage drop versus forward current

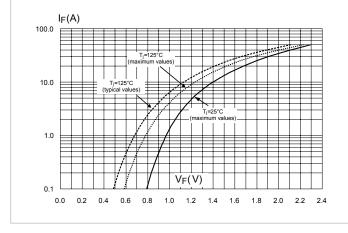


Figure 8. Junction capacitance versus reverse voltage applied (typical values)

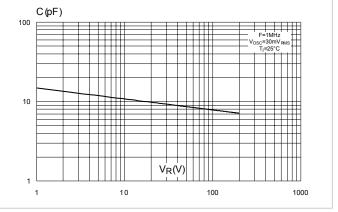


Figure 9. Relative variations of dynamic parameters versus junction temperature

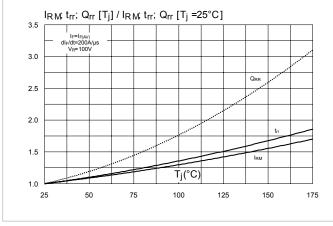
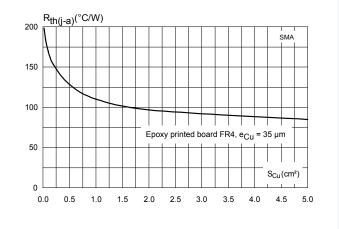


Figure 10. Thermal resistance junction to ambient versus copper surface under each lead (typical values)

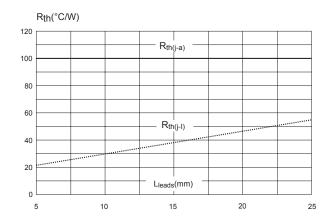


DS2448 - Rev 6 page 5/12

page 6/12



Figure 11. Thermal resistance versus lead length (DO-41)





# 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

#### 2.1 DO-41 package information

Epoxy meets UL 94, V0

Figure 12. DO-41 package outline

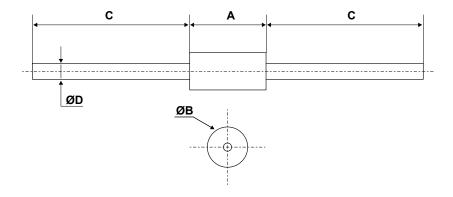


Table 5. DO-41 package mechanical data

	Dimensions						
Ref.	Ref. Millimeters			Inch	only)		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	4.07	-	5.20	0.160	-	0.205	
В	2.04	-	2.71	0.080	-	0.107	
С	25.40	-		1.000	-		
D	0.71	-	0.86	0.028	-	0.0034	

DS2448 - Rev 6 page 7/12



### 2.2 SMA package information

- Epoxy meets UL94, V0
- Cooling method : by conduction (C)

Figure 13. SMA package outline

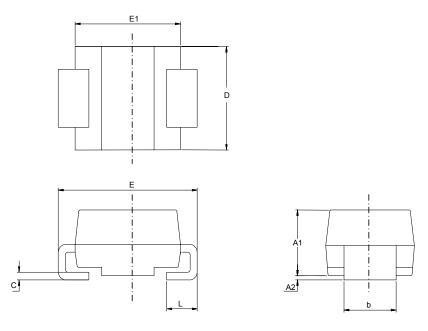


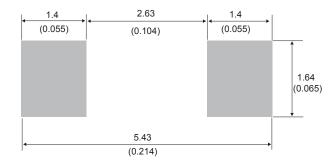
Table 6. SMA package mechanical data

	Dimensions						
Ref.	Millimeters		Inches (for re	ference only)			
	Min.	Max.	Min.	Max.			
A1	1.90	2.45	0.074	0.097			
A2	0.05	0.20	0.001	0.008			
b	1.25	1.65	0.049	0.065			
С	0.15	0.40	0.005	0.016			
D	2.25	2.90	0.088	0.115			
E	4.80	5.35	0.188	0.211			
E1	3.95	4.60	0.155	0.182			
L	0.75	1.50	0.029	0.060			

DS2448 - Rev 6 page 8/12



Figure 14. SMA recommended footprint in mm (inches)



DS2448 - Rev 6 page 9/12



# 3 Ordering information

**Table 7. Ordering information** 

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STTH102A	U12	SMA	0.068 g	5000	Tape and reel
STTH102	STTH102	DO-41	0.34 g	2000	Ammopack
STTH102RL	STTH102	DO-41	0.34 g	5000	Tape and reel



# **Revision history**

Table 8. Document revision history

Date	Revision	Changes
Jul-2003	2A	Last update.
Aug-2004	3	SMA package dimensions update. Reference A1 max. changed from 2.70mm (0.106inc.) to 2.03mm (0.080). SMA and DO-41 datasheets merged.
27-Jun-2005	4	Corrected error in title.
21-Nov-2006	5	Reformatted to current standards. Added Table 4. Dynamic electrical characteristics. Updated dimensions table for DO-41 plastic package. Added cathode bands to package illustrations.
05-Dec-2018	6	Add electrical schematics of single diode and ECOPACK®2 compliant.



#### **IMPORTANT NOTICE - PLEASE READ CAREFULLY**

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2018 STMicroelectronics - All rights reserved

DS2448 - Rev 6 page 12/12