

**PROTECTION PRODUCTS**
**Absolute Maximum Rating**

Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20\mu s$ )	$P_{pk}$	300	Watts
Peak Pulse Current ( $t_p = 8/20\mu s$ )	$I_{pp}$	17	A
Lead Soldering Temperature	$T_L$	260 (10 sec.)	°C
Operating Temperature	$T_J$	-55 to +125	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

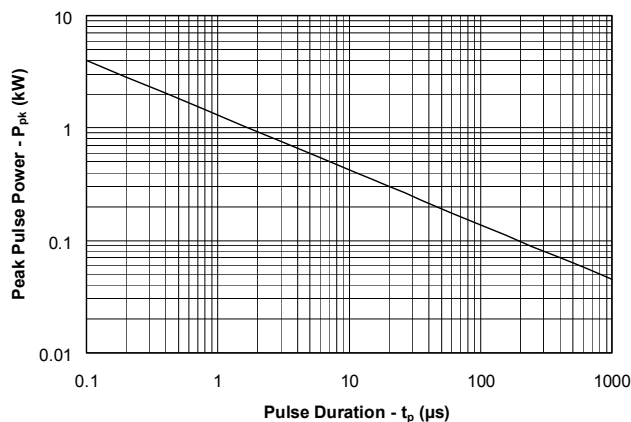
**Electrical Characteristics**

<b>SMDA05-6</b>						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	$V_{RWM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	$I_t = 1mA$	6			V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5V, T=25^\circ C$			20	$\mu A$
Clamping Voltage	$V_C$	$I_{pp} = 1A, t_p = 8/20\mu s$			9.8	V
Clamping Voltage	$V_C$	$I_{pp} = 5A, t_p = 8/20\mu s$			11	V
Junction Capacitance	$C_J$	Between I/O pins and Ground $V_R = 0V, f = 1MHz$			400	pF

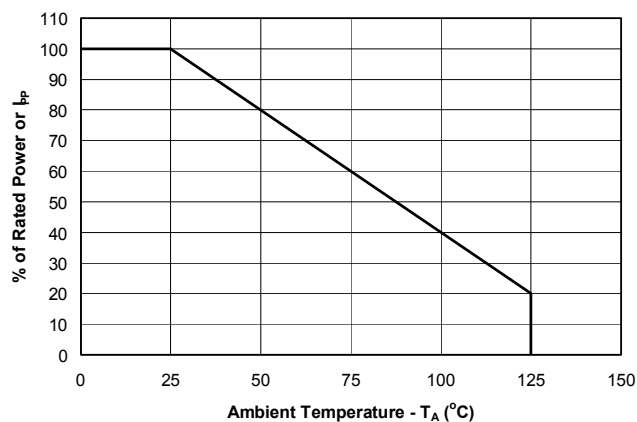
# PROTECTION PRODUCTS

## Typical Characteristics

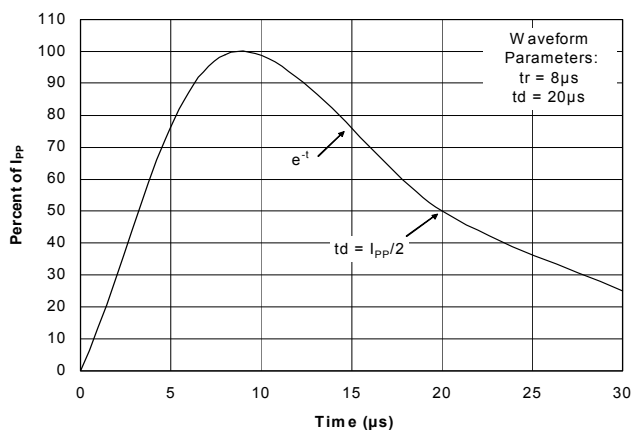
### Non-Repetitive Peak Pulse Power vs. Pulse Time



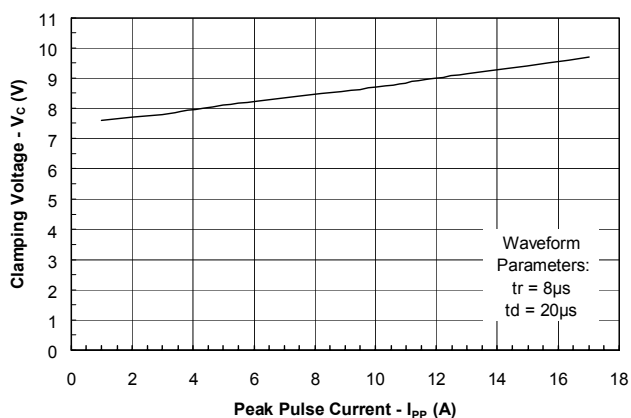
### Power Derating Curve



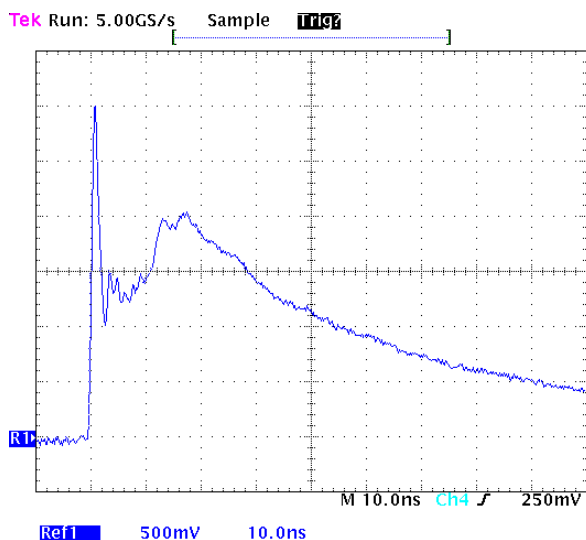
### Pulse Waveform



### Clamping Voltage vs. Peak Pulse Current



### ESD Pulse Waveform (IEC 61000-4-2)



### IEC 61000-4-2 Discharge Parameters

Level	First Peak Current (A)	Peak Current at 30 ns (A)	Peak Current at 60 ns (A)	Test Voltage (Contact Discharge) (kV)	Test Voltage (Air Discharge) (kV)
1	7.5	4	8	2	2
2	15	8	4	4	4
3	22.5	12	6	6	8
4	30	16	8	8	15

## PROTECTION PRODUCTS

### Applications Information

#### Device Connection for Protection of Six Data Lines

The SMDA05-6 is designed to protect up to 6 data or I/O lines operating at 5 volts. They are unidirectional devices and may be used on lines where the signal polarities are above ground (i.e. 0 to 5V).

The device is connected as follows:

- Pins 1, 2, 3, 4, 5 and 8 are connected to the lines that are to be protected. Pins 6 and 7 are connected to ground. The ground connections should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

#### Circuit Board Layout Recommendations for Suppression of ESD.

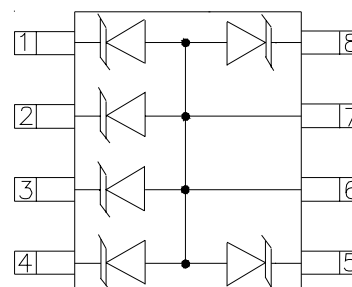
Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

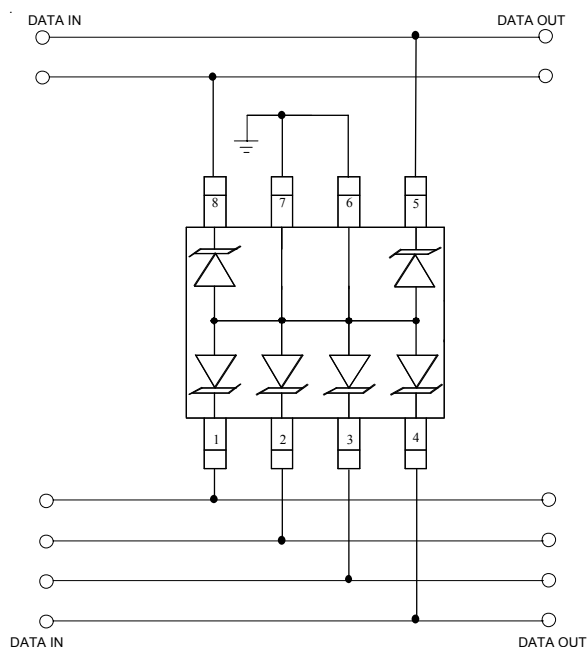
#### Matte Tin Lead Finish

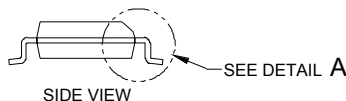
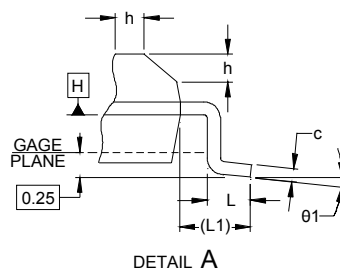
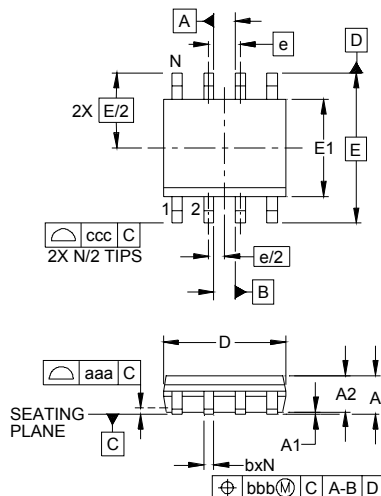
Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

**Circuit Diagram**



**Connection Diagram**

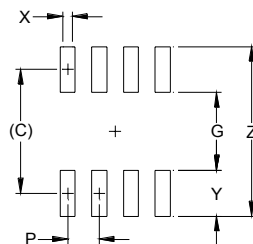


**PROTECTION PRODUCTS**
**Outline Drawing - S0-8**


DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.053	-	.069	1.35	-	1.75
A1	.004	-	.010	0.10	-	0.25
A2	.049	-	.065	1.25	-	1.65
b	.012	-	.020	0.31	-	0.51
c	.007	-	.010	0.17	-	0.25
D	.189	.193	.197	4.80	4.90	5.00
E	.150	.154	.157	3.80	3.90	4.00
E	.236 BSC			6.00 BSC		
e	.050 BSC			1.27 BSC		
h	.010	-	.020	0.25	-	0.50
L	.016	.028	.041	0.40	0.72	1.04
L1	(.041)			(1.04)		
N	8			8		
theta1	0°	-	8°	0°	-	8°
aaa	.004			0.10		
bbb	.010			0.25		
ccc	.008			0.20		

**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DATUMS **-A-** AND **-B-** TO BE DETERMINED AT DATUM PLANE **-H-**
3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
4. REFERENCE JEDEC STD MS-012, VARIATION AA.

**Land Pattern - S0-8**


DIM	DIMENSIONS	
	INCHES	MILLIMETERS
C	(.205)	(5.20)
G	.118	3.00
P	.050	1.27
X	.024	0.60
Y	.087	2.20
Z	.291	7.40

**NOTES:**

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.
2. REFERENCE IPC-SM-782A, RLP NO. 300A.

**PROTECTION PRODUCTS****Ordering Information**

Part Number	Lead Finish	Qty per Reel	Reel Size
SMDA05-6.TB	SnPb	500	7 inch
SMDA05-6.TBT	Pb Free	500	7 inch
SMDA05-6	SnPb	95/Tube	N/A
SMDA05-6.T	Pb Free	95/Tube	N/A

Note: Lead-free devices are RoHS/WEEE Compliant

**Contact Information**

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