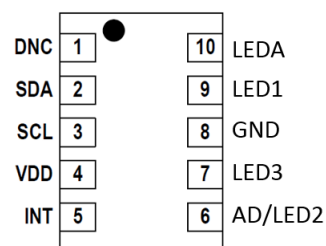
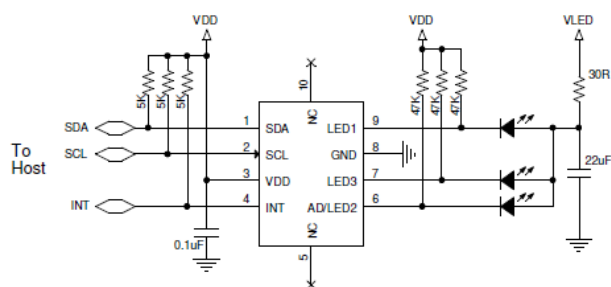


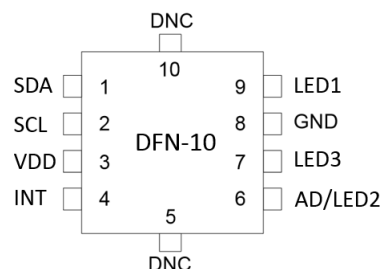
Si1153 LGA Module Basic Application



Pin Description 2.85x4.9 mm LGA Module



Si1153 DFN Basic Application



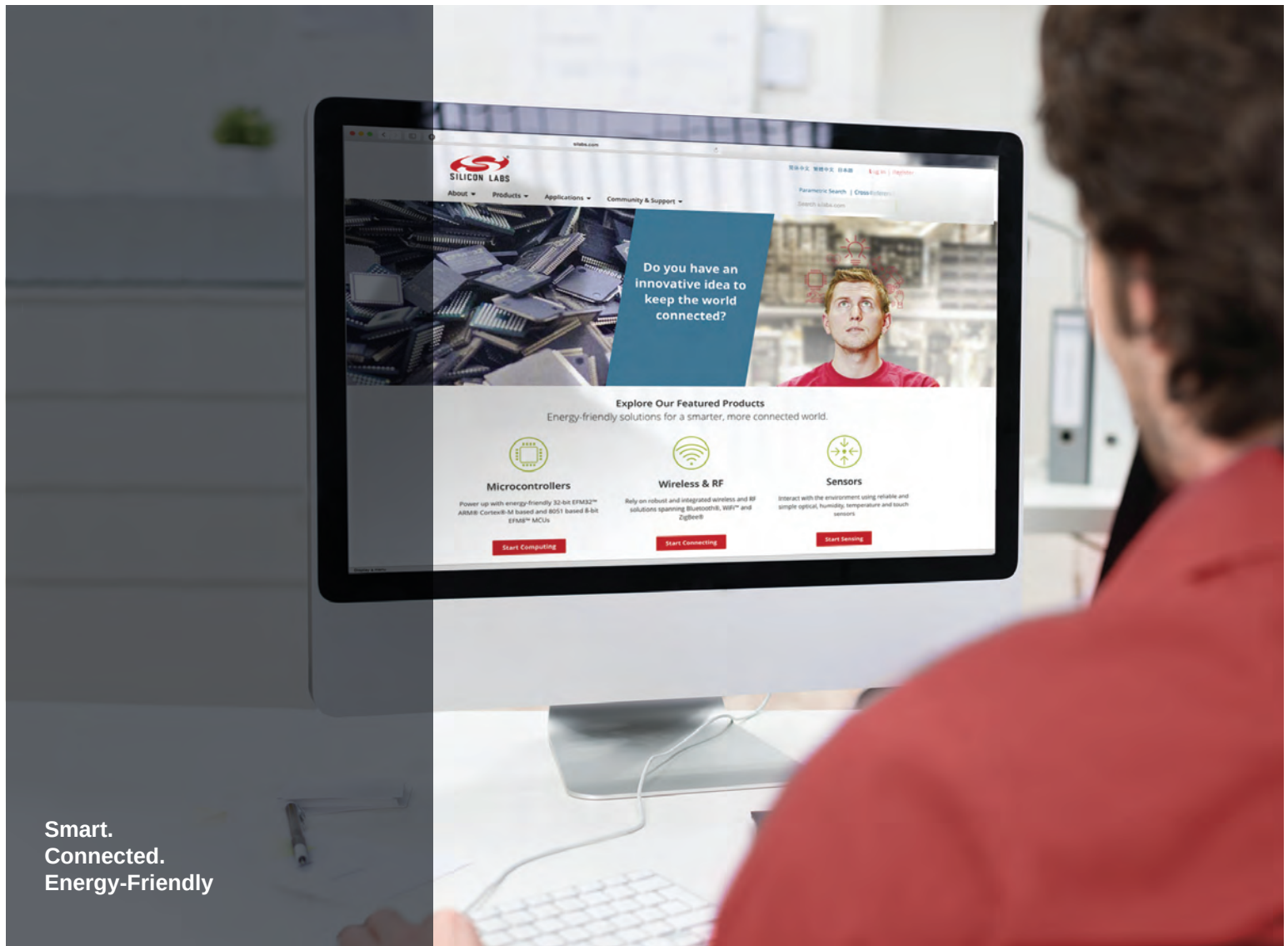
Pin Description 2x2 mm DFN

Table 1.1. Recommended Operating Conditions

Parameter	Symbol	Condition	Min	Typ	Max	Unit
V _{DD} Supply Voltage	V _{DD}		1.62	—	3.6	V
V _{DD} OFF Supply Voltage	V _{DD_OFF}	OFF mode	−0.3		1.0	V
V _{DD} Supply Ripple Voltage		V _{DD} = 3.3 V 1 kHz – 10 MHz	—	—	50	mVpp
Operating Temperature	T		−40	25	85	°C
SCL, SDA, Input High Logic Voltage	I ² C _{VIH}		V _{DD} × 0.7	—	V _{DD}	V
SCL, SDA Input Low Logic Voltage	I ² C _{VIL}		0	—	V _{DD} × 0.3	V
Start-Up Time		V _{DD} above 1.62 V	25	—	—	ms

Table 1.2. Ordering Guide

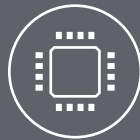
Family	DFN OPNs	Package	ALS	940 nm Filter	Proximity (# of LED Drivers)	# of LEDs Included
Si1153	Si1153-AA00-GMR	2 x 2 mm DFN	Y		3	0
Si1153	Si1153-AA09-GMR	2 x 2 mm DFN		Y	3	0
Si1153	Si1153-AA9x-GMR	2.85 x 4.9 mm LGA Module		Y	3	1



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