Electrical Specifications (-40°C ≤ T_A ≤ +85°C unless otherwise specified)

INPUT CHARACTERISTICS	PVD1352N	PVD1354N	Units
Minimum Control Current (see figures 1 and 2)			DC
For 500mA Continuous Load Current	2		mA@25°C
For 550mA Continuous Load Current	5		mA@40°C
For 350mA Continuous Load Current	5		mA@85°C
Maximum Control Current for Off-State Resistance at 25°C	10		µA(DC)
Control Current Range (Caution: current limit input LED. See figure 6)	2.0 to 25		mA(DC)
Maximum Reverse Voltage	6.0		V(DC)

OUTPUT CHARACTERISTICS	PVD1352N	PVD1354N	Units
Operating Voltage Range	0 to + 100		$V_{(PEAK)}$
Maxiumum Load Current 40°C I LED 5mA	550		$mA_{(DC)}$
Response Time @25°C (see figures 7 and 8)			
Max. $T_{(on)}$ @ 12mA Control, 50 mA Load, 100 VDC	150		µs
Max. $T_{(off)}$ @ 12mA Control, 50 mA Load, 100 VDC	125		µs
Max. On-state Resistance 25°C (Pulsed) (fig. 4) 200 mA Load, 5mA Contro	1.5		Ω
Min. Off-state Resistance 25°C @ 80 VDC (see figure 5)	10^8	10^{10}	Ω
Max. Thermal Offset Voltage @ 5.0mA Control	0.2		µvolts
Min. Off-State dv/dt	1000		V/µs
Typical Output Capacitance	20		pF @ 50VDC

GENERAL CHARACTERISTICS		(PVD1352N and PVD1354N)	Units
Dielectric Strength: Input-Output		4000	V_{RMS}
Insulation Resistance: Input-Output @ 90V_{DC}		10^{12} @ 25°C - 50% RH	Ω
Maximum Capacitance: Input-Output		1.0	pF
Max. Pin Soldering Temperature (1.6mm below seating plane, 10 seconds max.)		+260	°C
Ambient Temperature Range:	Operating	-40 to +85	
	Storage	-40 to +100	

International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.

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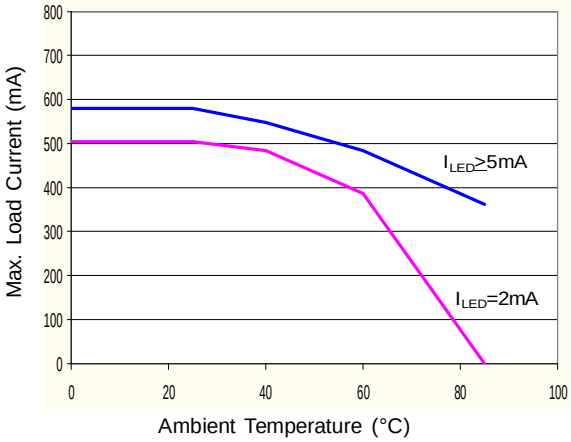

Figure 1. Current Derating Curves

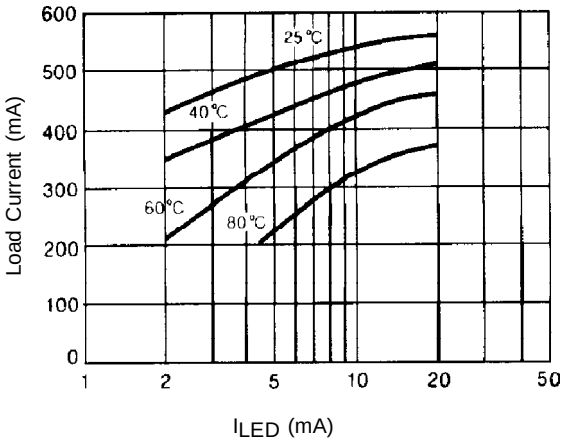

Figure 2. Typical Control Current Requirements

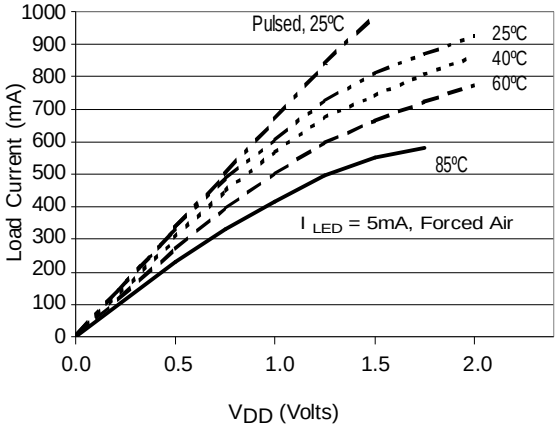

Figure 3.Typical On Characteristics

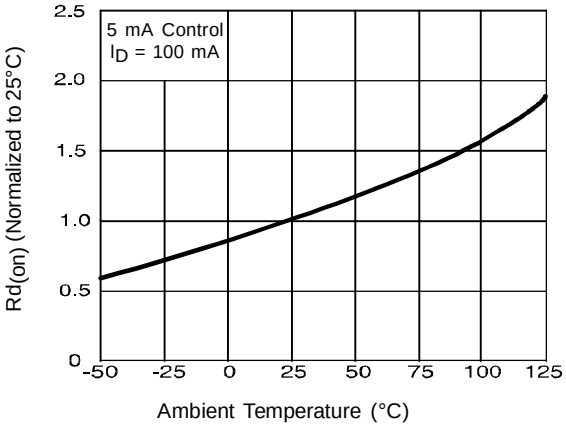

Figure 4. Typical Normalized On-Resistance

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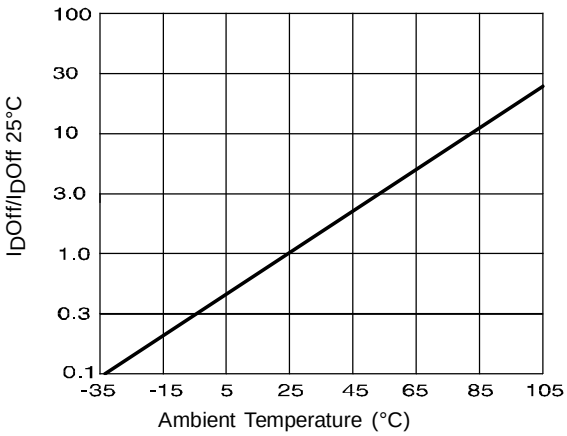

Figure 5. Typical Normalized Off-State Leakage

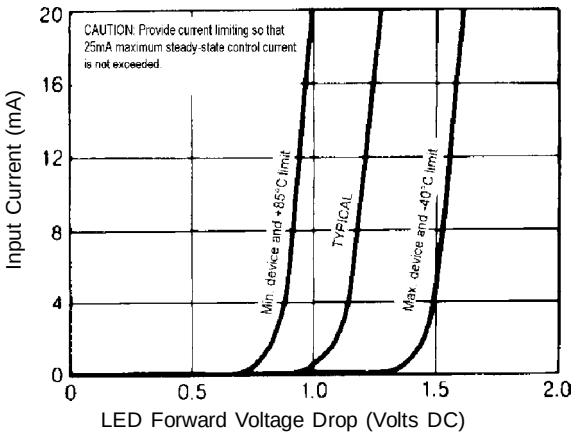

Figure 6. Input Characteristics (Current Controlled)

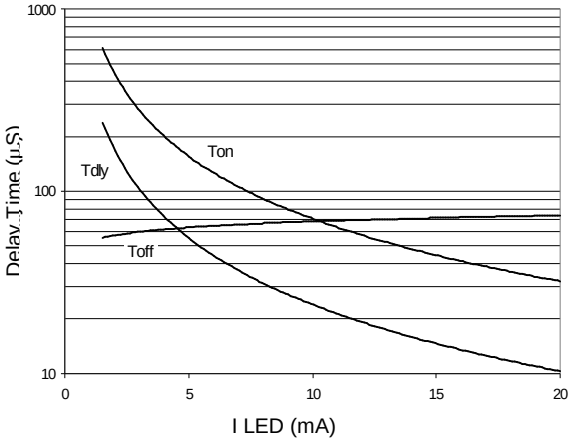

Figure 7.Typical Delay Times

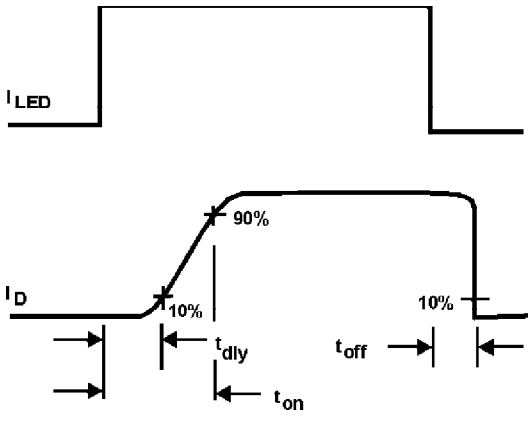

Figure 8. Delay Time Definitions

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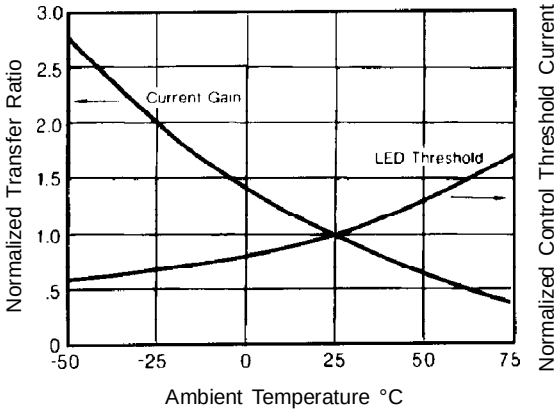


Figure 9. Typical Control Threshold and Transfer Ratio

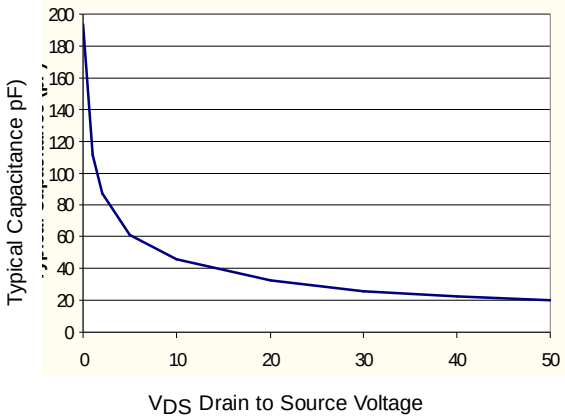
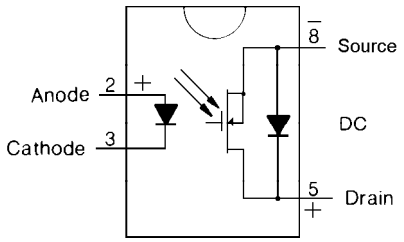


Figure 10. Typical Output Capacitance

Wiring Diagram



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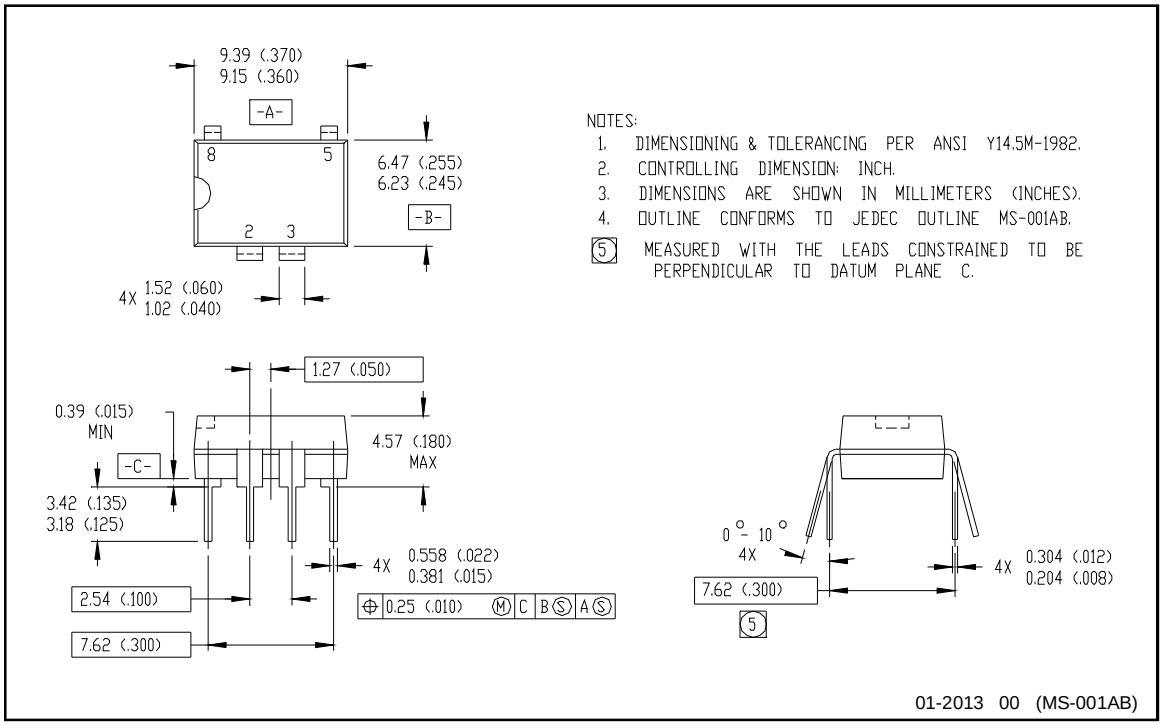
Case Outlines



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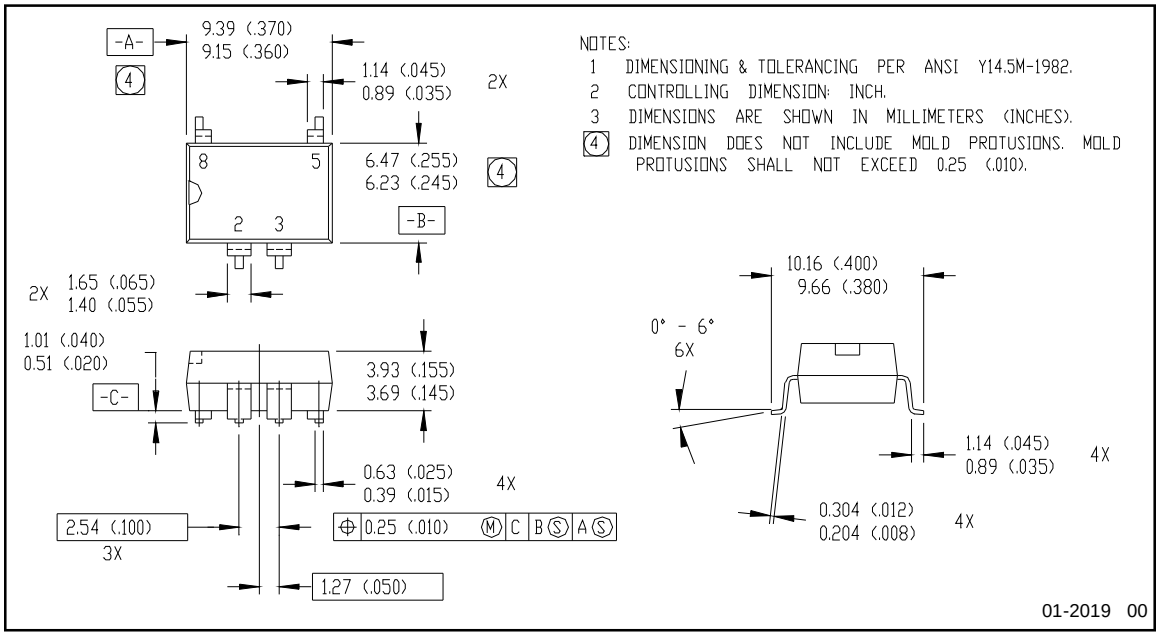

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