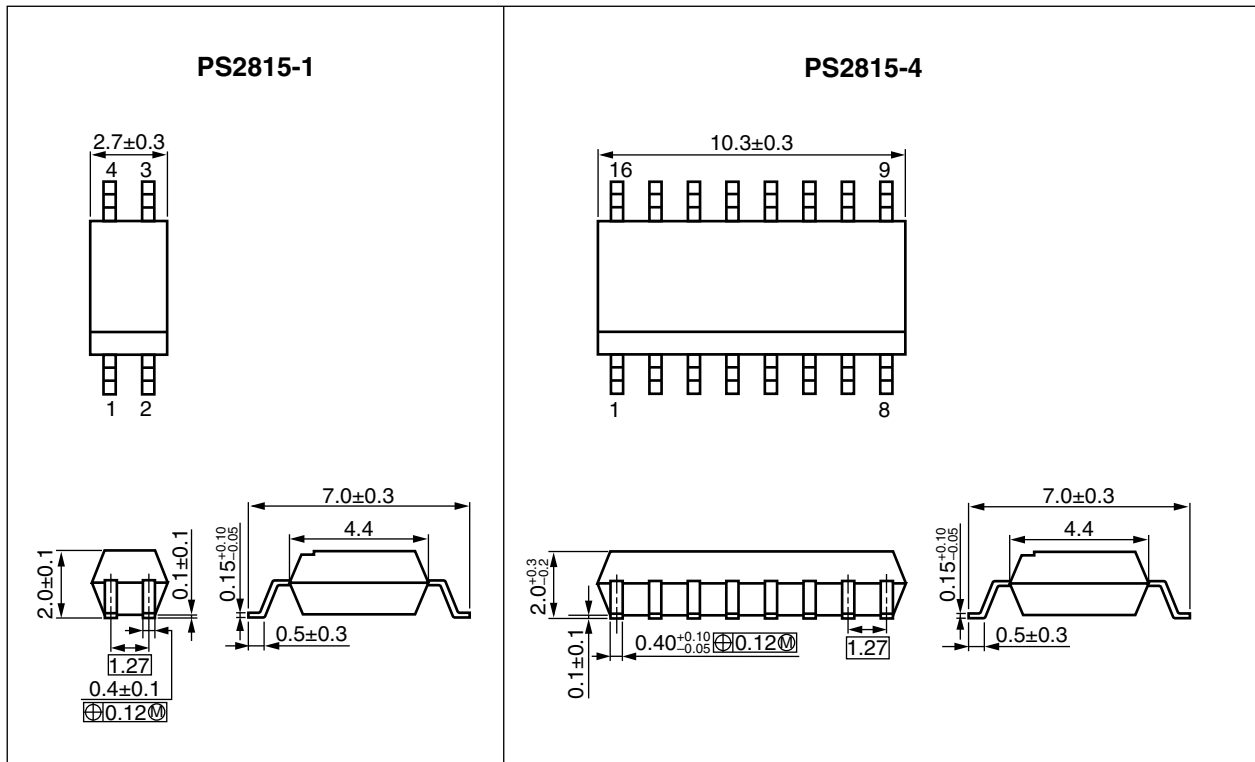
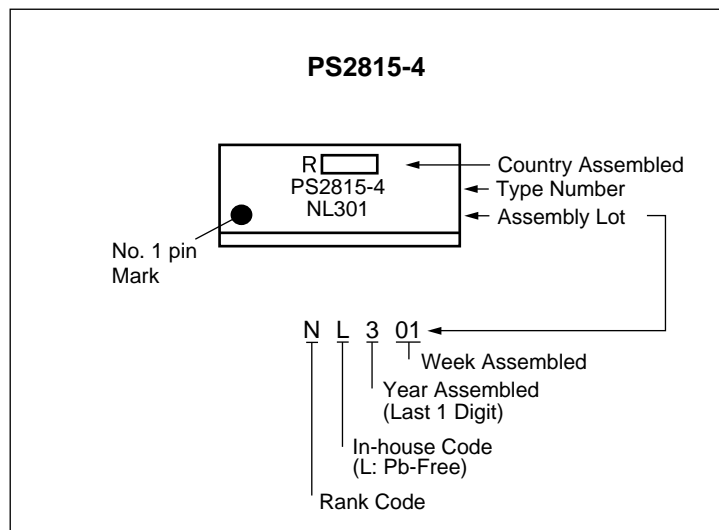


PS2815-1, PS2815-4
PACKAGE DIMENSIONS (UNIT: mm)

PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	4.5 mm
Creepage Distance	4.5 mm
Isolation Thickness	0.1 mm

PS2815-1, PS2815-4**MARKING EXAMPLE****PS2815-1**

Made in Taiwan	
Made in Japan	

PS2815-4

PS2815-1, PS2815-4

ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standards Approval	Application Part Number ^{*1}
PS2815-1-F3	PS2815-1-F3-A	Pb-Free	Embossed Tape 3 500 pcs/reel	Standard products (UL, CSA approved)	PS2815-1
PS2815-1-V-F3	PS2815-1-V-F3-A		Embossed Tape 3 500 pcs/reel	DIN EN 60747-5-5 (VDE 0884-5) Approved (Option)	
PS2815-4-F3	PS2815-4-F3-A		Embossed Tape 2 500 pcs/reel	Standard products (UL, CSA approved)	PS2815-4
PS2815-4-V-F3	PS2815-4-V-F3-A		Embossed Tape 2 500 pcs/reel	DIN EN 60747-5-5 (VDE 0884-5) Approved (Option)	

Note: ^{*1}. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS2815-1	PS2815-4	
Diode	Forward Current (DC)	I_F	± 50		mA/ch
	Power Dissipation Derating	$\Delta P_D/^\circ\text{C}$	0.6	0.7	mW/ $^\circ\text{C}$
	Power Dissipation	P_D	60	70	mW/ch
	Peak Forward Current ^{*1}	I_{FP}	± 1.0		A/ch
Transistor	Collector to Emitter Voltage	V_{CEO}	40		V
	Emitter to Collector Voltage	V_{ECO}	5		V
	Collector Current	I_C	40		mA/ch
	Power Dissipation Derating	$\Delta P_D/^\circ\text{C}$	1.2		mW/ $^\circ\text{C}$
	Power Dissipation	P_C	120		mW/ch
Isolation Voltage ^{*2}		BV	2 500		Vr.m.s.
Operating Ambient Temperature		T_A	-55 to +100		$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to +150		$^\circ\text{C}$

Notes: ^{*1}. PW = 100 μs , Duty Cycle = 1%

^{*2}. AC voltage for 1 minute at $T_A = 25^\circ\text{C}$, RH = 60% between input and output.

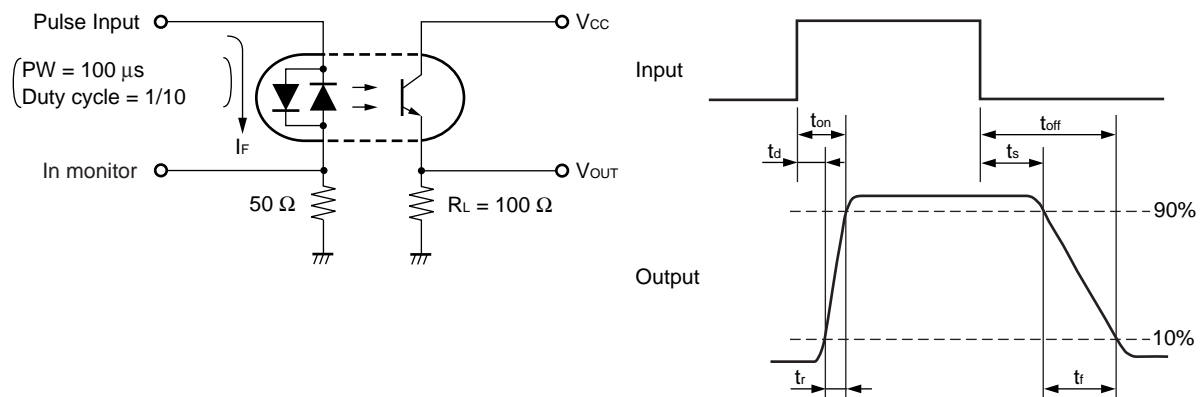
Pins 1-2 shorted together, 3-4 shorted together (PS2815-1).

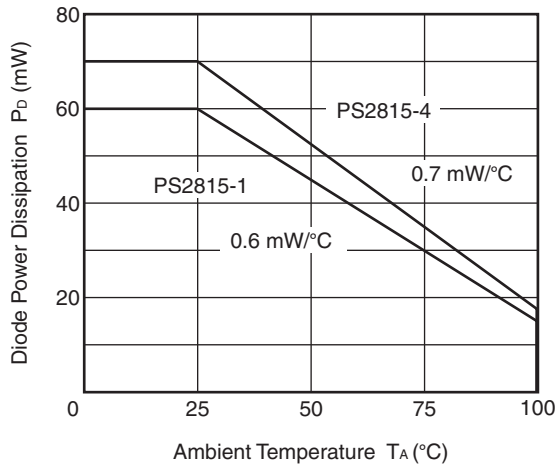
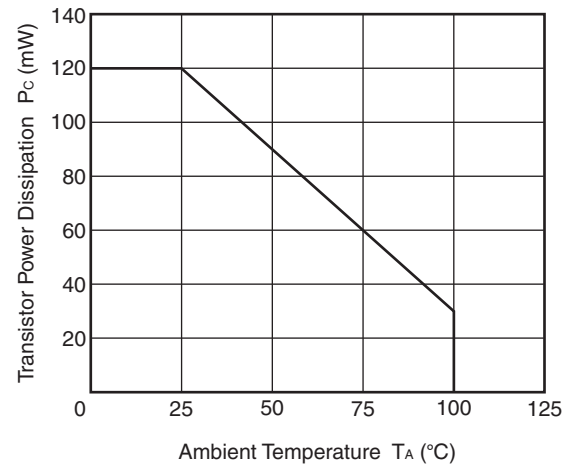
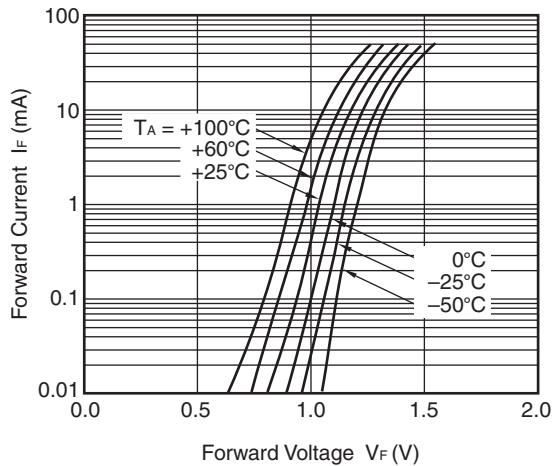
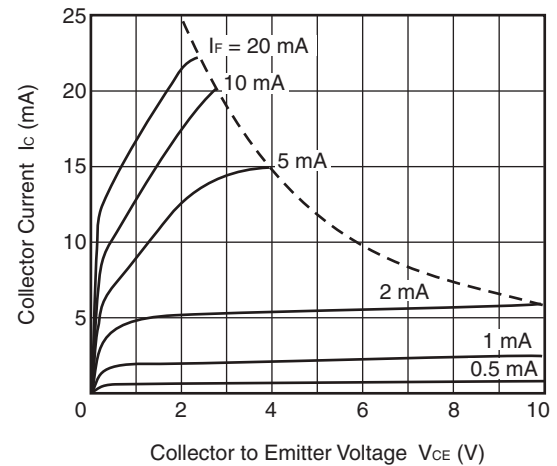
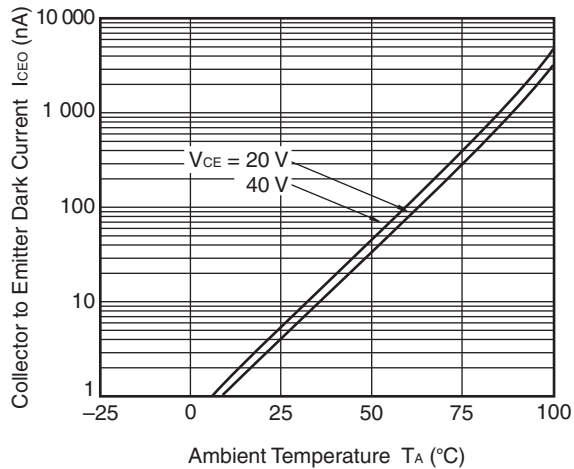
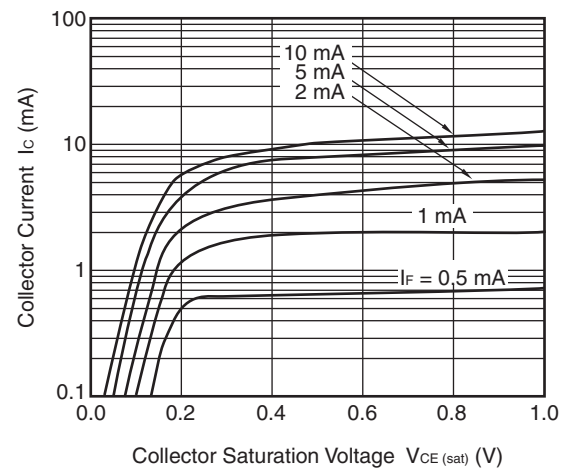
Pins 1-8 shorted together, 9-16 shorted together (PS2815-4).

PS2815-1, PS2815-4
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V _F	I _F = ±5 mA		1.15	1.4	V
	Terminal Capacitance	C _t	V = 0 V, f = 1 MHz		30		pF
Transistor	Collector to Emitter Dark Current	I _{CEO}	I _F = 0 mA, V _{CE} = 40 V			100	nA
Coupled	Current Transfer Ratio (I _C /I _F) ^{*1}	CTR	I _F = ±1 mA, V _{CE} = 5 V	100	200	400	%
	Collector Saturation Voltage	V _{CE(sat)}	I _F = ±1 mA, I _C = 0.2 mA			0.3	V
	Isolation Resistance	R _{I-O}	V _{I-O} = 1 kV _{DC}	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz		0.4		pF
	Rise Time ^{*1}	t _r	V _{CC} = 5 V, I _C = 2 mA, R _L = 100 Ω		4		μs
	Fall Time ^{*1}	t _f			5		
	Turn-on Time ^{*1}	t _{on}			7		
	Turn-off Time ^{*1}	t _{off}			5		

Notes: ^{*1}1. Test circuit for switching time

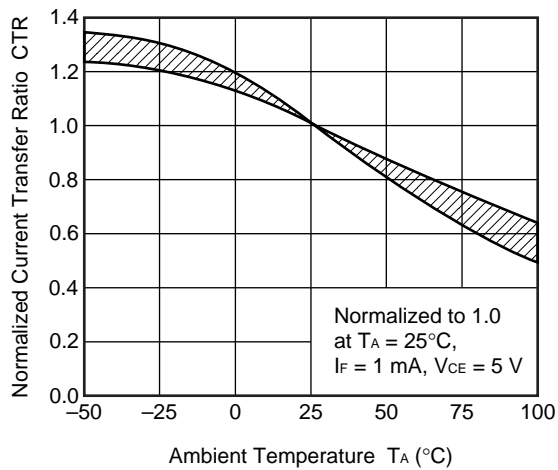


PS2815-1, PS2815-4
<R> TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE

TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE

FORWARD CURRENT vs. FORWARD VOLTAGE

COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE

COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE


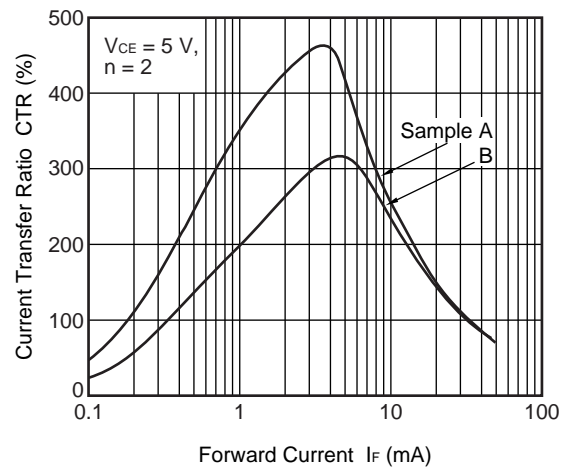
Remark The graphs indicate nominal characteristics.

PS2815-1, PS2815-4

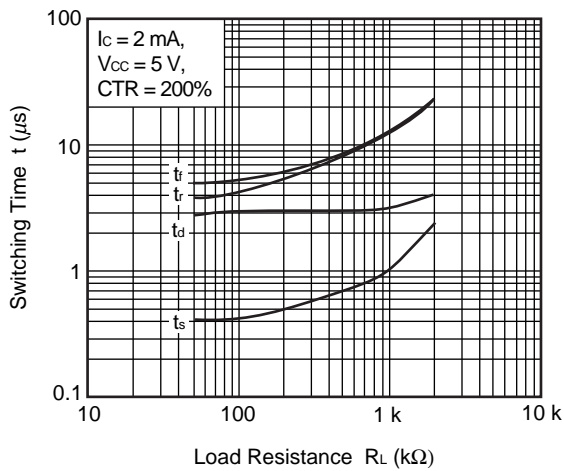
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



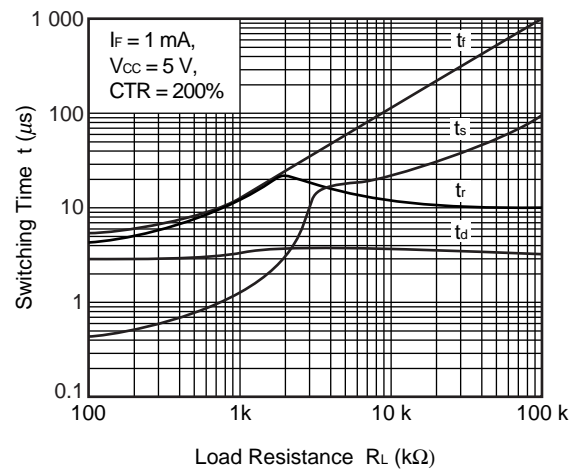
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



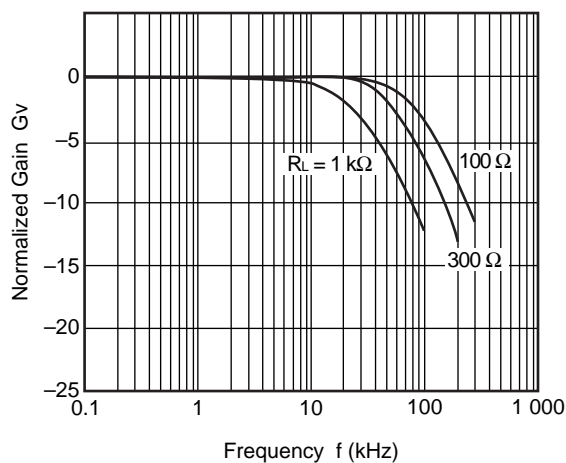
SWITCHING TIME vs. LOAD RESISTANCE



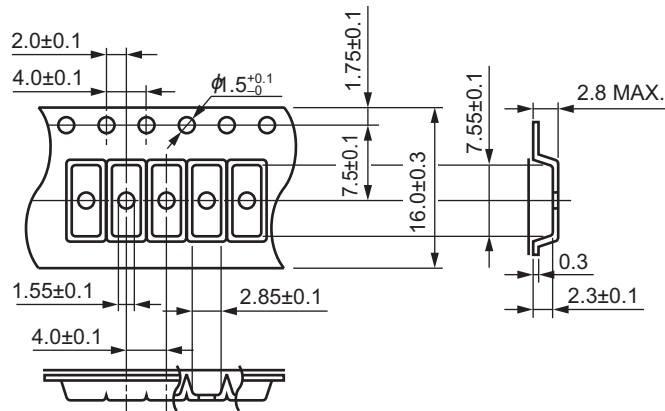
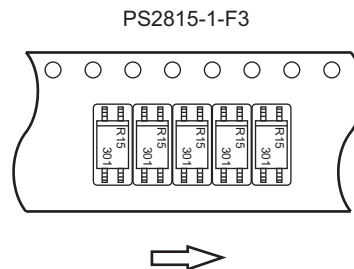
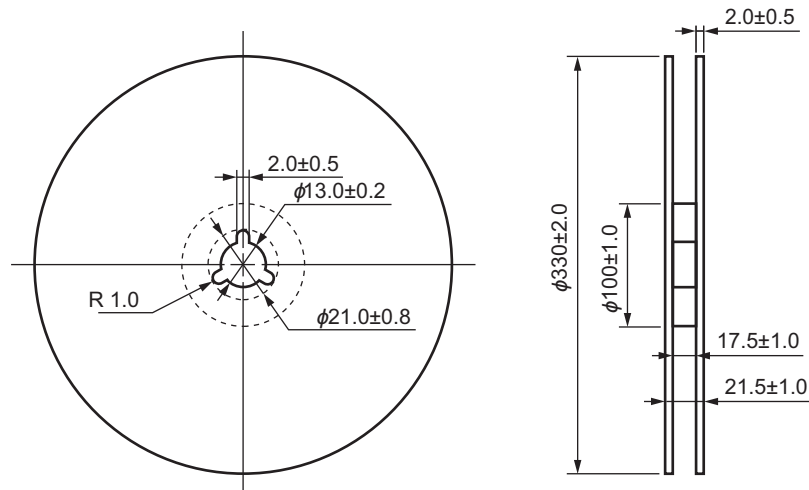
SWITCHING TIME vs. LOAD RESISTANCE



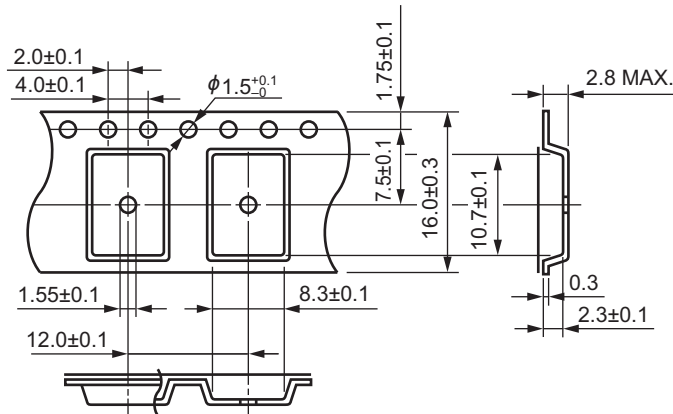
FREQUENCY RESPONSE



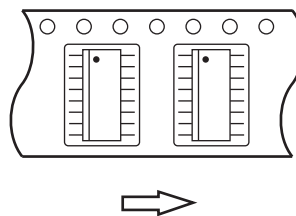
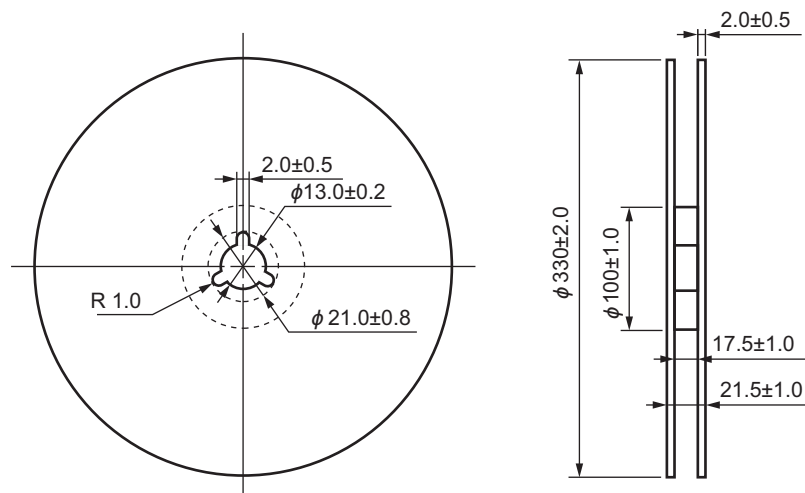
Remark The graphs indicate nominal characteristics.

PS2815-1, PS2815-4**TAPING SPECIFICATIONS (UNIT: mm)****Outline and Dimensions (Tape)****Tape Direction****Outline and Dimensions (Reel)**

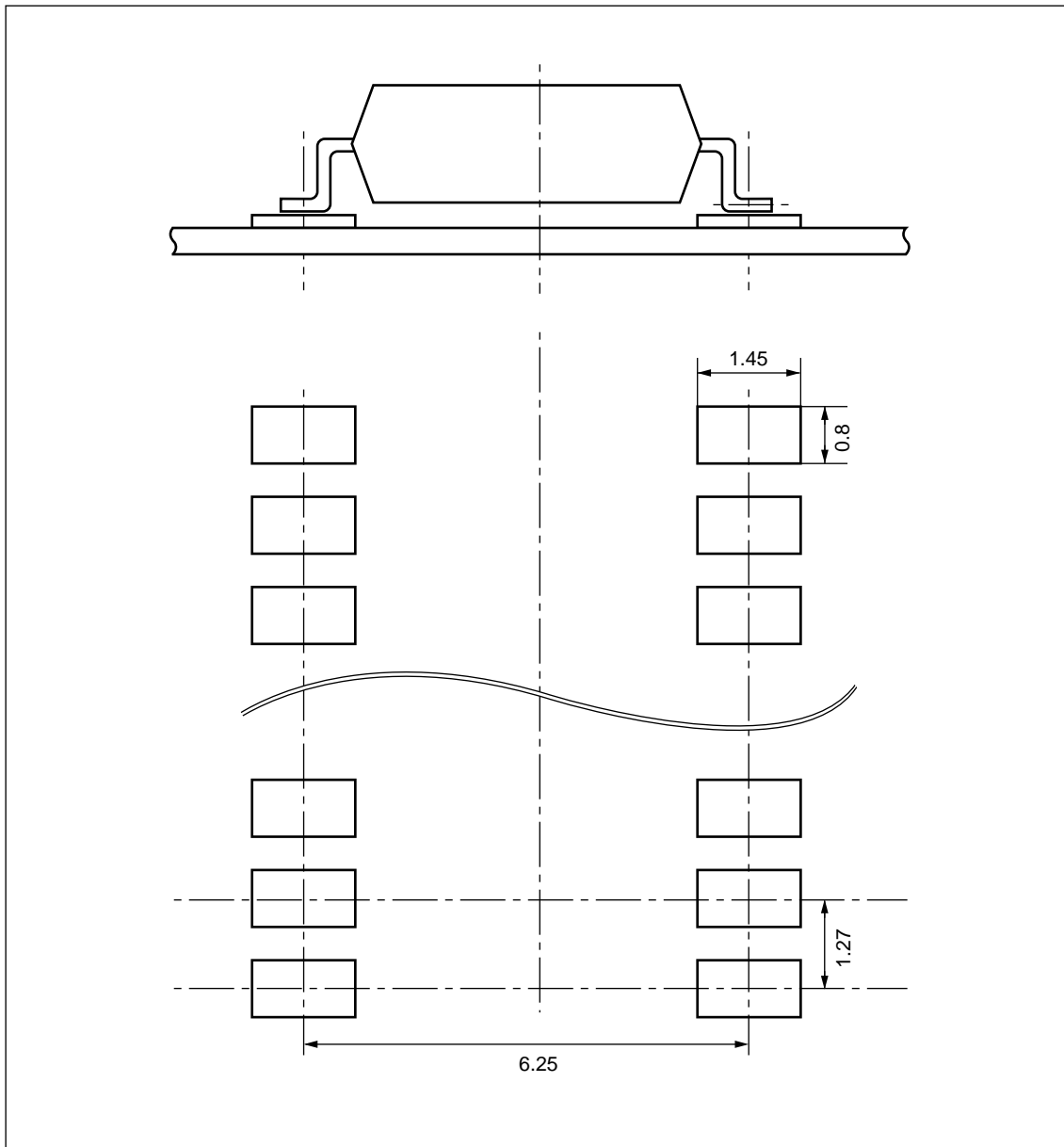
Packing: 3 500 pcs/reel

PS2815-1, PS2815-4**Outline and Dimensions (Tape)****Tape Direction**

PS2815-4-F3

**Outline and Dimensions (Reel)**

Packing: 2 500 pcs/reel

PS2815-1, PS2815-4**RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)**

Remark All dimensions in this figure must be evaluated before use.

PS2815-1, PS2815-4

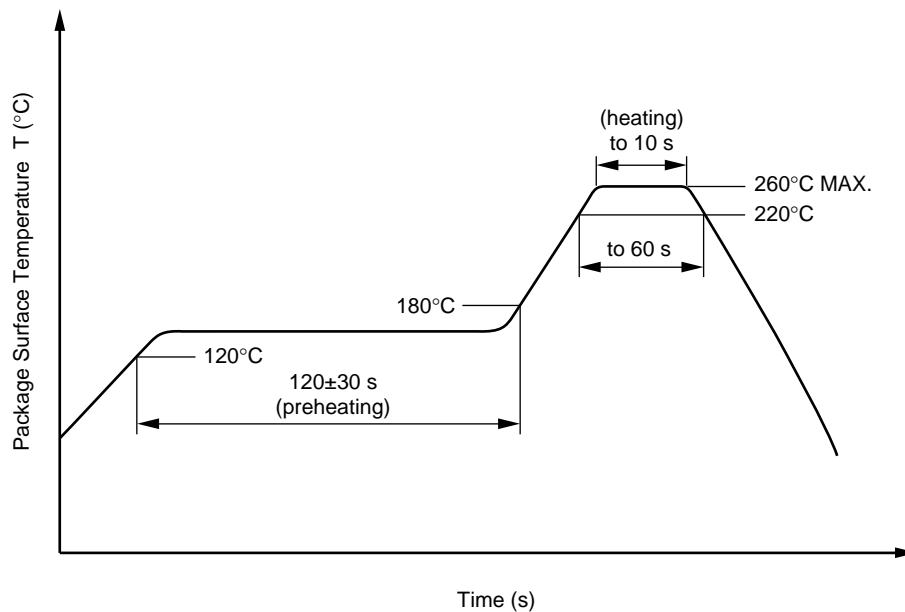
NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

- | | |
|---|--|
| • Peak reflow temperature | 260°C or below (package surface temperature) |
| • Time of peak reflow temperature | 10 seconds or less |
| • Time of temperature higher than 220°C | 60 seconds or less |
| • Time to preheat temperature from 120 to 180°C | 120±30 s |
| • Number of reflows | Three |
| • Flux | Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

- | | |
|-------------------------|--|
| • Temperature | 260°C or below (molten solder temperature) |
| • Time | 10 seconds or less |
| • Preheating conditions | 120°C or below (package surface temperature) |
| • Number of times | One (Allowed to be dipped in solder including plastic mold portion.) |
| • Flux | Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

(3) Soldering by Soldering Iron

- | | |
|--|--|
| • Peak Temperature (lead part temperature) | 350°C or below |
| • Time (each pins) | 3 seconds or less |
| • Flux | Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.) |

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(4) Cautions

- | | |
|----------|--|
| • Fluxes | Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent. |
|----------|--|

PS2815-1, PS2815-4

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.

PS2815-1, PS2815-4
SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength			
maximum operating isolation voltage	U_{IORM}	705	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random test)	U_{pr}	1 128	V_{peak}
$U_{pr} = 1.6 \times U_{IORM}$, $P_d < 5 \text{ pC}$			
Test voltage (partial discharge test, procedure b for all devices)	U_{pr}	1 322	V_{peak}
$U_{pr} = 1.875 \times U_{IORM}$, $P_d < 5 \text{ pC}$			
Highest permissible overvoltage	U_{TR}	6 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE 0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE 0110 Part 1)		III a	
Storage temperature range	T_{stg}	–55 to +150	°C
Operating temperature range	T_A	–55 to +100	°C
Isolation resistance, minimum value			
$V_{IO} = 500 \text{ V dc at } T_A = 25^\circ\text{C}$	Ris MIN.	10^{12}	Ω
$V_{IO} = 500 \text{ V dc at } T_A \text{ MAX. at least } 100^\circ\text{C}$	Ris MIN.	10^{11}	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)			
Package temperature	T_{si}	150	°C
Current (input current I_F , $\Psi_i = 0$)	I_{si}	300	mA
Power (output or total power dissipation)	Ψ_i	500	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc at } T_A = T_{si}$	Ris MIN.	10^9	Ω

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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