

# **Pin Description**

Pin #	Pin Name	I/O	Description		
2	PD	I	Power Down. When PD = '1', all paths are switched off and supply current is very low. $PD = '0'$ for normal operation.		
3 4	A0+ A0-	I/O	Signal I/O, Channel 0, Port A		
9	SEL	I	Operation mode Select (when SEL=0: A→B, when SEL=1: A→C)		
1, 6, 10	$V_{\mathrm{DD}}$	Pwr	3.3V ± 10% Positive Supply Voltage		
7	A1+	1/0	Signal I/O, Channel 1, Port A		
8	A1-	I/O			
5, 11, 20, Center Pad	GND	Pwr	Power ground		
13	C1+	1/0	Signal I/O, Channel 1, Port C		
12	C1-	I/O			
15	C0+	1.0	Signal I/O, Channel 0, Port C		
14	C0-	I/O			
17	B1+	110			
16	B1-	I/O	Signal I/O, Channel 1, Port B		
19	B0+	710			
18	В0-	I/O	Signal I/O, Channel 0, Port B		



## **Maximum Ratings**

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to Ground Potential	0.5V to +4.6V
Channel DC Input Voltage	0.5V to 1.5V
DC Output Current	120mA
Power Dissipation	0.5W
SEL/PD DC Input Voltage	0.5V to 4.6V

**Note:** Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### **Electrical Characteristics**

**Recommended Operating Conditions** 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
$V_{\mathrm{DD}}$	3.3V Power Supply		3.0	3.3	3.6	V
I <sub>DD</sub>	Total current from V <sub>DD</sub> 3.3V supply	$SEL = OV \text{ or } V_{DD}$		0.2	1	mA
I <sub>DD_PD</sub>	Power down current	PD = 1		20	40	μΑ
V <sub>I/O</sub> -DIF	Differential Voltage (differential pins)				1.6	V <sub>ppd</sub>
V <sub>I/O-CM</sub>	Common Mode Voltage (differential pins)		0		0.8	V
T <sub>CASE</sub>	Case temperature range for operation within spec.		-40		85	Celsius

# DC Electrical Characteristics for Switching over Operating Range

Parameters	Description	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(1)</sup>	Max	Units
VIH - SEL, PD	Input HIGH Voltage, SEL, PD Input		2		3.6	
V <sub>IL</sub> - <sub>SEL</sub> , PD	Input LOW Voltage, SEL, PD Input		0		0.8	V
V <sub>IK</sub>	Clamp Diode Voltage	$V_{DD} = Max., I_{IN} = -18mA$		-0.7	-1.2	
IIH	Input HIGH Current, SEL, PD	$V_{DD} = Max., V_{IN} = V_{DD}$			±5	
IIL	Input LOW Current, SEL, PD	$V_{DD} = Max., V_{IN} = 0V$			±5	μΑ
IIH	Input HIGH Current, A <sub>X</sub> , B <sub>X</sub> , C <sub>X</sub>	$V_{DD} = Max., V_{IN} = 1.5V$	-10		+10	
IIL	Input LOW Current, A <sub>X</sub> , B <sub>X</sub> , C <sub>X</sub>	$V_{DD} = Max., V_{IN} = 0V$	-10		+10	μΑ
I <sub>OZH</sub>	HighZ HIGH Current, B <sub>X</sub> , C <sub>X</sub>	$V_{\mathrm{DD}} = \mathrm{Max.}, V_{\mathrm{IN}} = 1.5 \mathrm{V}$	-10		+10	μΑ
IOZL	HighZ LOW Current, BX, CX	$V_{\rm DD}$ = Max., $V_{\rm IN}$ = 0V	-10		+10	μA

Note:

<sup>1.</sup> Typical values are at  $V_{DD} = 3.3V$ ,  $T_A = 25$ °C ambient and maximum loading.



# **Switching Characteristics**

Param- eters	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
tpZH, tpZL	Line Enable Time - SEL to $A_N$ , $B_{N, C_N}$			25	30	
tpHZ, tPLZ	Line Disable Time - SEL to A <sub>N</sub> , B <sub>N</sub> , C <sub>N</sub>			5	25	ns
t <sub>PLH</sub>	Propagation Delay, LOW to HIGH		17		36	ps
t <sub>PHL</sub>	Propagation Delay, HIGH to LOW		21		39	ps
t <sub>b-b</sub>	Bit-to-bit skew within the same differential pair			5	10	ps
t <sub>ch-ch</sub>	Channel-to-channel skew				20	ps

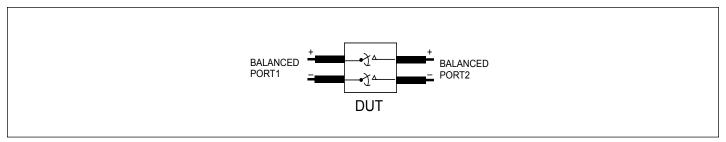
# **Dynamic Electrical Characteristics**

Parameter	Description	<b>Test Conditions</b>	Min.	Typ. <sup>1</sup>	Max.	Units	
DDIL <sup>2, 3</sup>	Differential Insertion Loss $(V_{IN} = -10 dBm, DC = 0V)$	f = 100MHz		-0.4			
		f = 100MHz-1.25GHz		-0.6			
		f = 1.25GHz-2.5GHz		-1.0		dB	
		f = 2.5GHz-4GHz		-1.7			
		f = 5GHz		-2.1			
		f = 100MHz		-59			
DDIL <sub>OFF</sub> <sup>2, 3</sup>	Differential Off Isolation	f = 100MHz-1.25GHz		-37		dB	
DDILOFF		f = 1.25GHz-2.5GHz		-27		ив	
		f = 2.5GHz-4GHz		-21			
	Differential Return Loss	f = 100MHz		-27		dB	
DDRL <sup>2</sup>		f = 100MHz-1.25GHz		-23.3			
DDKL		f = 1.25GHz-2.5GHz		-23.3			
		f = 2.5GHz-4GHz		-13.5			
	Near End Crosstalk	f = 100MHz		-57			
DDNEXT <sup>2, 3</sup>		f = 100MHz-1.25GHz		-38		dB	
DUNEAL		f = 1.25GHz-2.5GHz		-33			
		f = 2.5GHz-4GHz		-32			
BW	-3dB Bandwidth			8.1		GHz	

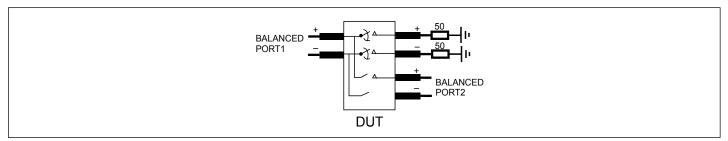
#### Notes:

- 1. Guaranteed by design. Typical values are at  $V_{DD} = 3.3 \text{V}$ ,  $T_A = 25^{\circ}\text{C}$  ambient and maximum loading.
- 2. S parameters are measured with our evaluation board made with Rogers (R04350) material. Trace width is 30 mil, length 540 mil, trace impedance is 50 Ohm (+/-5%) and total insertion loss of the trace is 0.5dB@4GHz.
- 3. Measurement done with fixture deembedding.

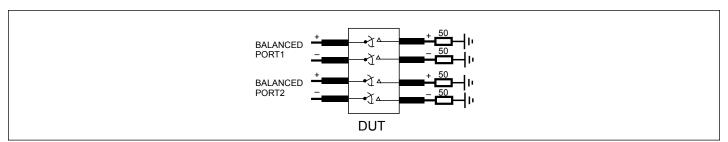




Diff. Insertion Loss and Return Test Circuit

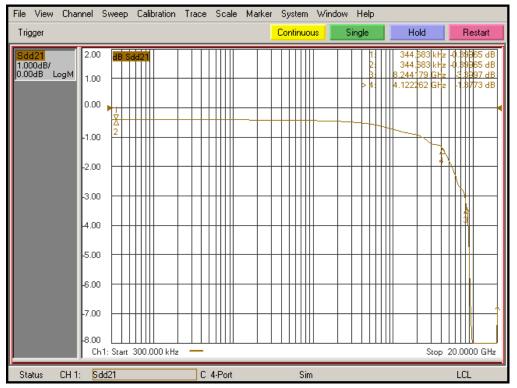


**Diff. Off Isolation Test Circuit** 



Diff. Near End Xtalk Test Circuit



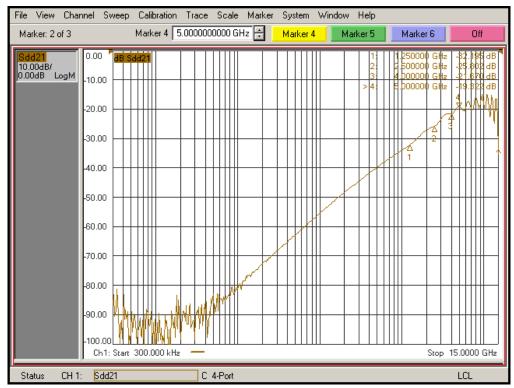


**Differential Insertion Loss** 

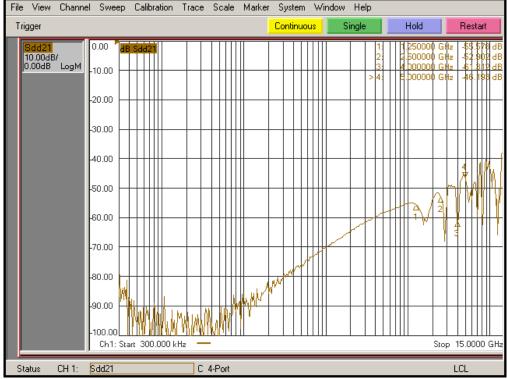


**Differential Return Loss** 



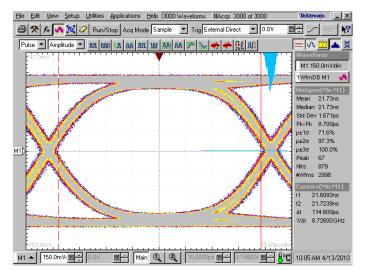


**Differential Off Isolation** 

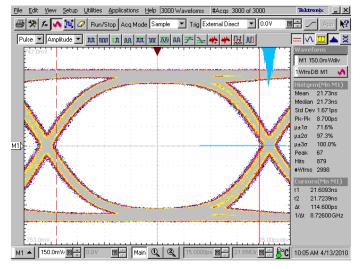


**Differential Crosstalk** 

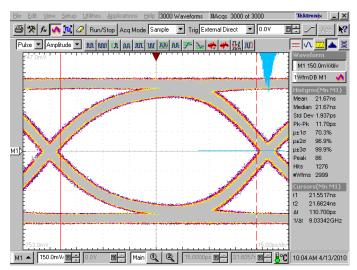




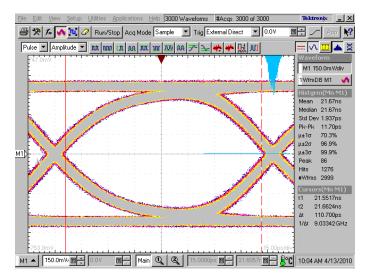
5.0 Gbps RX signal eye without PI3PCIE3212



8.0 Gbps RX signal eye without PI3PCIE3212



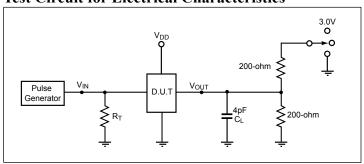
5.0 Gbps RX signal eye with PI3PCIE3212



8.0 Gbps RX signal eye with PI3PCIE3212



# Test Circuit for Electrical Characteristics<sup>(1-5)</sup>



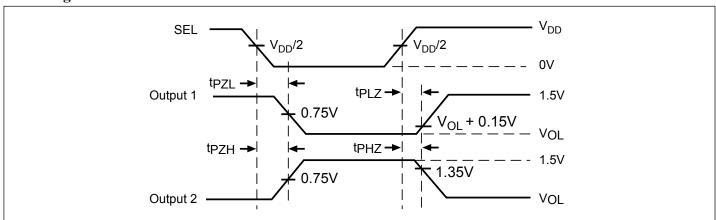
#### Notes:

- 1. C<sub>L</sub> = Load capacitance: includes jig and probe capacitance.
- 2.  $R_T$  = Termination resistance: should be equal to  $Z_{OUT}$  of the Pulse Generator
- 3. Output 1 is for an output with internal conditions such that the output is low except when disabled by the output control. output 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics:  $PRR \le MHz$ ,  $Z_O = 50\Omega$ ,  $t_R \le 2.5$ ns,  $t_F \le 2.5$ ns.
- 5. The outputs are measured one at a time with one transition per measurement.

### **Switch Positions**

Test	Switch		
t <sub>PLZ</sub> , t <sub>PZL</sub>	3.0V		
t <sub>PHZ</sub> , t <sub>PZH</sub>	GND		

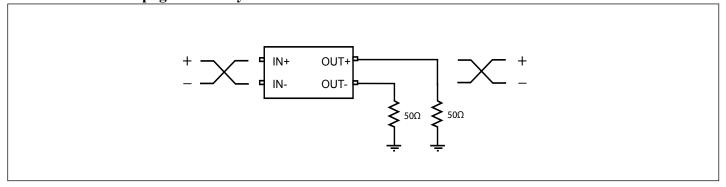
## **Switching Waveforms**



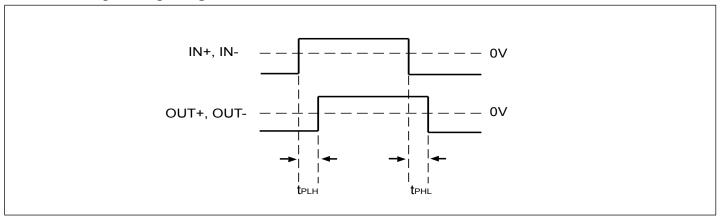
**Voltage Waveforms Enable and Disable Times** 



**Test Circuit for Propagation Delay** 



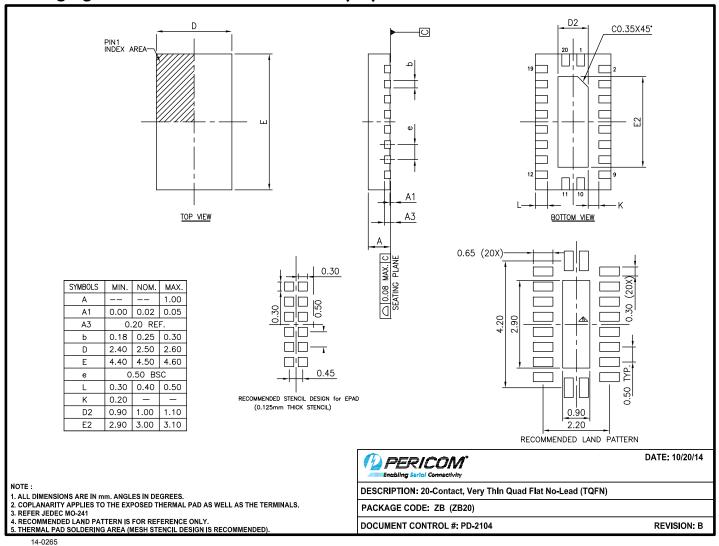
# **Differential Input/Output Signal Waveform**



14-0011



# Packaging Mechanical: 20-Contact TQFN (ZB)



• For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

# **Ordering Information**

Ordering Code	Package Code	Package Type
PI3PCIE3212ZBE	ZB	20-contact, Very Thin Quad Flat No-Lead (TQFN)

#### Notes:

- · Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel

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