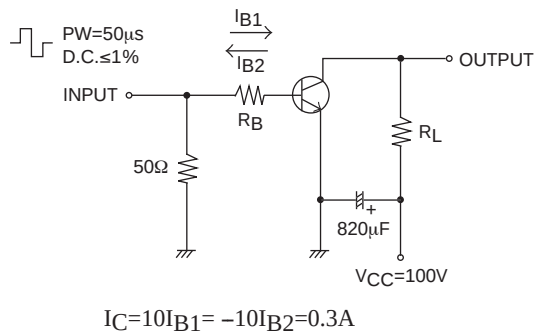
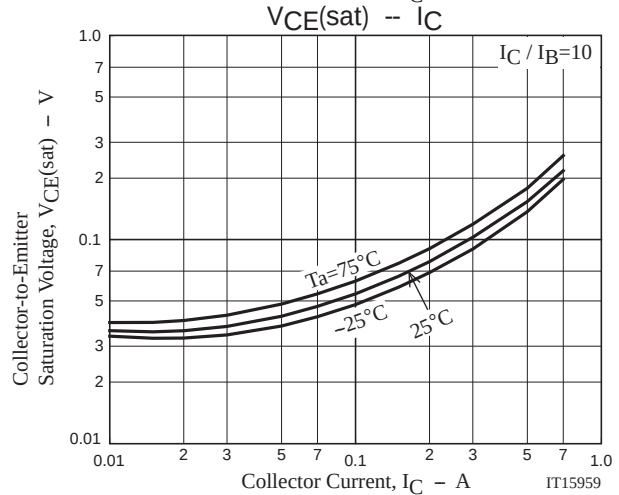
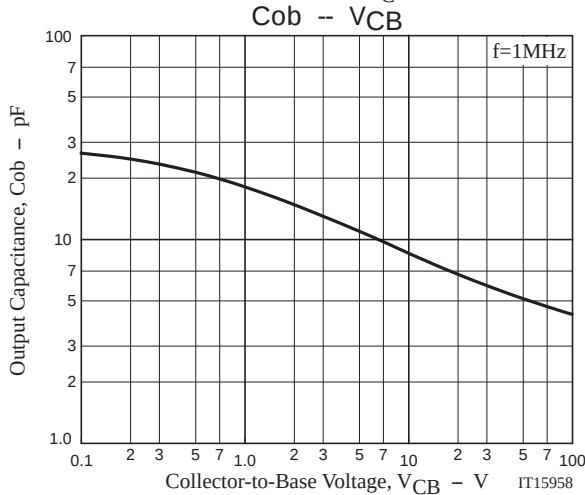
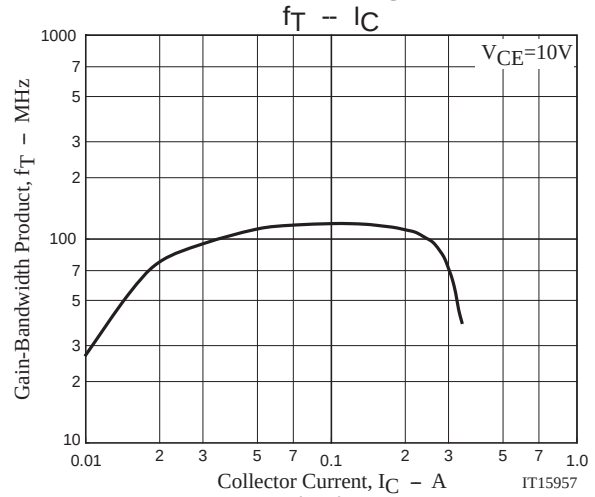
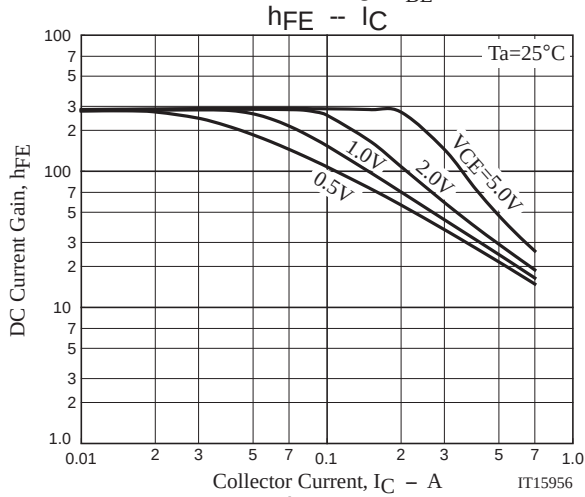
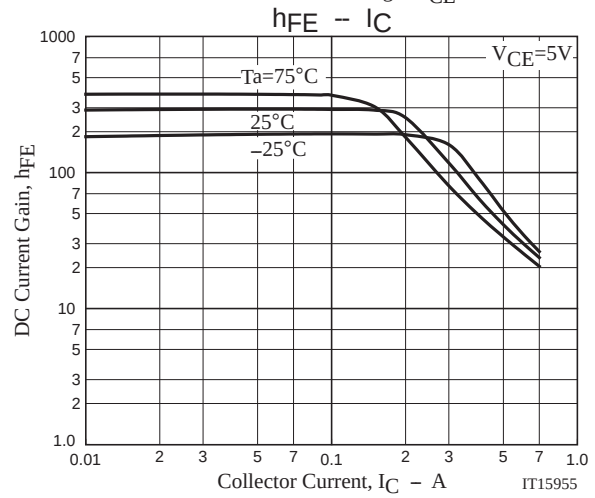
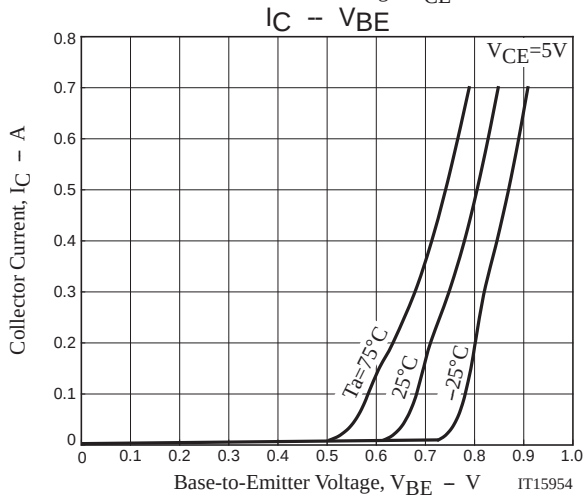
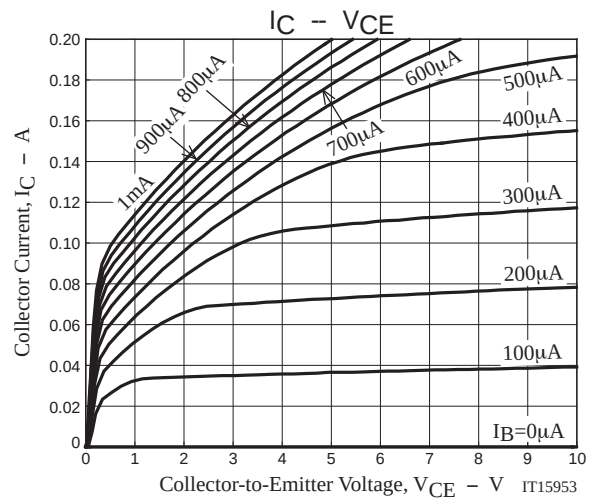
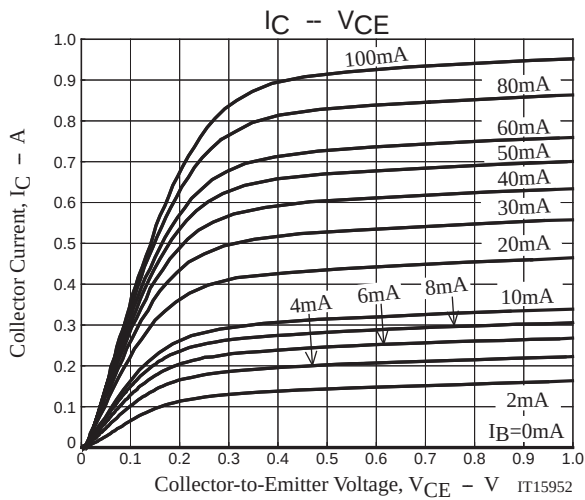
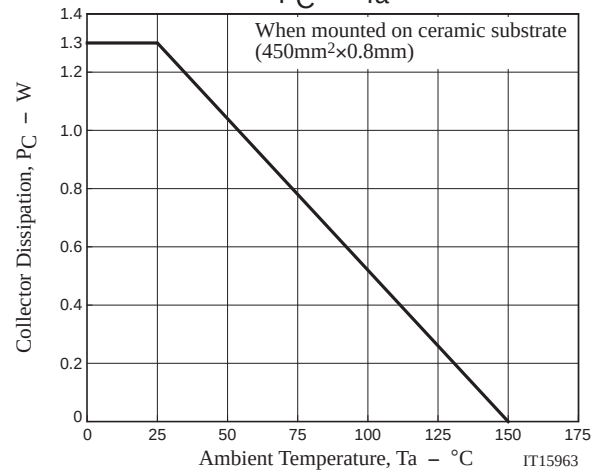
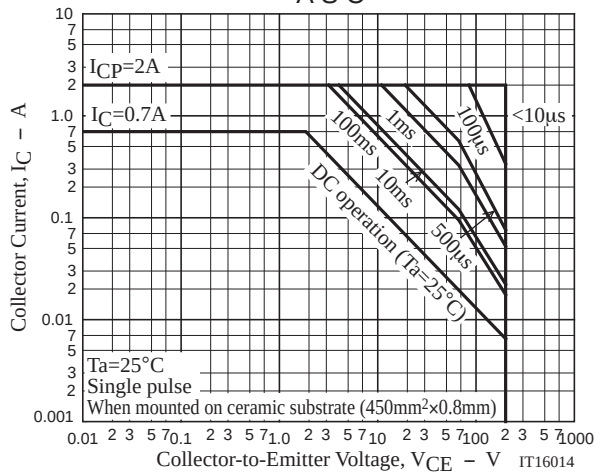
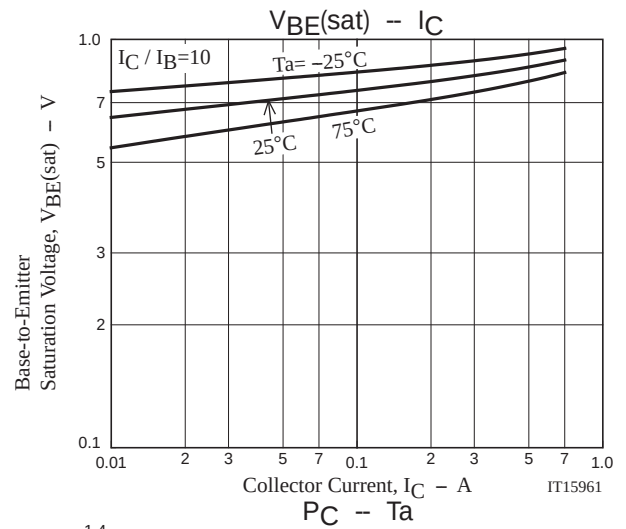
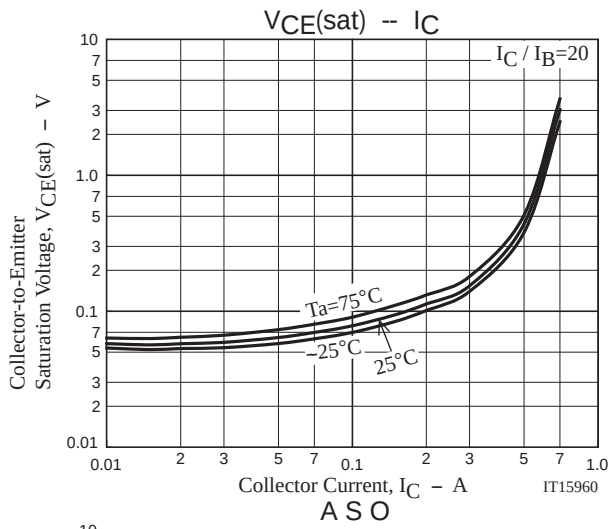


Electrical Characteristics at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CBO}	$V_{CB}=100\text{V}, I_E=0\text{A}$			1	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB}=4\text{V}, I_C=0\text{A}$			1	μA
DC Current Gain	h_{FE}	$V_{CE}=5\text{V}, I_C=100\text{mA}$	200		560	
Gain-Bandwidth Product	f_T	$V_{CE}=10\text{V}, I_C=100\text{mA}$		120		MHz
Output Capacitance	C_{ob}	$V_{CB}=10\text{V}, f=1\text{MHz}$		9		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=0.35\text{A}, I_B=35\text{mA}$		115	200	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=0.35\text{A}, I_B=35\text{mA}$		0.82	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu\text{A}, I_E=0\text{A}$	220			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1\text{mA}, R_{BE}=\infty$	200			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu\text{A}, I_C=0\text{A}$	8			V
Turn-On Time	t_{on}	See specified Test Circuit.		50		ns
Storage Time	t_{stg}			2		μs
Fall Time	t_f			70		ns

Switching Time Test Circuit





Bag Packing Specification

PCP1208-TD-H

1. Packing Format

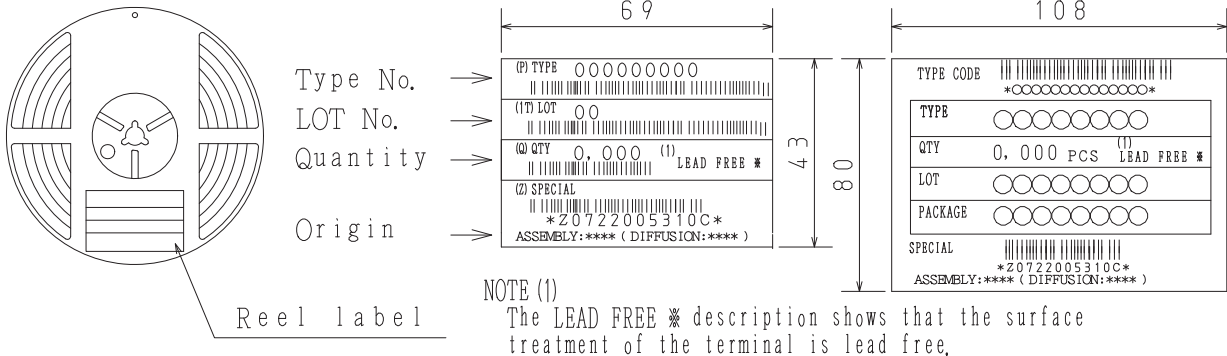
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
PCP	PCP	1,000	4,000	24,000	4 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit:mm)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.

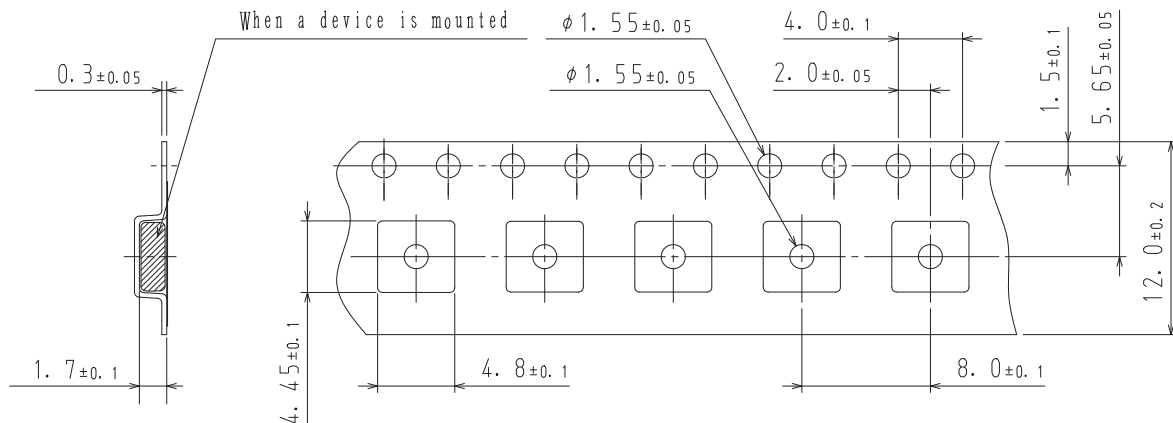
Packing method



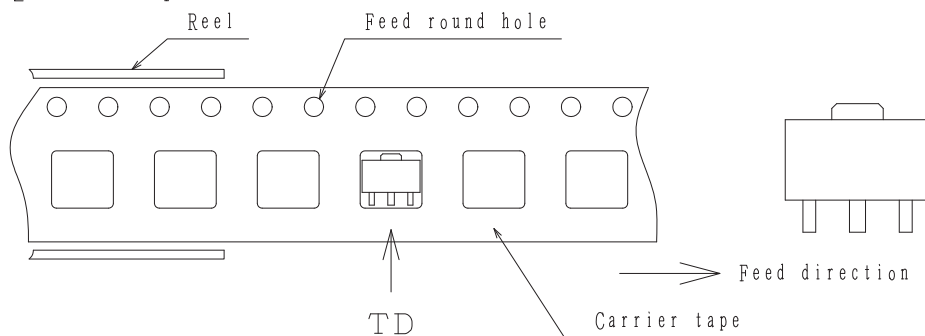
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

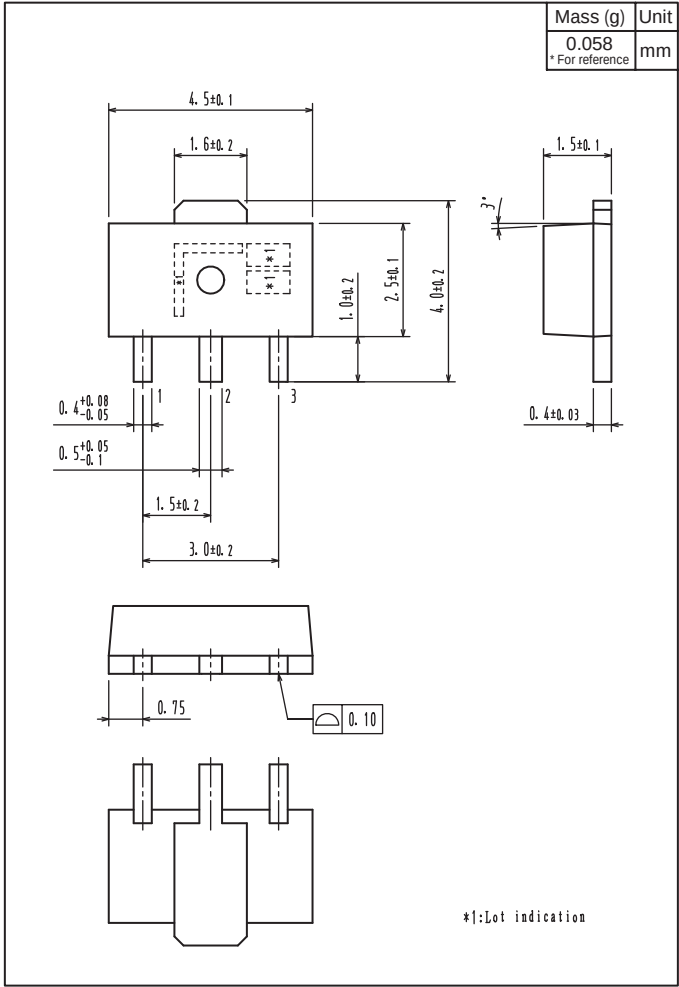


2-2. Device placement direction

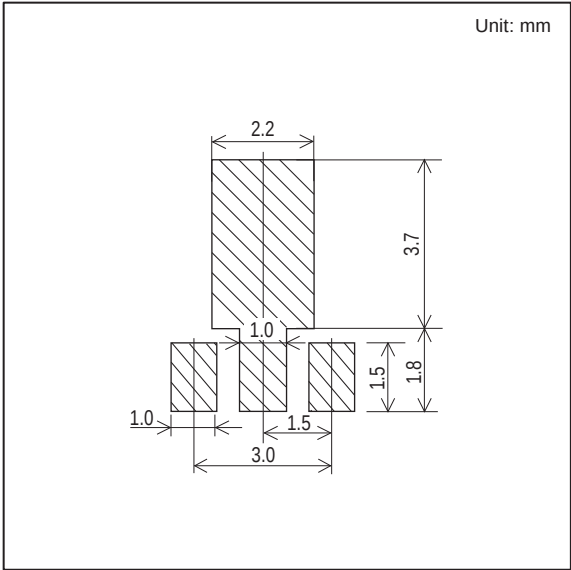


Those with pin 1 index on the feed hole side.....TD

Outline Drawing
PCP1208-TD-H



Land Pattern Example



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