

IBIS embedded IT eMMC 5.0 (PS8222) - v 1.12 (ZIP)

MTFC16GAKAENA-4M IT, MTFC16GAKAECN-4M IT, MTFC32GAKAENA-4M IT, MTFC32GAKAECN-4M IT, MTFC64GAKAEY-4M IT, MTFC8GACAENS-AIT, MTFC8GACAENS-K1 AIT, MTFC8GACAENS-5M AIT, MTFC8GACAEDQ-AIT, MTFC8GACAEDQ-K1 AIT, MTFC16GAKAECN-AIT, MTFC16GAKAECN-5M AIT, MTFC16GAKAEDQ-AIT,...

Automotive
Embedded
eMMC
Managed NAND

File Type: ZIP Updated: 02/21/2018

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF) Updated: 03/2018

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RoHS Certificates

China RoHS Certificate (PDF)

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

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Documentation & Support

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Technical Notes

SEARCH (53) TECHNICAL NOTES

Technical Notes

TN-00-08: Thermal Applications (PDF)

(TN-00-08) Describes some considerations in thermal applications for Micron memory devices

DRAM
Managed NAND
Multichip Packages
NAND Flash
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**TN-29-77: Using Chip Enable Pin Reduction in NAND Flash Memory Devices (PDF)**

NAND Flash

(TN-29-77) This technical note describes how to enable and use the chip enable pin reduction feature in Micron's NAND Flash memory devices and discusses reduction limitations based on NAND package and system configuration.

File Type: PDF

Updated: 05/08/2017

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TN-29-62: Software Device Drivers for Large Page Micron NAND Flash Memory Devices (PDF)NAND Flash
SLC NAND

(TN-29-62) Download the low-level driver described in this document [here](#).

File Type: PDF

Updated: 04/24/2017

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**TN-13-49: SLC NAND Device Refresh (PDF)**NAND Flash
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

Updated: 10/31/2016

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TN-29-14: NAND Flash Performance Increase Using PROGRAM PAGE CACHE MODE Command (PDF)

NAND Flash

(TN-29-14) This technical note highlights the significant performance gains realized when using the PROGRAM PAGE CACHE MODE command in Micron's NAND Flash devices.

File Type: PDF

Updated: 09/29/2016

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SEARCH (53) TECHNICAL NOTES

Customer Service Note

Customer Service Note

Shipping Quantities (PDF)DRAM
DRAM Modules
Hybrid Memory Cube

(CSN-04) This customer service note describes the standard shipping quantity, box quantity, and types such as tape and reel for Micron's products.

Multichip Packages
See More Tags

File Type: PDF Updated: 02/01/2018

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Customer Service Note

ESD Charge Device Model (CDM) Test Conversion (PDF)

DRAM
DRAM Modules
Hybrid Memory Cube
Multichip Packages
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(CSN-41) This customer service note discusses Micron's conversion of JEDEC customer device-qualification documents to reporting CDM data per JS-002.

File Type: PDF Updated: 11/28/2017

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Customer Service Note

Wafer Packaging and Packaging Materials (PDF)

DRAM
NAND Flash

(CSN-20) Provides complete shipping and recycling information about each of the materials used for shipping Micron's products.

File Type: PDF Updated: 06/21/2016

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SEARCH (3) CUSTOMER SERVICE NOTE

Parts with the same Data Sheet (14)

MTFC32GAKAEFF-AIT (Current)	MTFC32GAKAECN-AIT	MTFC16GAKAEFF-AIT	MTFC32GAKAEDQ-AIT
MTFC32GAKAEFF-AIT (Current)			


Where to Buy


Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MTFC32GAKAEFF-AIT	Production	N/A	SNPPF	N/A	N/A	No		N/A

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- DRAM Modules
- NAND Flash
- Managed NAND
- NOR Flash
- Hybrid Memory Cube
- Multichip Packages
- Memory Cards
- Solid State Drives

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 - Consumer
 - Embedded Memory Solutions
 - Federal Solutions
 - Financial Services
 - Industrial Memory Solutions
 - Mobile Memory Solutions
 - Networking Innovations

By Application

- Client
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- Data Center
- Storage Data Security
- Enterprise SSD Storage
- Supercomputing Memory

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