

0.8 Ω , Low-Voltage, Single-Supply Dual SPST Analog Switches

ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to GND

V+, IN_ -0.3V to +4V

COM_, NO_, NC_ (Note 1) -0.3V to (V+ + 0.3V)

Continuous Current COM_, NO_, NC_ ± 150 mA

Peak Current COM_, NO_, NC_ (pulsed at 1ms 10% duty cycle) ± 300 mA

Continuous Power Dissipation (T_A = +70°C)

8-Pin SOT23 (derate 7.52mW/°C above +70°C) 602mW

8-Pin μ MAX (derate 4.5mW/°C above +70°C) 362mW

8-Pin μ DFN (derate 4.8mW/°C above +70°C) 381mW

Operating Temperature Range -40°C to +85°C

Maximum Junction Temperature +150°C

Storage Temperature Range -65°C to +150°C

Lead Temperature (soldering, 10s) +300°C

Note 1: Signals on COM_, NO_, or NC_ exceeding V+ or GND are clamped by internal diodes. Limit forward current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at V+ = +3.0V, T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T _A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{COM_} , V _{NO_} , V _{NC_}			0		V+	V
On-Resistance	R _{ON}	V+ = 2.7V, I _{COM_} = 100mA, V _{NO_} or V _{NC_} = 1.5V	+25°C	0.5	0.8		Ω
			T _{MIN} to T _{MAX}		0.9		
On-Resistance Match Between Channels (Note 4)	ΔR_{ON}	V+ = 2.7V, I _{COM_} = 100mA, V _{NO_} or V _{NC_} = 1.5V	+25°C	0.05	0.08		Ω
			T _{MIN} to T _{MAX}		0.09		
On-Resistance Flatness (Note 5)	R _{FLAT(ON)}	V+ = 2.7V, I _{COM_} = 100mA, V _{NO_} or V _{NC_} = 1V, 1.5V, 2V	+25°C	0.05	0.18		Ω
			T _{MIN} to T _{MAX}		0.20		
NO_ or NC_ Off-Leakage Current	I _{NO_(OFF)} , I _{NC_(OFF)}	V+ = 3.3V, V _{COM_} = 0.3V, 3V, V _{NO_} or V _{NC_} = 3V, 0.3V	+25°C	-1		1	nA
			T _{MIN} to T _{MAX}	-5		5	
COM_ Off-Leakage Current	I _{COM_(OFF)}	V+ = 3.3V, V _{COM_} = 0.3V, 3V V _{NO_} or V _{NC_} = 3V, 0.3V or floating	+25°C	-1		1	nA
			T _{MIN} to T _{MAX}	-5		5	
COM_ On-Leakage Current	I _{COM_(ON)}	V+ = 3.3V, V _{COM_} = 3V, 0.3V; V _{NO_} or V _{NC_} = 3V, 0.3V or floating	+25°C	-2		2	nA
			T _{MIN} to T _{MAX}	-10		10	

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

MAX4741/MAX4742/MAX4743

ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at V+ = +3.0V, T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T _A	MIN	TYP	MAX	UNITS
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1	+25°C	18	24	ns	
			T _{MIN} to T _{MAX}	28			
Turn-Off Time	t _{OFF}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1	+25°C	12	16	ns	
			T _{MIN} to T _{MAX}	18			
Break-Before-Make (Note 6)	t _{BBM}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1 (MAX4743)	+25°C	6		ns	
			T _{MIN} to T _{MAX}	1			
Charge Injection	Q	V _{GEN} = 0, R _{GEN} = 0, C _L = 1.0nF, Figure 3	+25°C	28		pC	
NO_ or NC_ Off-Capacitance	C _{OFF}	f = 1MHz, Figure 4	+25°C	32		pF	
COM_ Off-Capacitance	C _{COM_(OFF)}	f = 1MHz, Figure 4	+25°C	32		pF	
COM_ On-Capacitance	C _{COM_(ON)}	f = 1MHz, Figure 4	+25°C	44		pF	
-3dB On-Channel Bandwidth	BW	Signal = 0, R _{IN} = R _{OUT} = 50Ω, C _L = 5pF, Figure 2		100		MHz	
Off-Isolation (Note 7)	V _{ISO}	f = 1MHz, V _{COM_} = 1V _{RMS} , R _L = 50Ω, C _L = 5pF, Figure 2	+25°C	-55		dB	
Crosstalk (Note 8)		f = 1MHz, V _{COM_} = 1V _{RMS} , R _L = 50Ω, C _L = 5pF, Figure 2	+25°C	-110		dB	
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, V _{COM_} = 2Vp-p, R _L = 32Ω	+25°C	0.02		%	
LOGIC INPUT							
Input Logic High	V _{IH}			1.4		V	
Input Logic Low	V _{IL}			0.5		V	
Input Leakage Current	I _{IN}	V _{IN_} = 0 or V+		-1	0.005	1	μA
POWER SUPPLY							
Power-Supply Range	V+			1.6	3.6		V
Positive Supply Current	I+	V+ = 3.6V, V _{IN_} = 0 or V+, all channels on or off	+25°C	0.2		μA	

0.8 Ω , Low-Voltage, Single-Supply Dual SPST Analog Switches

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply

(V+ = +1.8V, V_{IH} = +1.0V, V_{IL} = 0.4V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T _A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{COM_} , V _{NO_} , V _{NC_}			0		V+	V
On-Resistance	R _{ON}	I _{COM_} = 10mA, V _{NO_} or V _{NC_} = 0.9V	+25°C	1.3	2.5	Ω	
			T _{MIN} to T _{MAX}	5			
NO_ or NC_ Off-Leakage Current	I _{NO_(OFF)} , I _{NC_(OFF)}	V _{COM_} = 0.3V, 1.5V; V _{NO_} or V _{NC_} = 1.5V, 0.3V	+25°C	-1	1	nA	
			T _{MIN} to T _{MAX}	-5	5		
COM_ Off-Leakage Current	I _{COM_(OFF)}	V _{COM_} = 0.3V, 1.5V; V _{NO_} or V _{NC_} = 1.5V, 0.3V	+25°C	-1	1	nA	
			T _{MIN} to T _{MAX}	-5	5		
COM_ On-Leakage Current	I _{COM_(ON)}	V _{COM_} = 0.3V, 1.5V, V _{NO_} or V _{NC_} = 0.3V, 1.5V, or floating	+25°C	-2	2	nA	
			T _{MIN} to T _{MAX}	-10	10		
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1	+25°C	25	35	ns	
			T _{MIN} to T _{MAX}	40			
Turn-Off Time	t _{OFF}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1	+25°C	16	25	ns	
			T _{MIN} to T _{MAX}	30			
Break-Before-Make (Note 6)	t _{BBM}	V _{NO_} , V _{NC_} = 1.5V, R _L = 50Ω, C _L = 35pF, Figure 1 (MAX4743)	+25°C	10		ns	
			T _{MIN} to T _{MAX}	1			
Charge Injection	Q	V _{GEN} = 0, R _{GEN} = 0, C _L = 1nF, Figure 2	+25°C	16		pC	
Off-Isolation (Note 7)	V _{ISO}	f = 1MHz, V _{NO_} = V _{NC_} = 1V _{RMS} , R _L = 50Ω, C _L = 5pF, Figure 2	+25°C	-50		dB	
Crosstalk (Note 8)		f = 1MHz, V _{COM_} = 1V _{RMS} , R _L = 50Ω, C _L = 5pF, Figure 2	+25°C	-110		dB	

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply (continued)

($V_+ = +1.8\text{V}$, $V_{IH} = +1.0\text{V}$, $V_{IL} = 0.4\text{V}$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise specified. Typical values are at $T_A = +25^\circ\text{C}$.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
LOGIC INPUT							
Input Logic High	V_{IH}			1			V
Input Logic Low	V_{IL}					0.4	V
Input Leakage Current	I_{IN}	$V_{IN} = 0$ or V_+		-1		1	μA

Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value is a maximum, is used in this data sheet.

Note 3: μDFN and SOT23 packaged parts are 100% tested at $+25^\circ\text{C}$. Limits across the full temperature range are guaranteed by design and correlation. μMAX packaged parts -40°C specifications are guaranteed by design.

Note 4: $\Delta R_{ON} = R_{ON}(\text{MAX}) - R_{ON}(\text{MIN})$.

Note 5: Flatness is defined as the difference between the maximum and the minimum value of on-resistance as measured over the specified analog signal ranges.

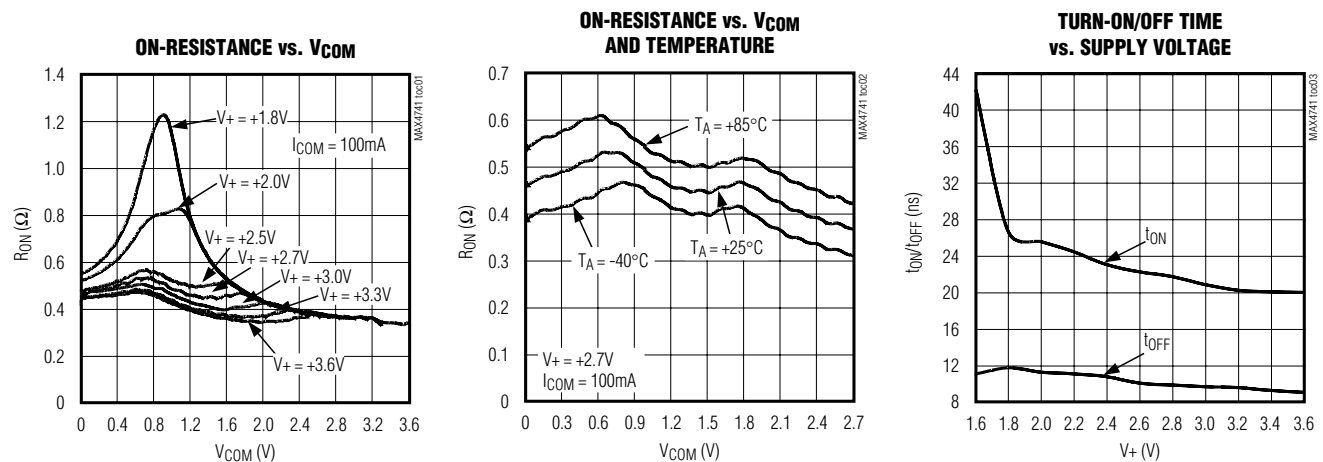
Note 6: Guaranteed by design.

Note 7: Off-Isolation = $20\log_{10}(V_{COM}/V_{NO-})$, V_{COM} = output, V_{NO-} = input to off switch.

Note 8: Between two switches.

Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

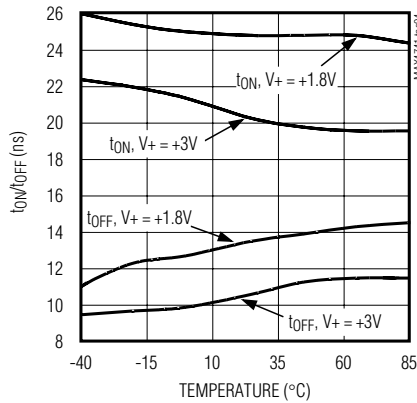


0.8 Ω , Low-Voltage, Single-Supply Dual SPST Analog Switches

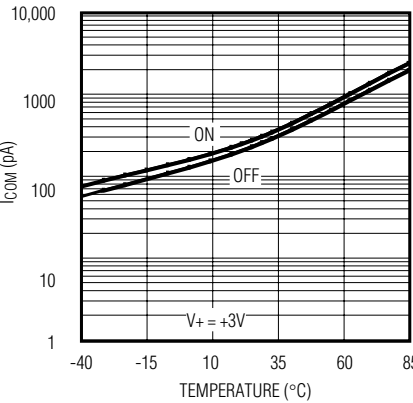
Typical Operating Characteristics (continued)

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

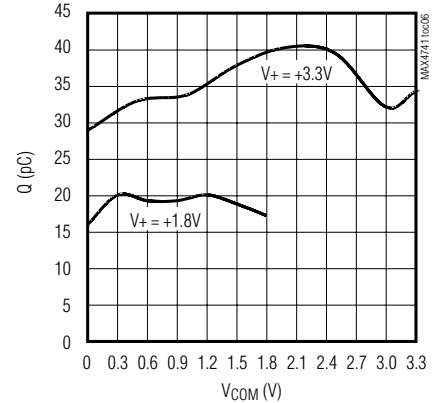
**TURN-ON/OFF TIME
vs. TEMPERATURE**



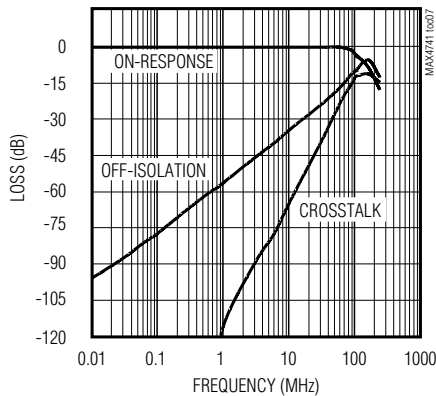
**LEAKAGE CURRENT
vs. TEMPERATURE**



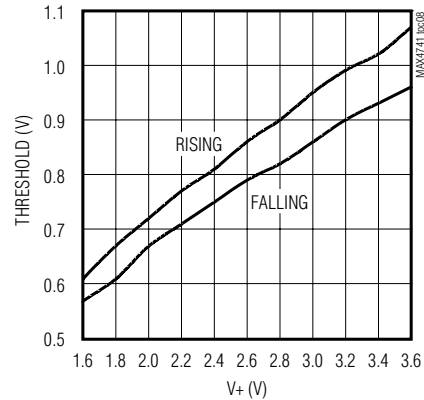
CHARGE INJECTION vs. OUTPUT VOLTAGE



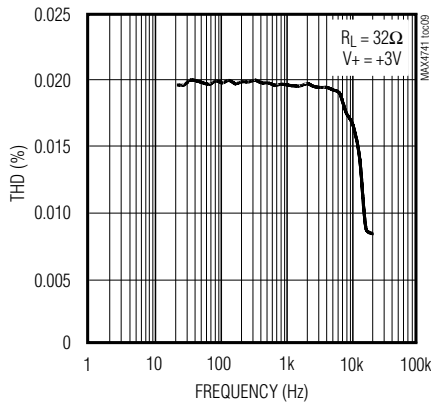
FREQUENCY RESPONSE



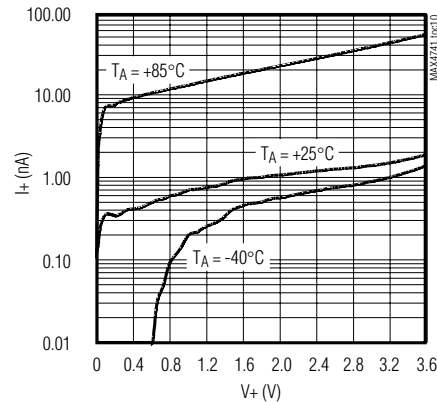
**LOGIC THRESHOLD VOLTAGE
vs. SUPPLY VOLTAGE**



**TOTAL HARMONIC DISTORTION
vs. FREQUENCY**



**SUPPLY CURRENT vs. SUPPLY
VOLTAGE AND TEMPERATURE**



0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Pin Description

PIN						NAME	FUNCTION
MAX4741		MAX4742		MAX4743			
μMAX/μDFN	SOT23-8	μMAX	SOT23-8	μMAX	SOT23-8		
1	8	—	—	1	8	NO1	Analog Switch 1 Normally Open
—	—	1	8	—	—	NC1	Analog Switch 1 Normally Closed
2	7	2	7	2	7	COM1	Analog Switch 1 Common
3	6	3	6	3	6	IN2	Logic Control Input Switch 2
4	5	4	5	4	5	GND	Ground
5	3	—	—	—	—	NO2	Analog Switch 2 Normally Open
—	—	5	3	5	3	NC2	Analog Switch 2 Normally Closed
6	4	6	4	6	4	COM2	Analog Switch 2 Common
7	1	7	1	7	1	IN1	Logic Control Input Switch 1
8	2	8	2	8	2	V+	Positive Supply Voltage

Detailed Description

The MAX4741/MAX4742/MAX4743 are low 0.8Ω max (at V+ = +3V) on-resistance, low-voltage, dual analog switches that operate from a +1.6V to +3.6V single supply. CMOS switch construction allows switching analog signals that are within the supply voltage range (GND to V+).

When powered from a +3V supply, the 0.8Ω max R_{ON} allows high continuous currents to be switched in a variety of applications.

Applications Information

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings can cause permanent damage to the devices. Always sequence V+ on first, followed by NO_, NC_, or COM_.

Although it is not required, power-supply bypassing improves noise margin and prevents switching noise propagation from the V+ supply to other components. A 0.1μF capacitor, connected from V+ to GND, is adequate for most applications.

Logic Inputs

The MAX4741/MAX4742/MAX4743 logic inputs can be driven up to +3.6V regardless of the supply voltage. For example, with a +1.8V supply, IN_ may be driven low to GND and high to +3.6V. Driving IN_ rail-to-rail minimizes power consumption.

Analog Signal Levels

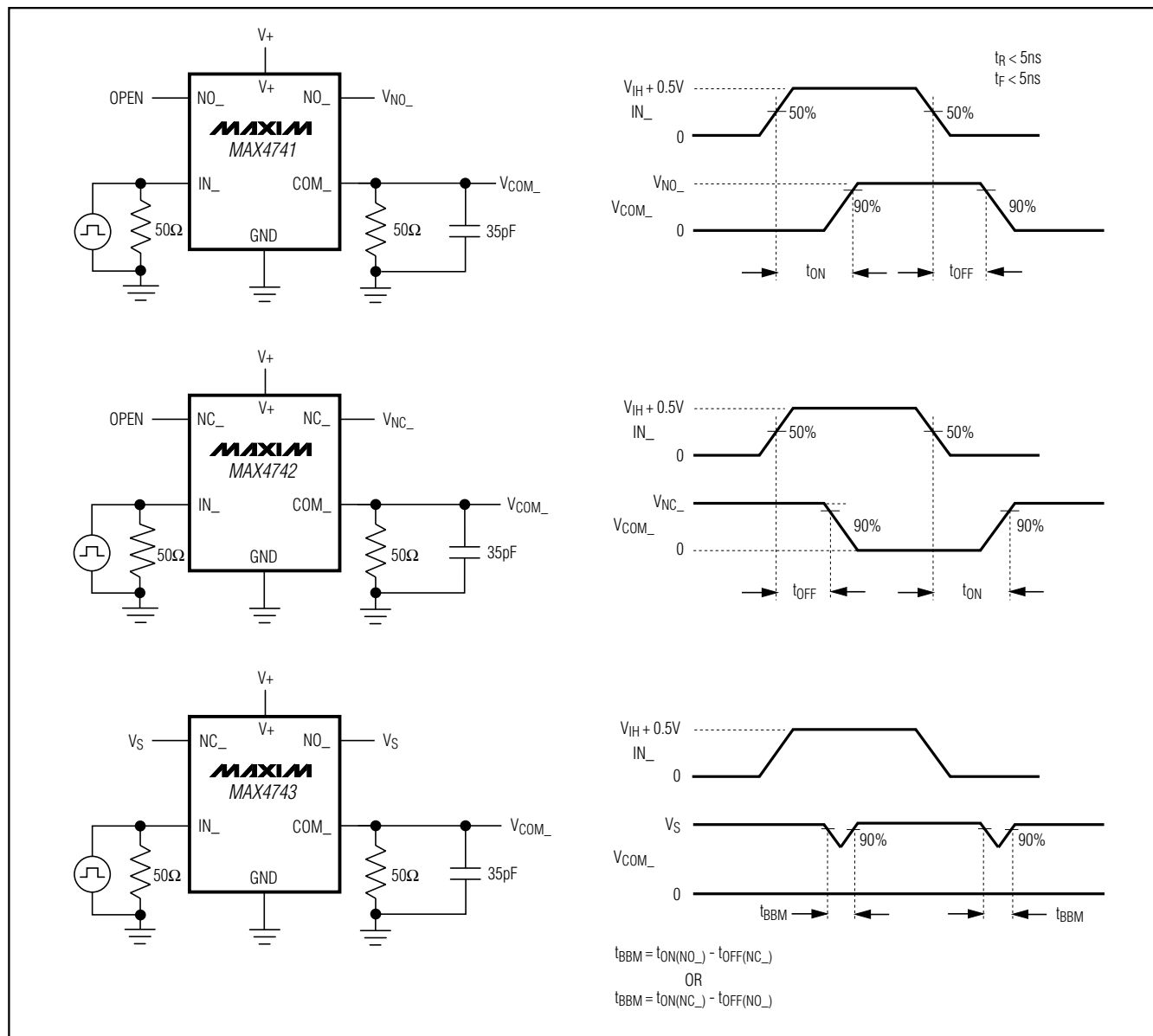
Analog signals that range over the entire supply voltage (V+ to GND) can be passed with very little change in on-resistance (see *Typical Operating Characteristics*). The switches are bidirectional, so the NO_, NC_, and COM_ pins can be used as either inputs or outputs.

Layout

High-speed switches require proper layout and design procedures for optimum performance. Reduce stray inductance and capacitance by keeping traces short and wide. Ensure that bypass capacitors are as close to the device as possible. Use large ground planes where possible.

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Test Circuits/Timing Diagrams



0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Test Circuits/Timing Diagrams (continued)

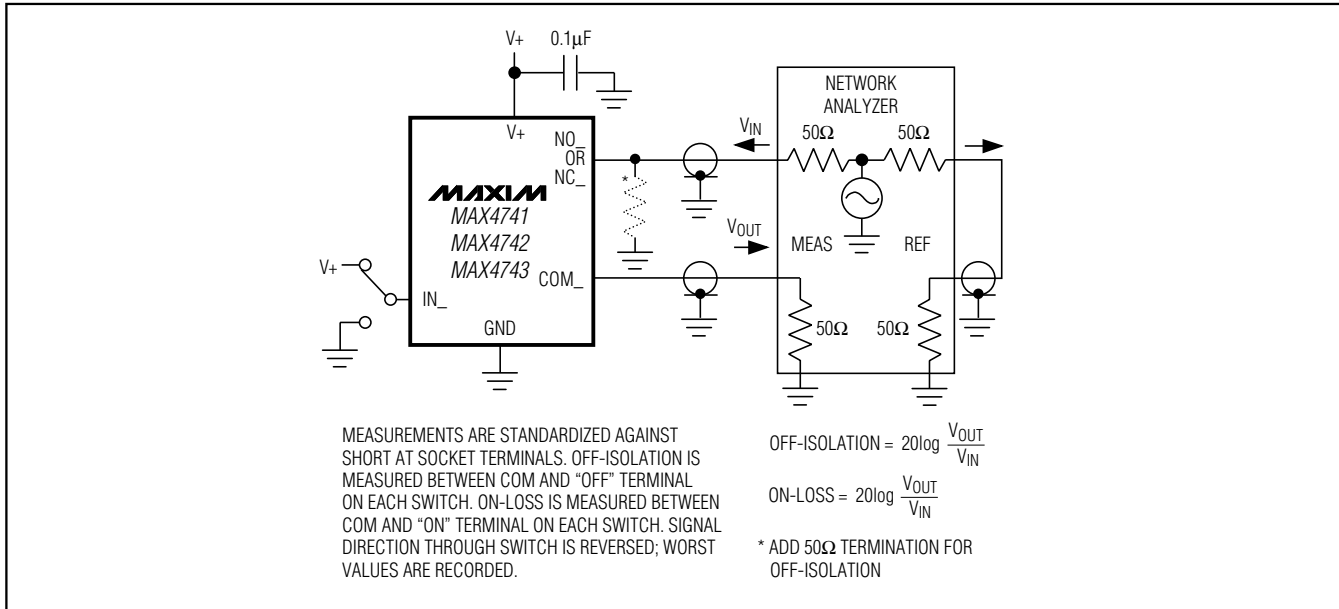


Figure 2. Off-Isolation, On-Loss, and Crosstalk

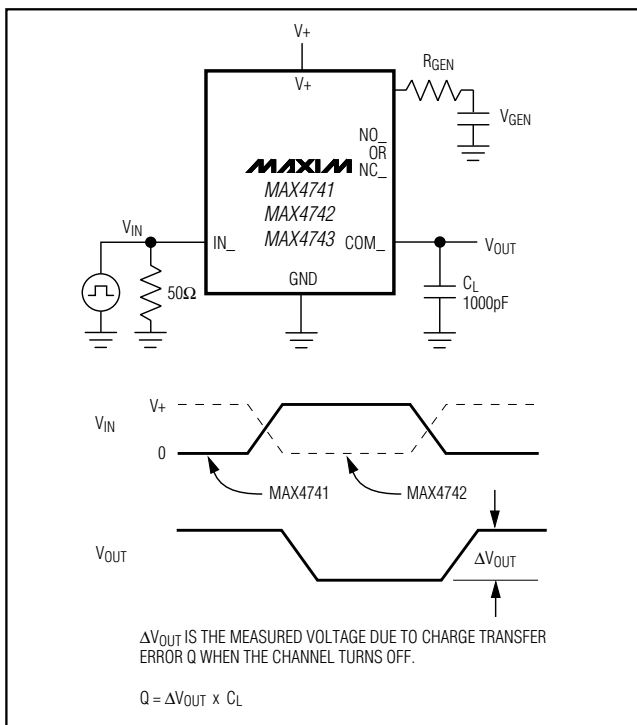


Figure 3. Charge Injection

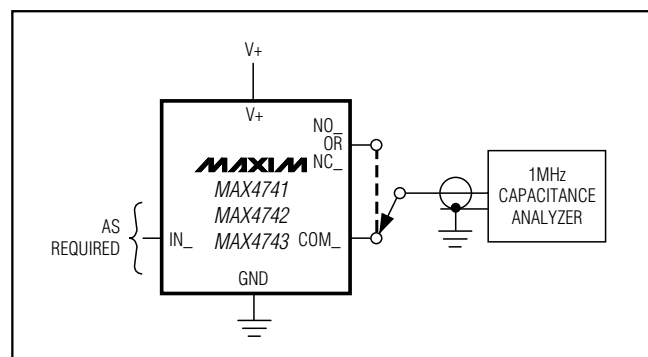
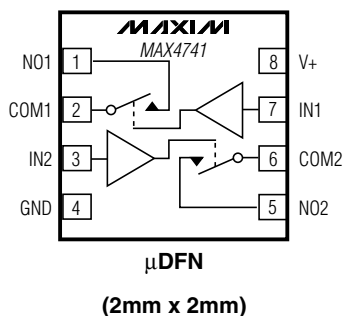
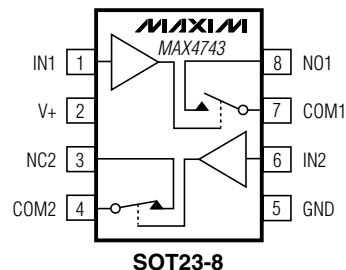
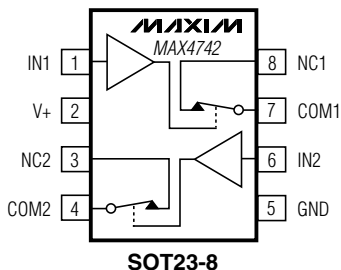
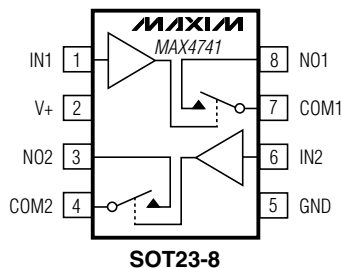


Figure 4. NO_, NC_, and COM_ Capacitance

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Pin Configurations (continued)

TOP VIEW



Chip Information

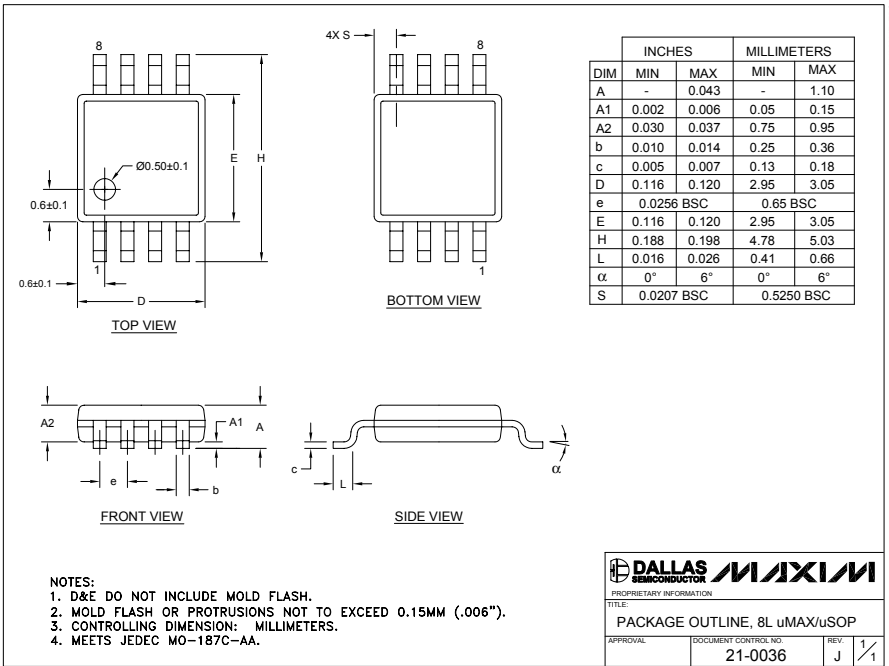
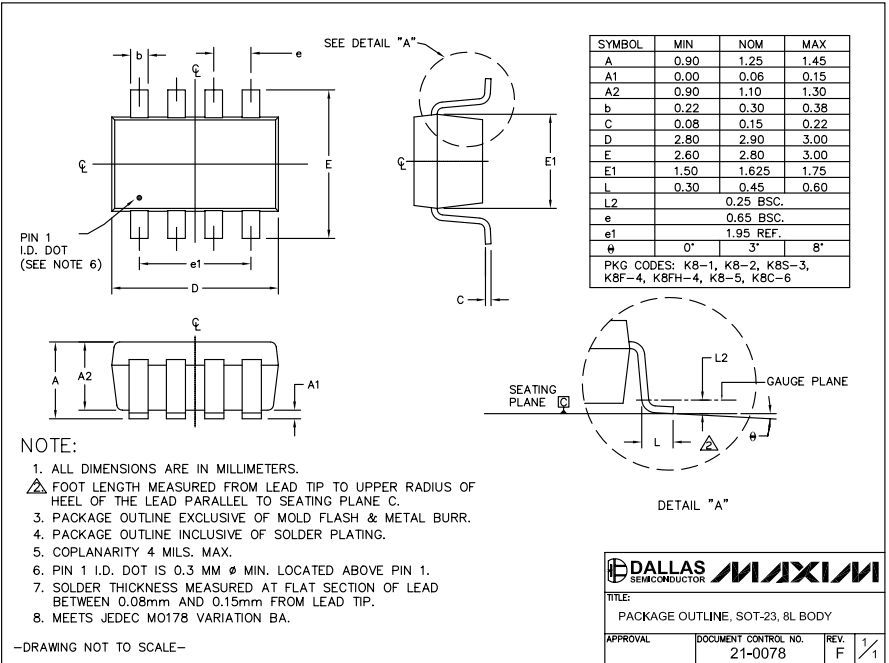
TRANSISTOR COUNT = 121

PROCESS = CMOS

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Package Information

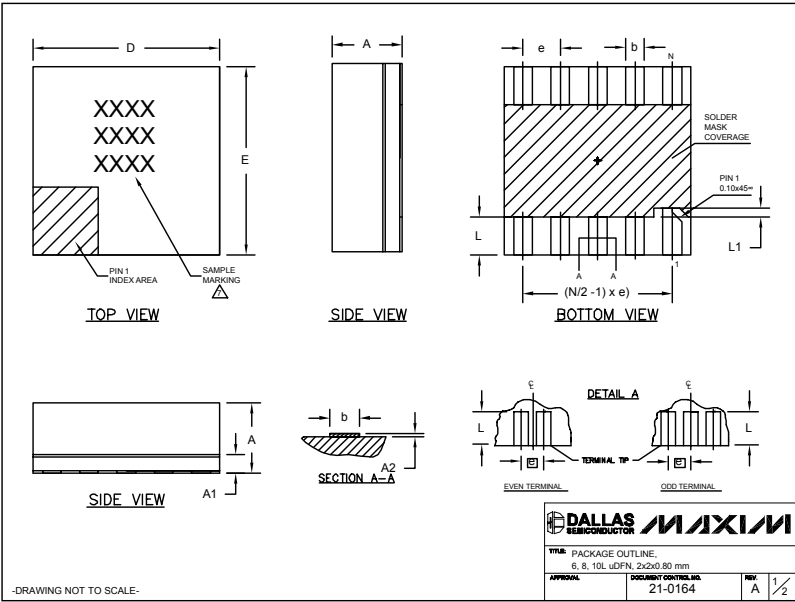
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



COMMON DIMENSIONS			
SYMBOL	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.15	0.20	0.25
A2	0.020	0.025	0.035
D	1.95	2.00	2.05
E	1.95	2.00	2.05
L	0.30	0.40	0.50
L1	0.10 REF.		

PACKAGE VARIATIONS				
PKG. CODE	N	e	b	(N/2 - 1) x e
L622-1	6	0.65 BSC	0.30±0.05	1.30 REF.
L822-1	8	0.50 BSC	0.25±0.05	1.50 REF.
L1022-1	10	0.40 BSC	0.20±0.03	1.60 REF.

NOTES:

- ALL DIMENSIONS ARE IN mm, ANGLES IN DEGREES.
- COPLANARITY SHALL NOT EXCEED 0.08mm.
- WARPAGE SHALL NOT EXCEED 0.10mm.
- PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
- "N" IS THE TOTAL NUMBER OF LEADS.
- NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

MAXIM

PACKAGE OUTLINE

6, 8, 10L UDFN, 2x2x0.80 mm

21-0164

1/2

Revision History

Pages changed at Rev 2: 1, 12

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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