

LB1846M, 1848M

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	$V_{CC\text{ max}}$		-0.3 to +8.0	V
Output voltage	V_{OUT}		$V_{CC} + V_{SF}$	V
Input voltage	V_{IN}		-0.3 to +8.0	V
Ground pin outflow current	I_{GND}	Per channel	800	mA
Allowable power dissipation	$P_{d\text{ max1}}$	Independent IC	350	mW
	$P_{d\text{ max2}}$	When mounted*	870	mW
Operating temperature	T_{opr}		-20 to +75	$^\circ\text{C}$
Storage temperature	T_{stg}		-40 to +150	$^\circ\text{C}$

Note: *On the specified circuit board (a $114.3 \times 76.2 \times 1.5\text{-mm}^3$ glass-epoxy printed circuit board)

Allowable Operating Ranges at $T_a = 25^\circ\text{C}$

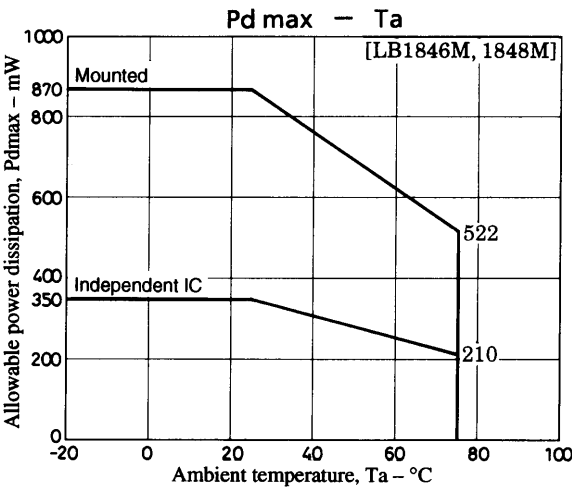
Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V_{CC}		2.5 to 7.5	V
Input high-level voltage	V_{IH}		2.0 to 7.5	V
Input low-level voltage	V_{IL}		-0.3 to +0.7	V

Electrical Characteristics at $T_a = 25^\circ\text{C}$, $V_{CC} = 5.0\text{ V}$

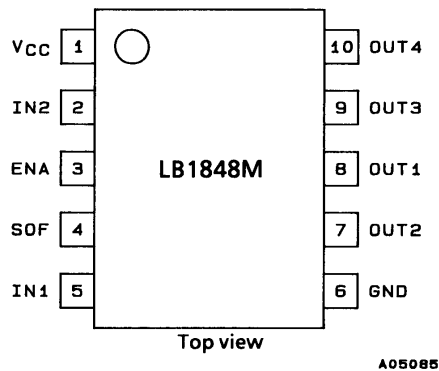
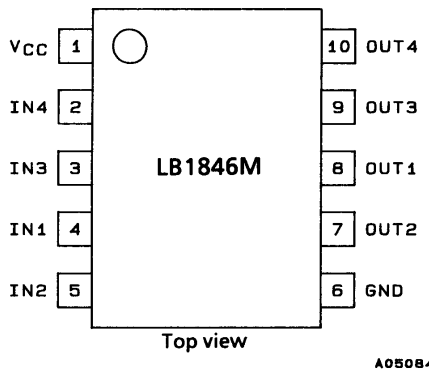
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[LB1846M]						
Current drain	I _{CC0}	IN1, 2, 3, 4 = 0 V		0.1	10	μA
	I _{CC1}	IN1, 3 = 3 V, IN2, 4 = 0 V		30	40	mA
Output saturation voltage	V _{OUT1}	V _{IN} = 3 V or 0 V, V _{CC} = 3 to 7.5 V, I _{OUT} = 200 mA		0.27	0.4	V
	V _{OUT2}	V _{IN} = 3 V or 0 V, V _{CC} = 4 to 7.5 V, I _{OUT} = 400 mA		0.55	0.8	V
Input current	I _{IN}	V _{IN} = 5 V		150	200	μA
[Spark Killer Diode]						
Reverse current	I _{S(leak)}				30	μA
Forward voltage	V _{SF}	I _{OUT} = 400 mA			1.7	V
[LB1848M]						
Current drain	I _{CC0}	ENA = 0 V, V _{IN} = 3 V or 0 V		0.1	10	μA
	I _{CC1}	ENA = 3 V, V _{IN} = 3 V or 0 V		25	35	mA
Output saturation voltage	V _{OUT1}	ENA = 3 V, V _{IN} = 3 V or 0 V, V _{CC} = 3 to 7.5 V, I _{OUT} = 200 mA		0.27	0.4	V
	V _{OUT2}	ENA = 3 V, V _{IN} = 3 V or 0 V, V _{CC} = 4 to 7.5 V, I _{OUT} = 400 mA		0.55	0.8	V
Input current 1	I _{IN}	V _{IN} = 5 V		75	100	μA
Input current 2	I _{ENA}	ENA = 5 V		85	110	μA
[Spark Killer Diode]						
Reverse current	I _{S(leak)}				30	μA
Forward voltage	V _{SF}	I _{OUT} = 400 mA			1.7	V

Note: The thermal shutdown circuit function values are design guarantees, and are not tested.

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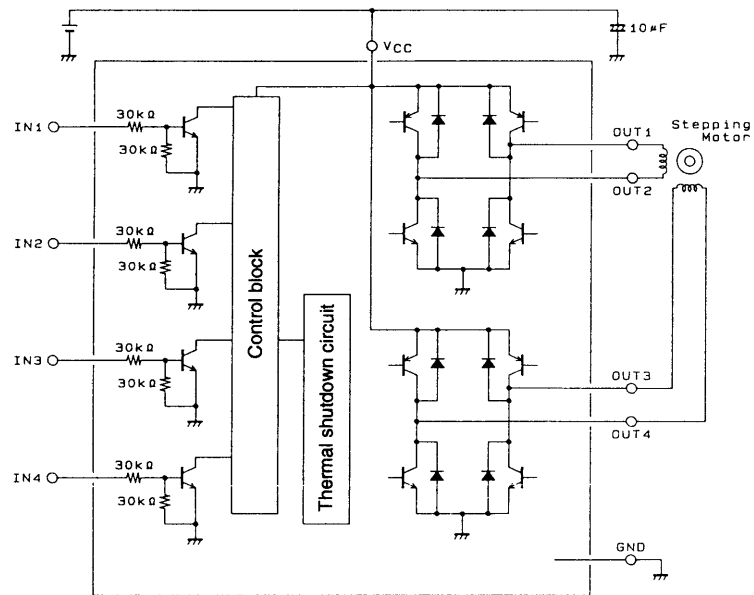


Pin Assignments



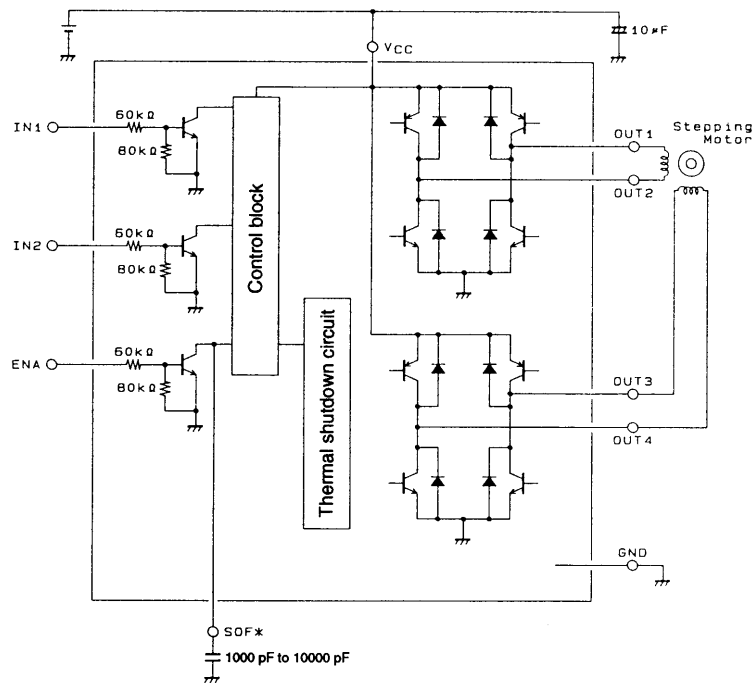
Block Diagrams

[LB1846M]



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[LB1848M]



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Note: When the "soft off" function is used, a capacitor must be connected to the S0F pin. If this function is not used, this pin must be left open with absolutely no signals or lines connected.

Notes on Wiring and Lines

Since large currents flow in the V_{CC} and ground lines, oscillations may occur on these lines. The following points should be observed if such oscillations occur.

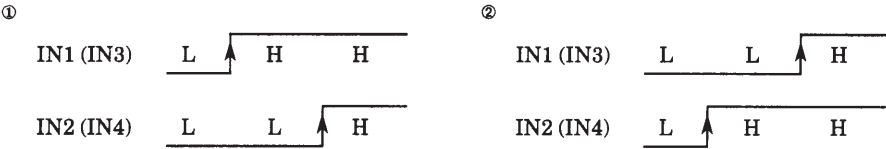
- (1) Lower the line impedances by making them shorter and thicker.
- (2) Attach capacitors close to the IC.
- (3) If the controller (CPU) is mounted on a separate printed circuit board, insert series resistors (of about 10 kΩ) between the controller outputs and this IC.

Truth Tables

[LB1846M]

IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4	Notes
L	L	L	L	OFF	OFF	OFF	OFF	Standby
H	L	L	L	H	L	OFF	OFF	1-2 phase excitation
H	L	H	L	H	L	H	L	
L	L	H	L	OFF	OFF	H	L	
L	H	H	L	L	H	H	L	
L	H	L	L	L	H	OFF	OFF	
L	H	L	H	L	H	L	H	
L	L	L	H	OFF	OFF	L	H	
H	L	L	H	H	L	L	H	
H	H	—	—	The logic output for the first high-level input is produced. *2				
—	—	H	H					

- Note: 1. “—” indicates a “don’t care” input.
2. If two high levels (H/H) are input to the IN1/IN2 pins with the timing shown in ① in the figure below, then the IN2 input that arrived later will be ignored and the IC will function as though an H/L combination is applied to the IN1/IN2 pins. Similarly, the timing shown in ② results in a L/H combination on the IN1/IN2 pins.



[LB1848M]

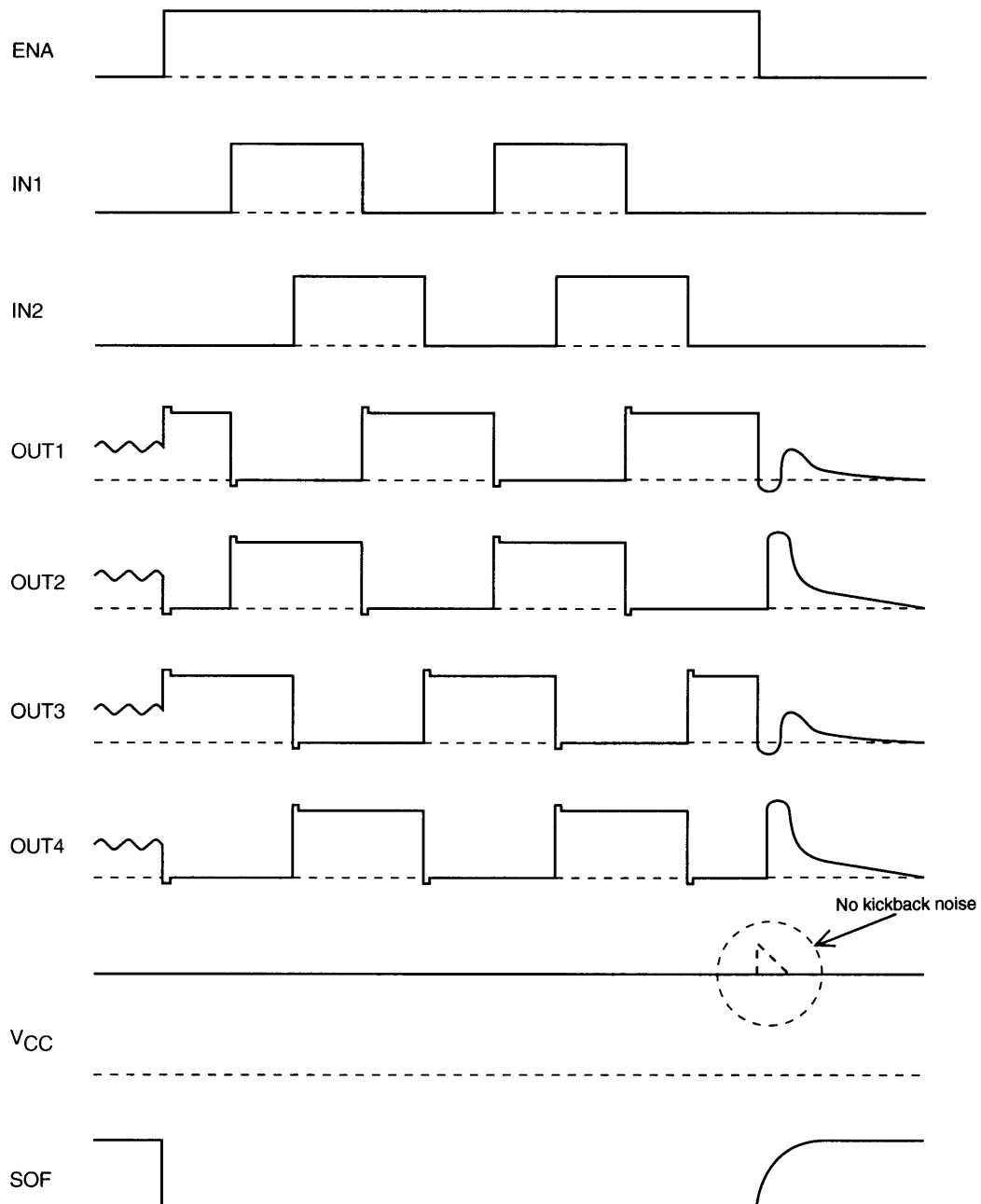
ENA	IN1	IN2	OUT1	OUT2	OUT3	OUT4	Notes
L	—	—	OFF	OFF	OFF	OFF	Standby
H	L	L	H	L	H	L	2-phase excitation
	L	H	H	L	L	H	
	H	H	L	H	L	H	
	H	L	L	H	H	L	

Note: “—” indicates a “don’t care” input.

SOF Pin ("Soft Off" Function) Operation [LB1848M only]

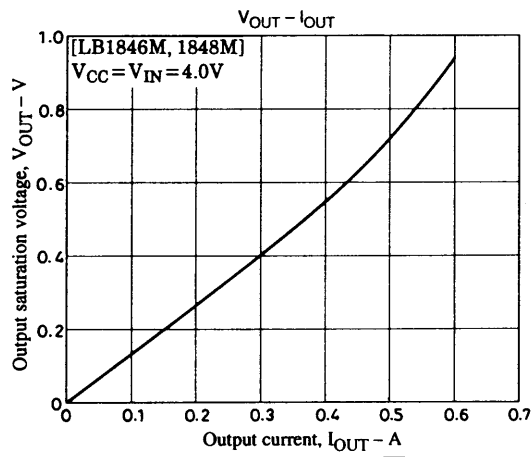
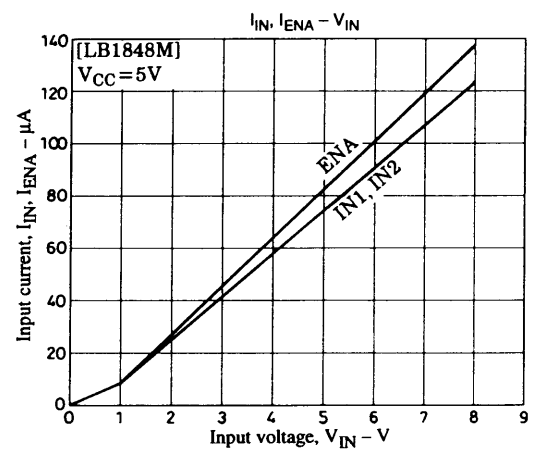
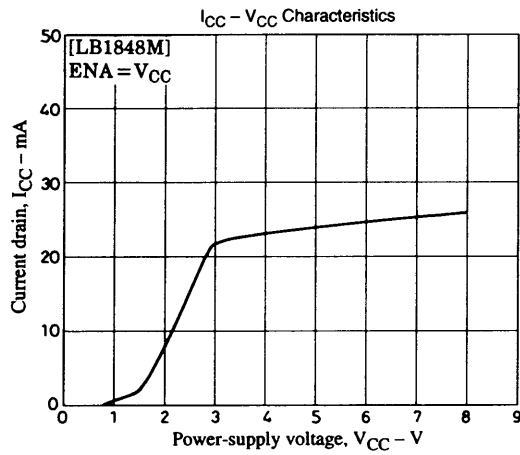
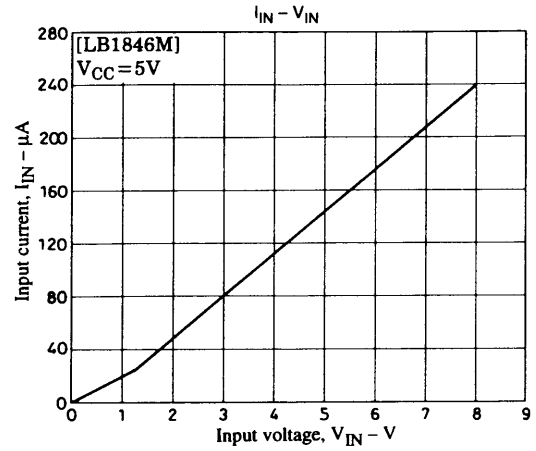
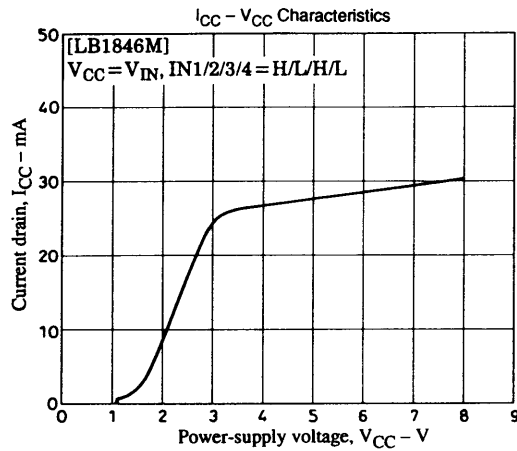
The soft off function reduces power supply line noise due to the kickback current generated when the stepping motor drive mode is switched from drive to standby. The "soft off" function provided by this IC operates when a capacitor (0.001 to 0.01 μF) is connected between the SOF pin and ground. (Leave the SOF pin open to disable the soft off function.)

The waveforms for each pin are shown below.



Timing Chart for Stepping Motor 2-Phase Excitation

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