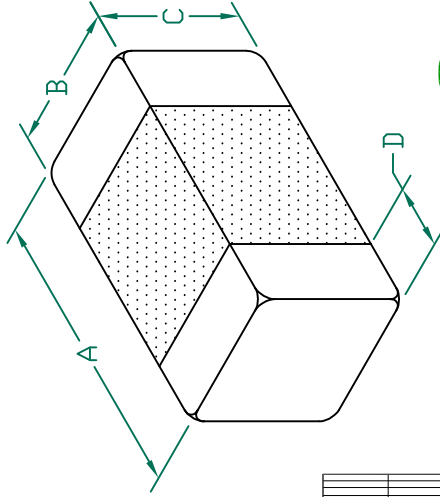


HZ0603A182R-10

UNCONTROLLED
DOCUMENT

PHYSICAL DIMENSIONS:

A	1.60	[.063]	+ 0.15	[.006]
B	0.80	[.031]	+ 0.15	[.006]
C	0.80	[.031]	+ 0.15	[.006]
D	0.36	[.014]	+ 0.15	[.006]



ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	1800	
Minimum	1350	
Maximum	2250	50 mA

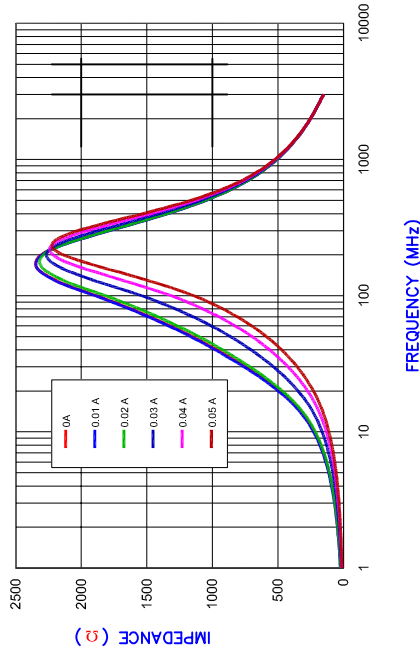
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (INCLUDING SELF-HEATING)

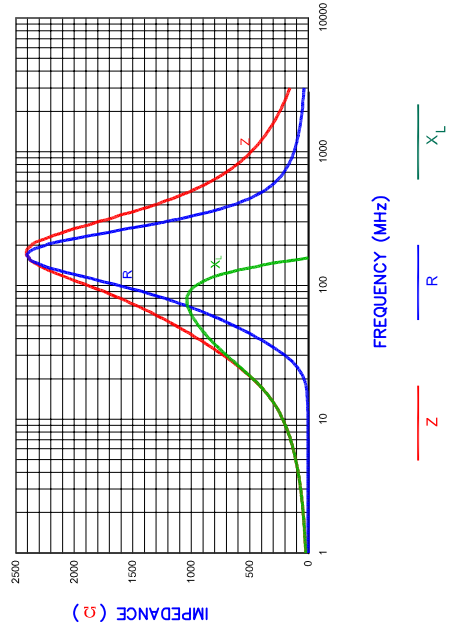


Z vs. FREQUENCY

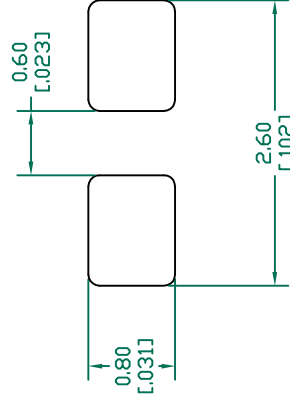
IMPEDANCE UNDER DC BIAS



|Z|, R, AND X vs. FREQUENCY

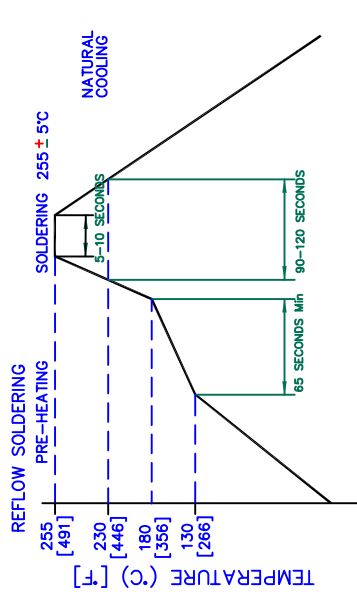


LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [0.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].

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REV	DATE	INT	DATE	JRK	DATE	SCALE	TOOL #	SHEET
D	08/05/13	OU	02/22/08	JRK	03/30/06	—	—	2 of 2
C	07/25/06	JRK	03/30/06	JRK	—	—	—	—
B	03/30/06	JRK	—	—	—	—	—	—
A	—	—	—	—	—	—	—	—
REV	DESCRIPTION	DATE	INT	DATE	JRK	SCALE	TOOL #	SHEET
D	ADD OPERATING TEMPERATURE	08/05/13	OU	02/22/08	JRK	03/30/06	—	2 of 2
C	UPDATE LAIRD LOGO AND REFLOW CURVE	07/25/06	JRK	03/30/06	JRK	—	—	—
B	ADD DC BIAS CURVE	—	—	—	—	—	—	—
A	ORIGINAL DRAFT	—	—	—	—	—	—	—
REV	DESCRIPTION	DATE	INT	DATE	JRK	SCALE	TOOL #	SHEET

Laird