



## Insertion Loss vs. Temperature



**Return Loss vs. Attenuation** 



**Relative Phase** 



\*Two-tone input power = -8 dBm each tone, 1 MHz spacing.

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# HMC346LP3 / 346LP3E

# GaAs MMIC VOLTAGE-VARIABLE ATTENUATOR, DC - 14 GHz



### Relative Attenuation vs. Control Voltage @ 10 GHz



Input IP3 vs. Attenuation\*



ATTENUATORS - SMT C

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**ATTENUATORS - SMT** 

### Input IP2 vs. Attenuation\*



0.25 dB Compression vs. Attenuation



Second Harmonic vs. Attenuation, Pin = -8 dBm



20 15 INPUT P1dB (dBm) 10 5 0 dB (BEF) 0 . . . . . . . . . 6 dB -5 2 8 10 12 16 0 6 14 4 FREQUENCY (GHz)

1 dB Compression vs. Attenuation

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# ROHS V

# Absolute Maximum Ratings

RF Input Power	+18 dBm	
Control Voltage Range	+1 to -5V	
Storage Temperature	-65 to +150 °C	
Operating Temperature	-40 to +85 °C	
ESD Sensitivity (HBM)	Class 1A	

# HMC346LP3 / 346LP3E

# GaAs MMIC VOLTAGE-VARIABLE ATTENUATOR, DC - 14 GHz



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

-.016 [0.40] REF

# **Outline Drawing**



SEATING

PLANE

-C-



BOTTOM VIEW

PIN

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#### NOTES:

- 1. LEADFRAME MATERIAL: COPPER ALLOY
- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- 6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

## Package Information

.003[0.08] C

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC346LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	346 XXXX
HMC346LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	<u>346</u> XXXX

[1] Max peak reflow temperature of 235  $^\circ\text{C}$ 

[2] Max peak reflow temperature of 260  $^\circ\text{C}$ 

[3] 4-Digit lot number XXXX

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ATTENUATOR, DC - 14 GHz

GaAs MMIC VOLTAGE-VARIABLE

# RoHS

## **Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 10, 12	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.	⊖ GND 
2, 11	RF1 RF2	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if the RF line potential is not equal to 0V.	
4, 9, 13, 14, 15, 16	N/C	This pin should be connected to PCB RF ground.	
5, 8	V2, V1	Control input (master).	500
6	I	Control input (slave).	500

# Single-Line Control Driver



External op-amp control circuit maintains impedance match while attenuation is varied. Input control ranges from 0 Volts (min. attenuation) to -3.0 Volts (max. attenuation.)

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# HMC346LP3 / 346LP3E

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## **Evaluation PCB**



v05.1008

## List of Materials for Evaluation PCB 105709<sup>[1]</sup>

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3 - J6	DC Pin
U1	HMC346LP3 / HMC346LP3E VVA
PCB [2]	105695 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF ports should be 50 ohm impedance and the package ground leads and package bottom should be connected directly to the PCB RF ground plane, similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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