

Figure 1. 8/20 us pulse waveform according to IEC 61000-4-5

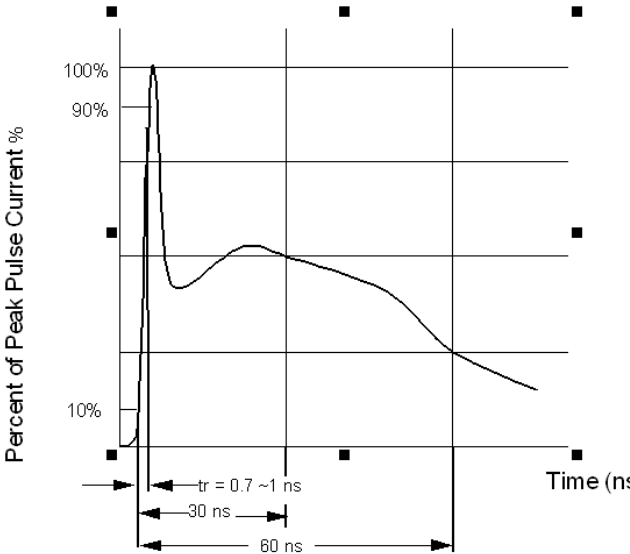


Figure 2. ESD pulse waveform according to IEC 61000-4-2

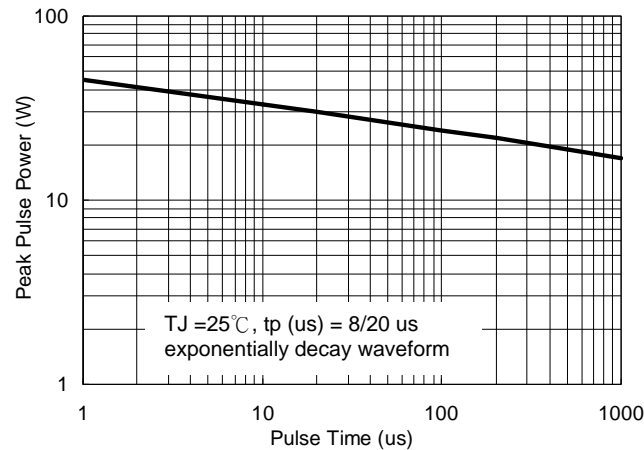


Figure 3. Power Dissipation versus Pulse Time

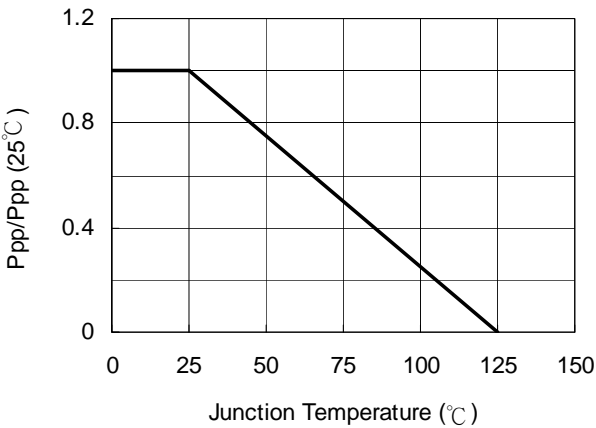


Figure 4. Peak pulse power versus T_J

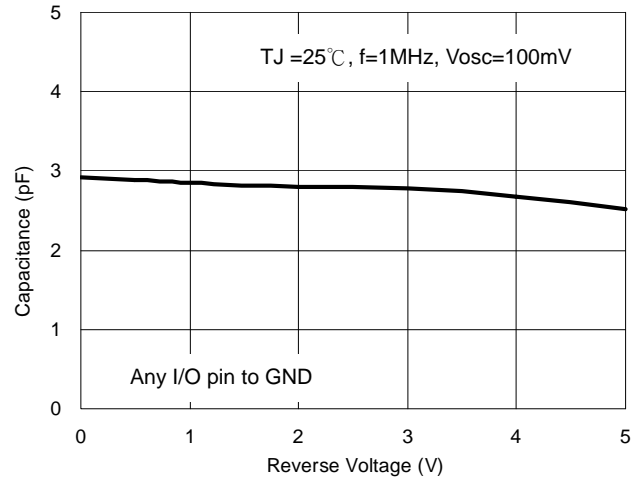


Figure 5. Capacitance versus Reverse Voltage

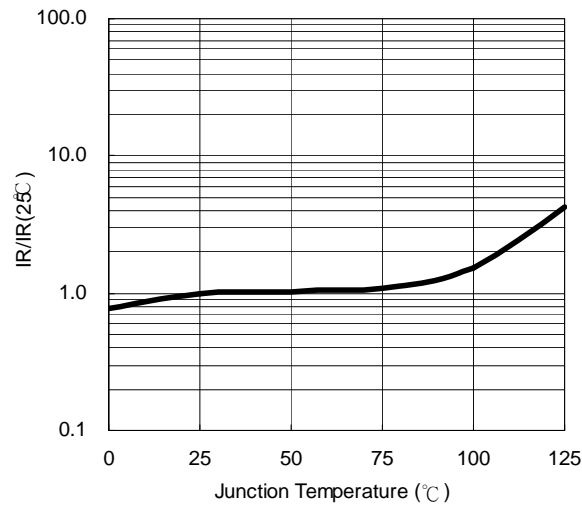


Figure 6. Reverse Leakage Current versus T_J

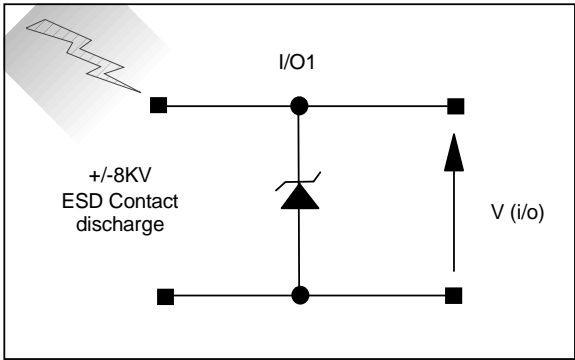


Figure 7. ESD Test Configuration

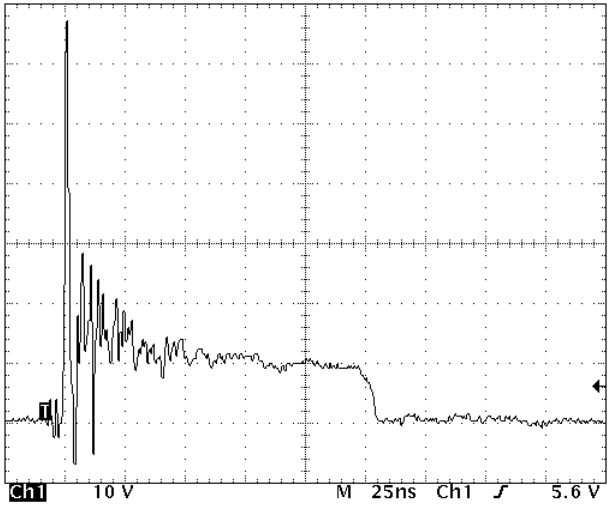


Figure 8. Clamped +8 kV ESD voltage waveform

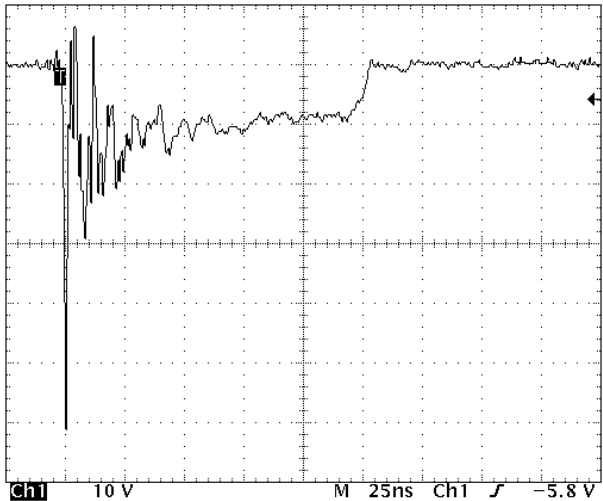


Figure 9. Clamped -8 kV ESD voltage waveform

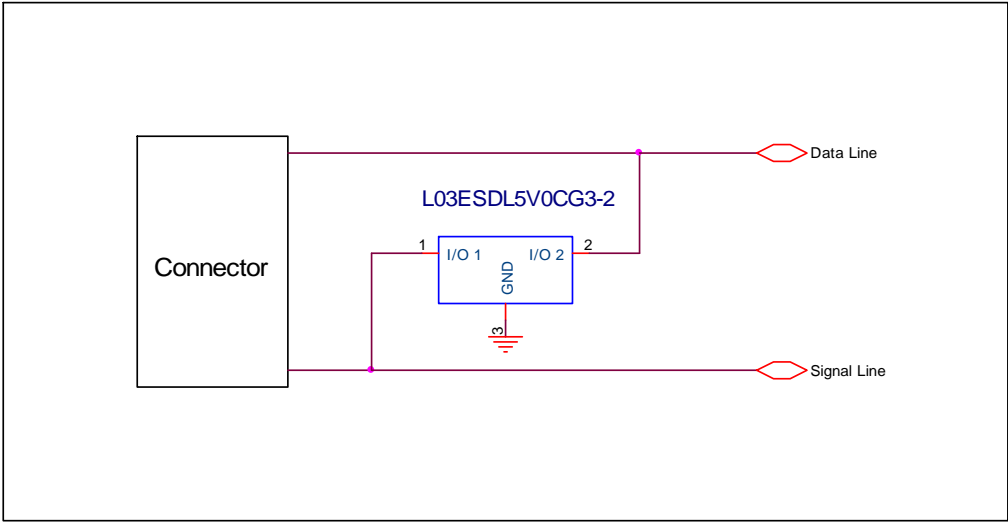


Figure 10. Computers, Peripherals, and Communication System ESD Protection

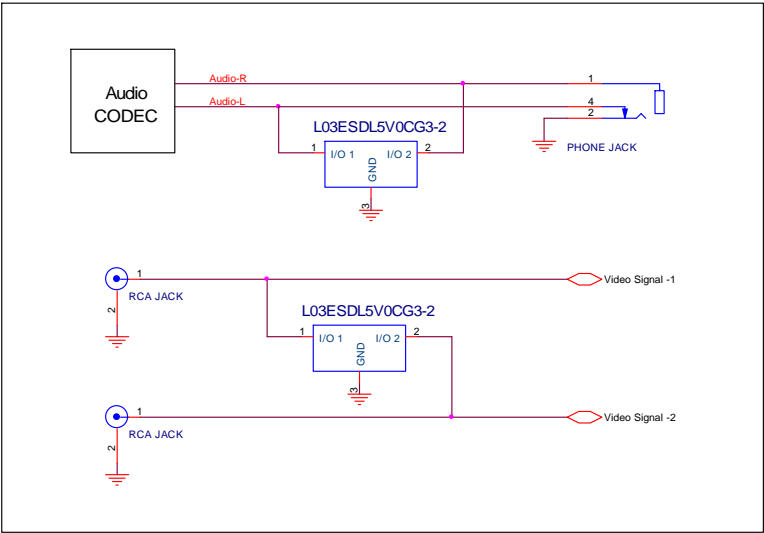


Figure 11. Audio and Video Equipment ESD Protection

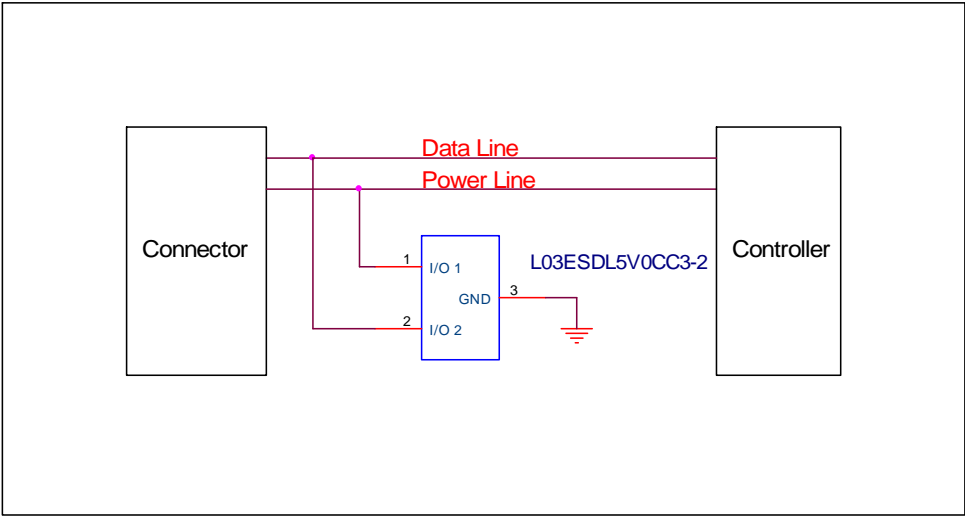
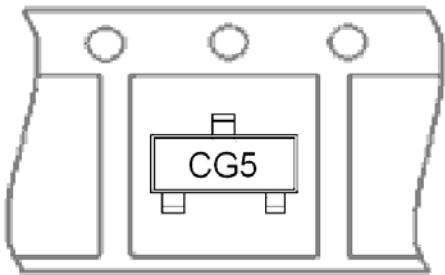


Figure 12. Portable Instrumentation ESD Protection

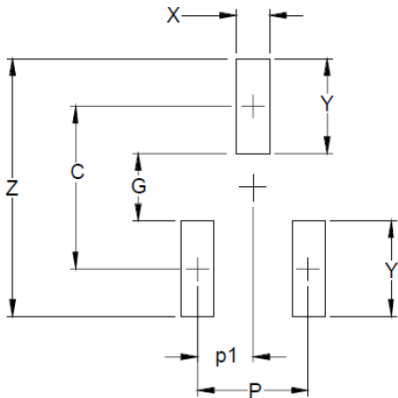
Marking & Orientation



Packaging Information

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L03ESDL5V0CG3-2	3000	7	45000	180K

SOT-523 Soldering Pad Layout



Dim.	Millimeters	Inches
C	(1.40)	(0.055)
P	1.00	0.39
p1	0.50	0.20
G	0.60	0.24
X	0.40	0.16
Y	0.80	0.31
Z	2.20	0.87

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