Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		1.7	K/W
Thermal resistance, junction – ambient	R_{thJA}		62	

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	V_{GE}=0V, I_C=0.25mA	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	V_{GE} = 15V, I_C=6A				
		T_j=25°C	-	1.5	2.05	
		T_j=175°C	-	1.8		
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C=0.18mA, V_{CE}=V_{GE}	4.1	4.6	5.7	
Zero gate voltage collector current	I_{CES}	V_{CE}=600V, V_{GE}=0V				µA
		T_j=25°C	-	-	40	
		T_j=175°C	-	-	700	
Gate-emitter leakage current	I_{GES}	V_{CE}=0V,V_{GE}=20V	-	-	100	nA
Transconductance	g_{fs}	V_{CE}=20V, I_C=6A	-	3.6	-	S
Integrated gate resistor	R_{Gint}		none			Ω
Dynamic Characteristic						
Input capacitance	C_{iss}	V_{CE}=25V,	-	368	-	pF
Output capacitance	C_{oss}	V_{GE}=0V,	-	28	-	
Reverse transfer capacitance	C_{rss}	f=1MHz	-	11	-	
Gate charge	Q_{Gate}	V_{CC}=480V, I_C=6A V_{GE}=15V	-	42	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current[1)]	$I_{C(SC)}$	V_{GE}=15V,t_{SC}≤5µs V_{CC} = 400V, T_j = 25°C	-	55	-	A

[1)] Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at T_j=25 °C

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T_j=25°C, V_{CC}=400V, I_C=6A, V_{GE}=0/15V, r_G=23Ω, L_σ=60nH, C_σ=40pF L_σ, C_σ from Fig. E Energy losses include "tail" and diode reverse recovery. Diode used IDP06E60	-	9	-	ns
Rise time	t_r		-	6	-	
Turn-off delay time	$t_{d(off)}$		-	130	-	
Fall time	t_f		-	58	-	
Turn-on energy	E_{on}		-	0.09	-	mJ
Turn-off energy	E_{off}		-	0.11	-	
Total switching energy	E_{ts}		-	0.2	-	

Switching Characteristic, Inductive Load, at T_j=175 °C

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T_j=175°C, V_{CC}=400V, I_C=6A, V_{GE}=0/15V, r_G= 23Ω L_σ=60nH, C_σ=40pF L_σ, C_σ from Fig. E Energy losses include "tail" and diode reverse recovery. Diode used IDP06E60	-	9	-	ns
Rise time	t_r		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	165	-	
Fall time	t_f		-	84	-	
Turn-on energy	E_{on}		-	0.14	-	mJ
Turn-off energy	E_{off}		-	0.18	-	
Total switching energy	E_{ts}		-	0.335	-	

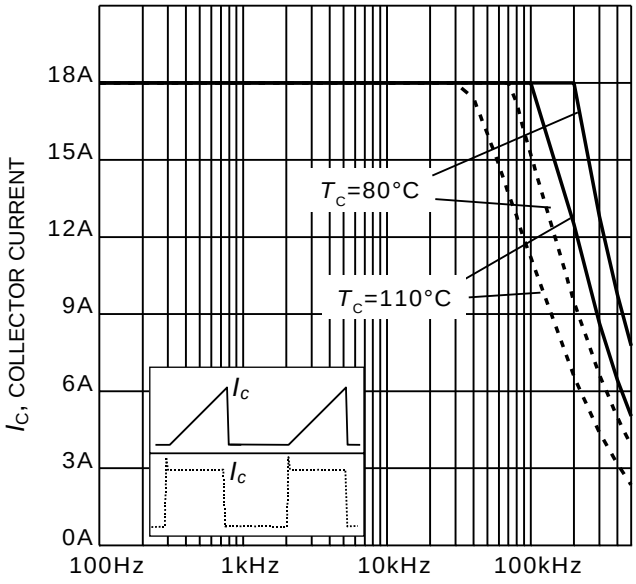


Figure 1. Collector current as a function of switching frequency
($T_j \leq 175°C$, $D = 0.5$, $V_{CE} = 400V$, $V_{GE} = 0/15V$, $r_G = 23\Omega$)

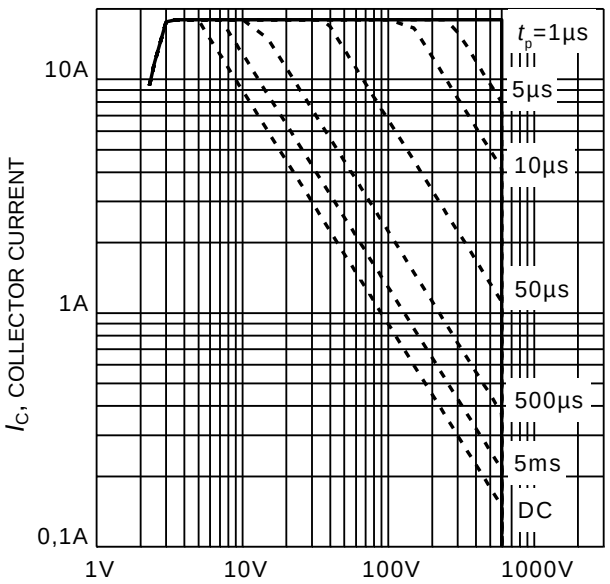


Figure 2. Safe operating area
($D = 0$, $T_C = 25°C$, $T_j \leq 175°C$; V_{GE}=0/15V)

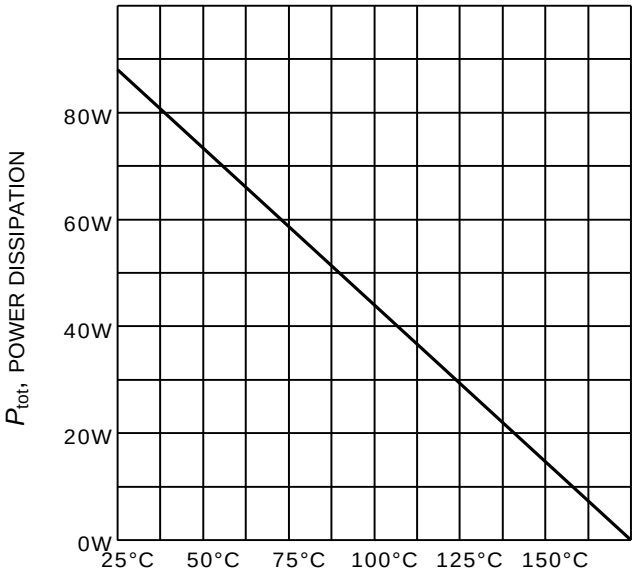


Figure 3. Power dissipation as a function of case temperature
($T_j \leq 175°C$)

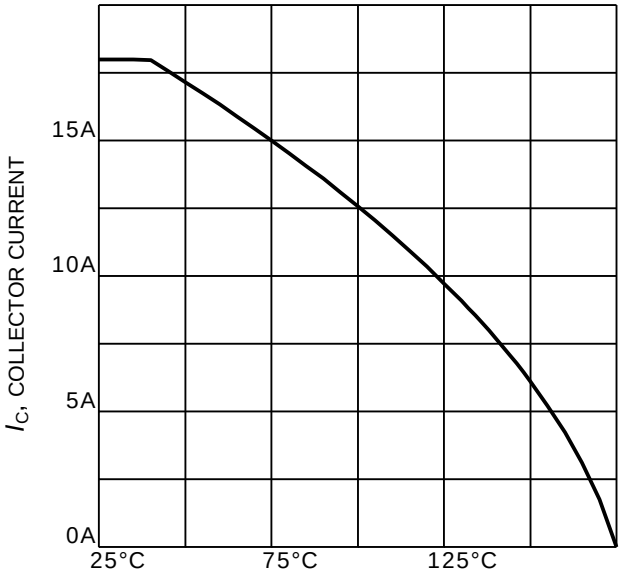


Figure 4. Collector current as a function of case temperature
($V_{GE} \geq 15V$, $T_j \leq 175°C$)

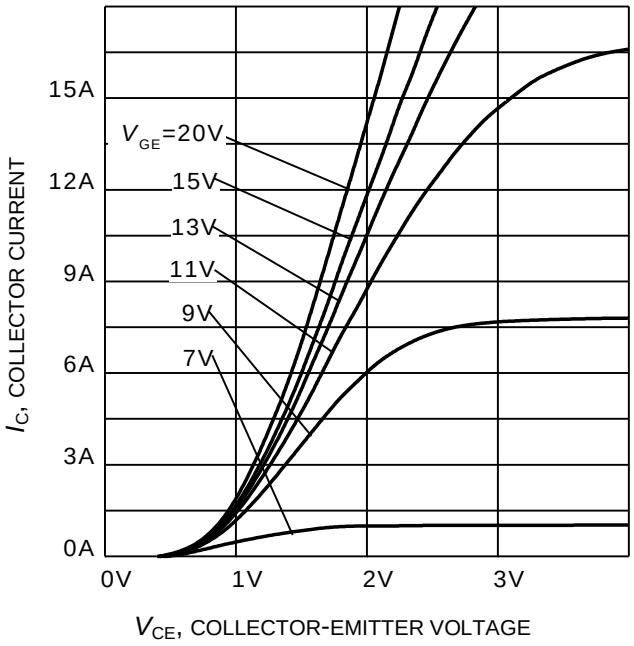


Figure 5. Typical output characteristic
(T_j = 25°C)

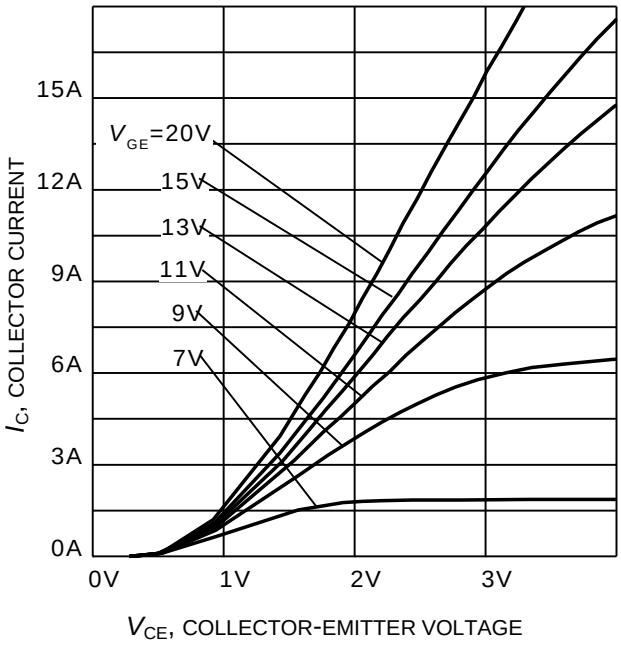


Figure 6. Typical output characteristic
(T_j = 175°C)

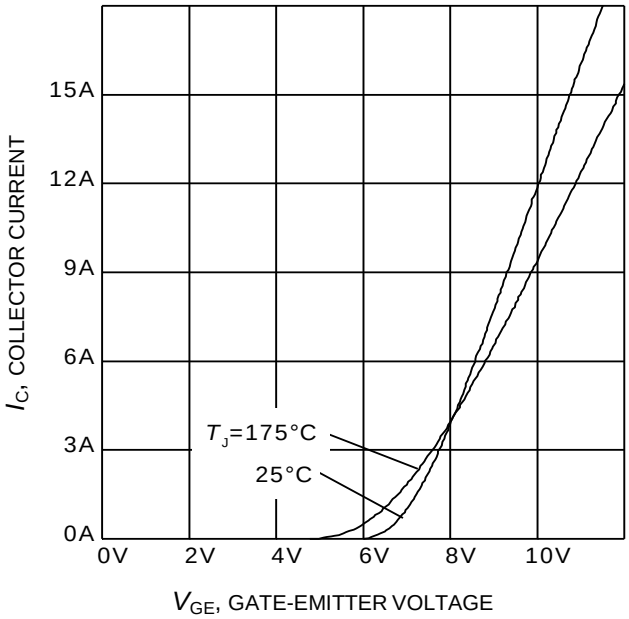


Figure 7. Typical transfer characteristic
(V_{CE}=20V)

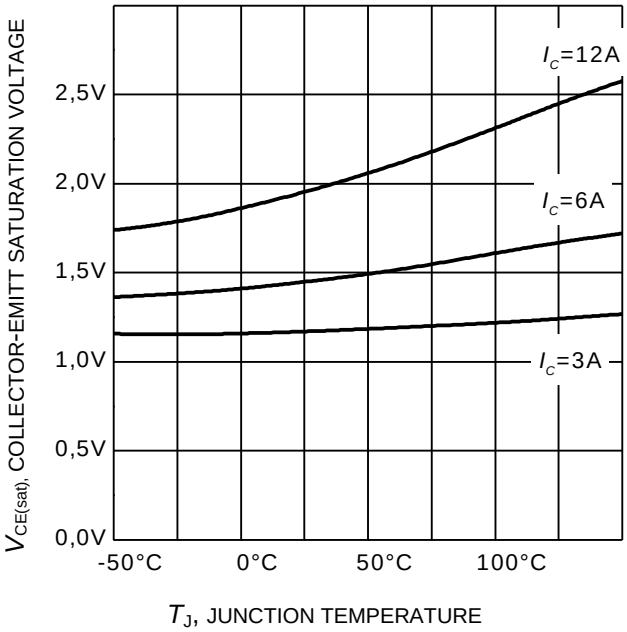


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
(V_{GE} = 15V)

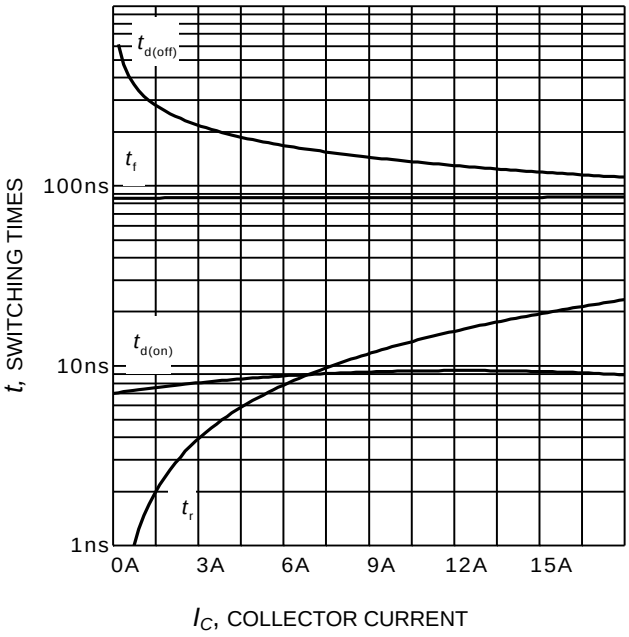


Figure 9. Typical switching times as a function of collector current
(inductive load, T_J=175°C,
V_{CE} = 400V, V_{GE} = 0/15V, r_G = 23Ω,
Dynamic test circuit in Figure E)

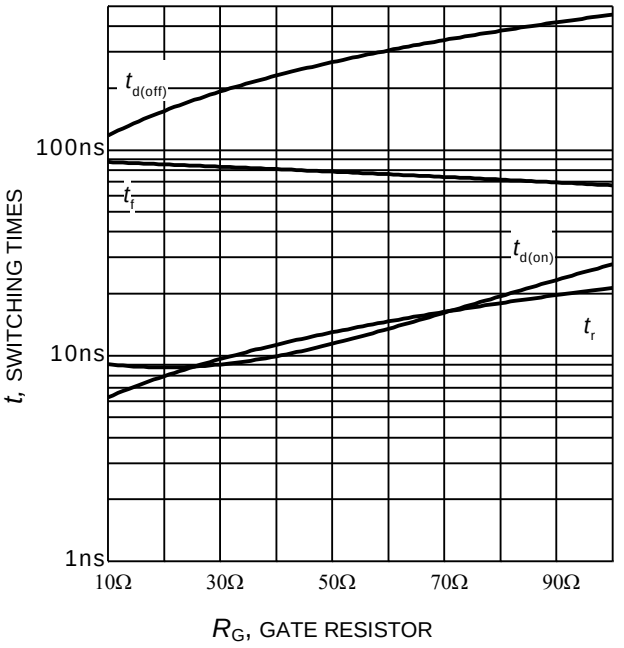


Figure 10. Typical switching times as a function of gate resistor
(inductive load, T_J=175°C,
V_{CE} = 400V, V_{GE} = 0/15V, I_C = 6A,
Dynamic test circuit in Figure E)

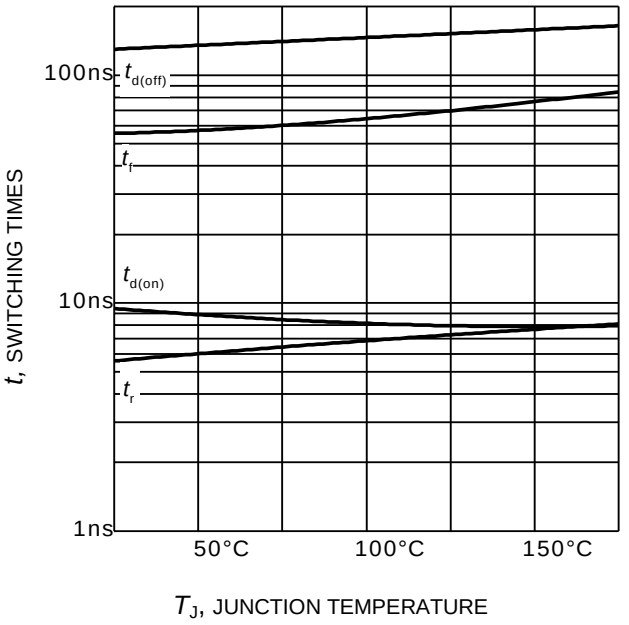


Figure 11. Typical switching times as a function of junction temperature
(inductive load, V_{CE} = 400V,
V_{GE} = 0/15V, I_C = 6A, r_G = 23Ω,
Dynamic test circuit in Figure E)

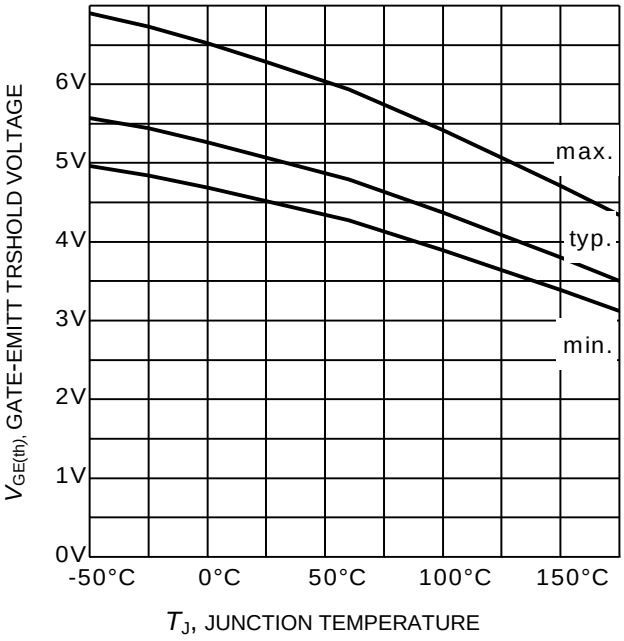


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
(I_C = 0.18mA)

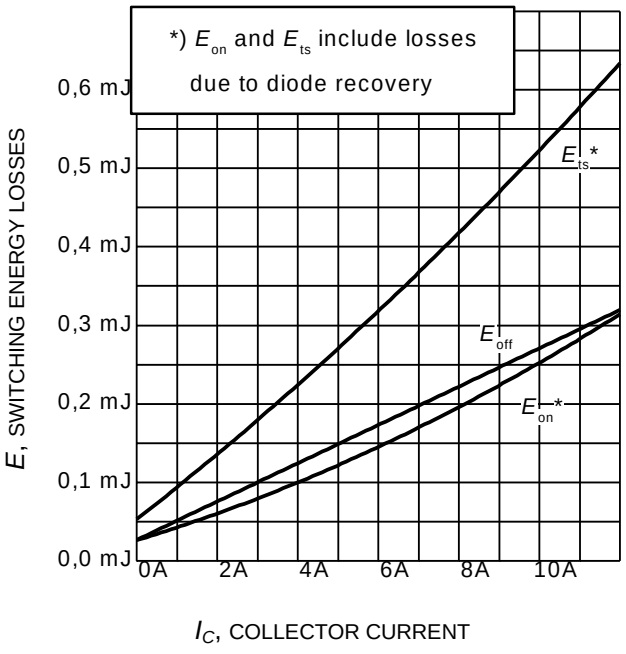


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, T_J=175°C, V_{CE}=400V, V_{GE}=0/15V, r_G=23Ω, Dynamic test circuit in Figure E)

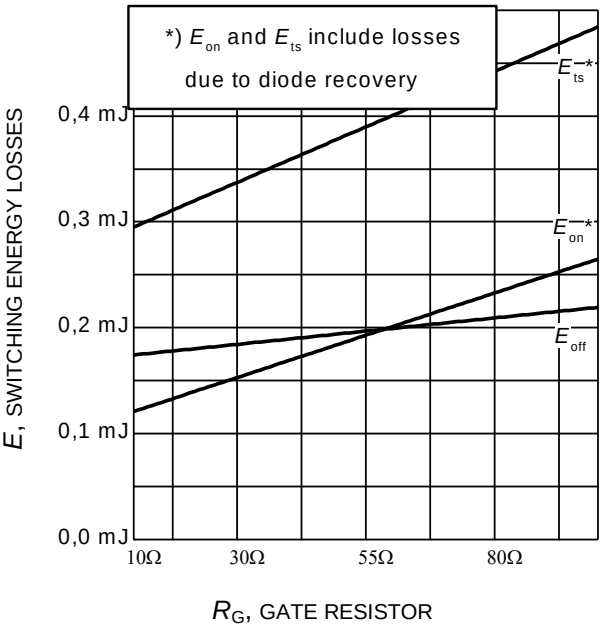


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, T_J=175°C, V_{CE} = 400V, V_{GE} = 0/15V, I_C = 6A, Dynamic test circuit in Figure E)

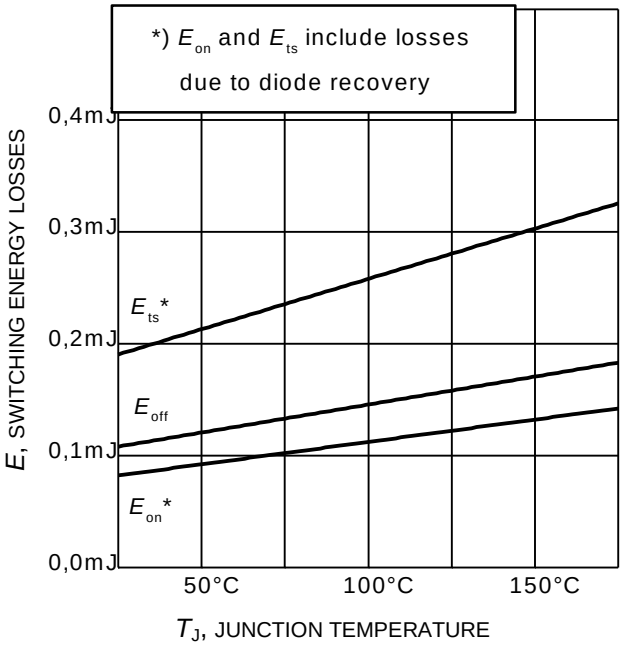


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, V_{CE}=400V, V_{GE} = 0/15V, I_C = 6A, r_G = 23Ω, Dynamic test circuit in Figure E)

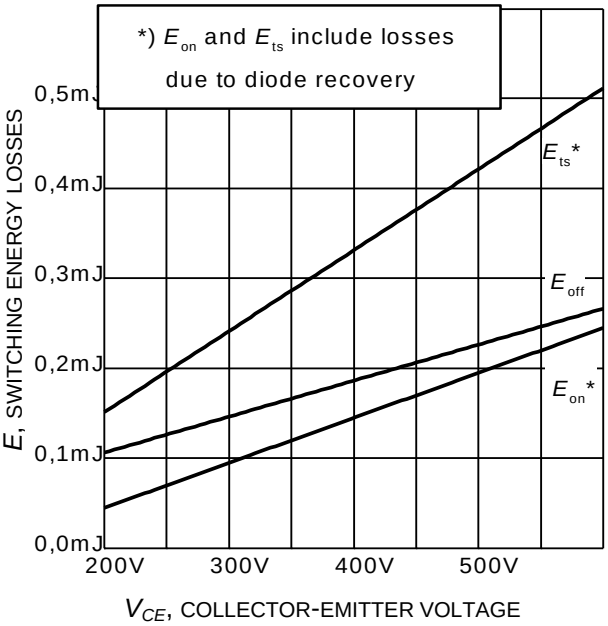


Figure 16. Typical switching energy losses as a function of collector emitter voltage
(inductive load, T_J = 175°C, V_{GE} = 0/15V, I_C = 6A, r_G = 23Ω, Dynamic test circuit in Figure E)

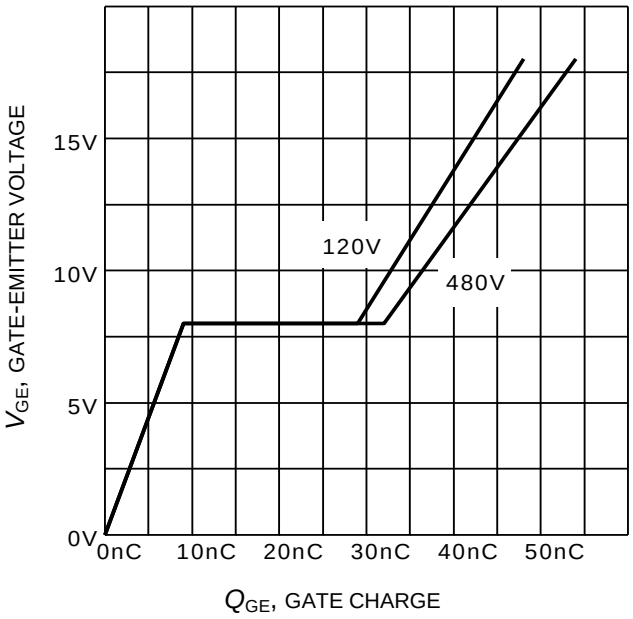


Figure 17. Typical gate charge
(I_C = 6 A)

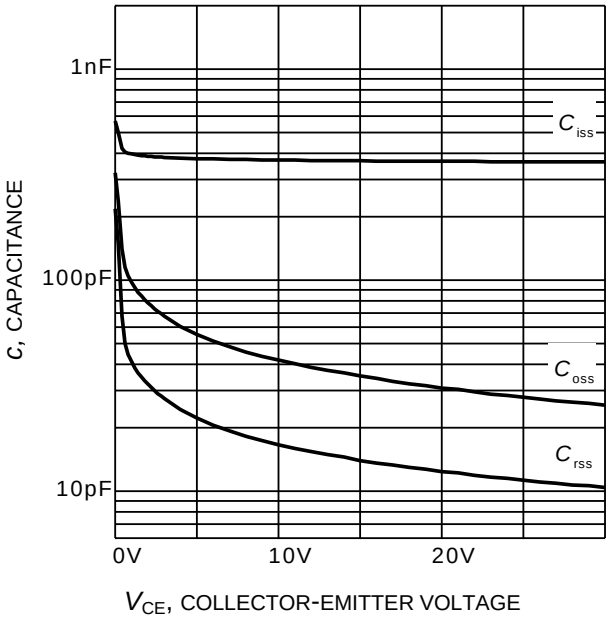


Figure 18. Typical capacitance as a function of collector-emitter voltage
(V_{GE}=0V, f = 1 MHz)

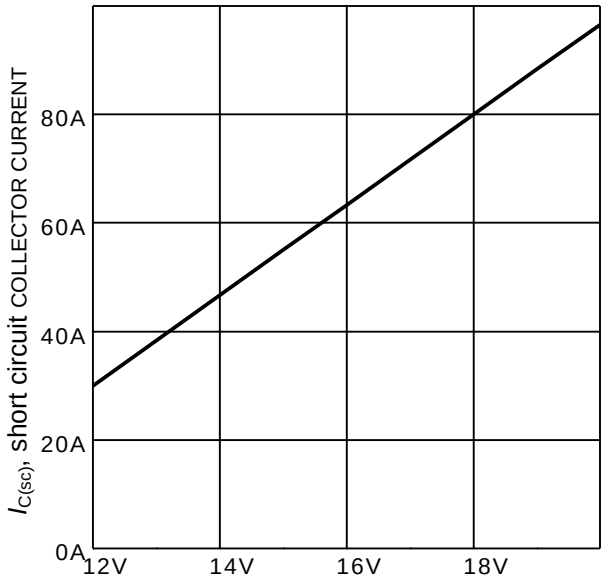


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \le$ 400V, $T_j \le$ 150°C)

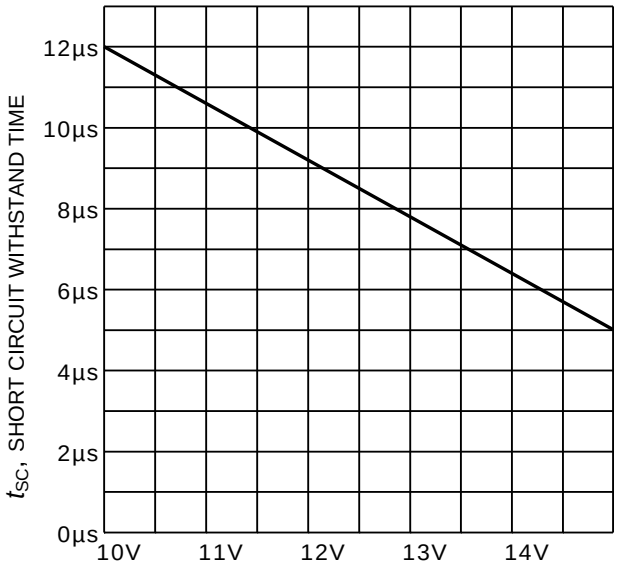


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
(V_{CE}=400V, start at T_J=25°C, T_{Jmax}<150°C)

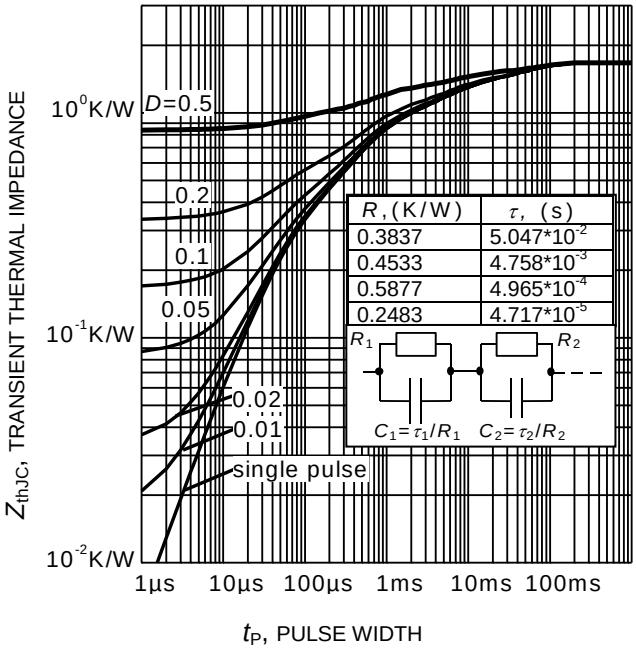
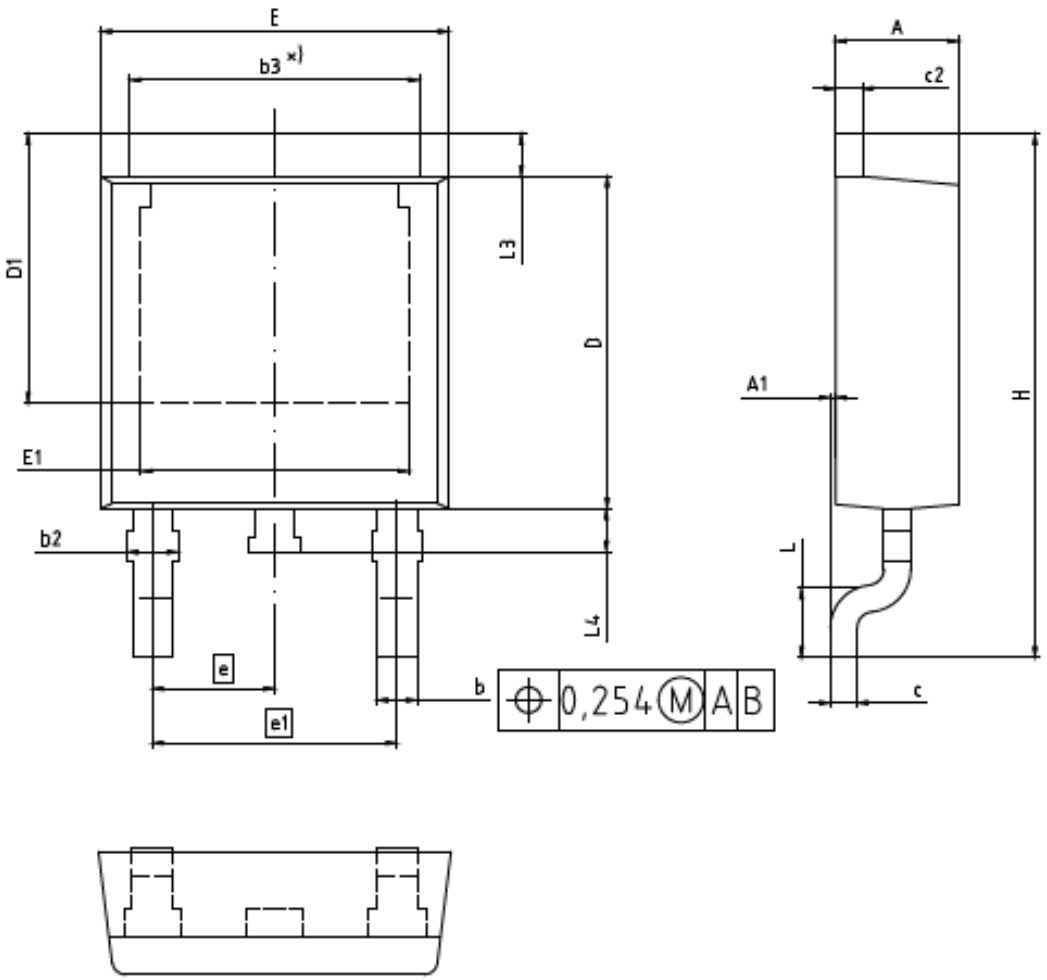


Figure 21. IGBT transient thermal impedance
($D = t_p / T$)

Package Drawing PG-TO252-3



NOTES:
1. ALL DIMENSIONS REFER TO JEDEC STANDARD TO-252 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

DIM	MILLIMETERS	
	MIN	MAX
A	2.16	2.41
A1	0.00	0.15
b	0.64	0.89
b2	0.65	1.15
b3	4.95	5.50
c	0.46	0.61
c2	0.40	0.98
D	5.97	6.22
D1	5.02	5.84
E	6.35	6.73
E1	4.32	5.21
e	2.29 (BSC)	
e1	4.57 (BSC)	
N	3	
H	9.40	10.48
L	1.18	1.78
L3	0.89	1.27
L4	0.51	1.02

DOCUMENT NO.	Z8B00003328
SCALE	0 2.5 0 2.5 5mm
EUROPEAN PROJECTION	
ISSUE DATE	05-02-2016
REVISION	06

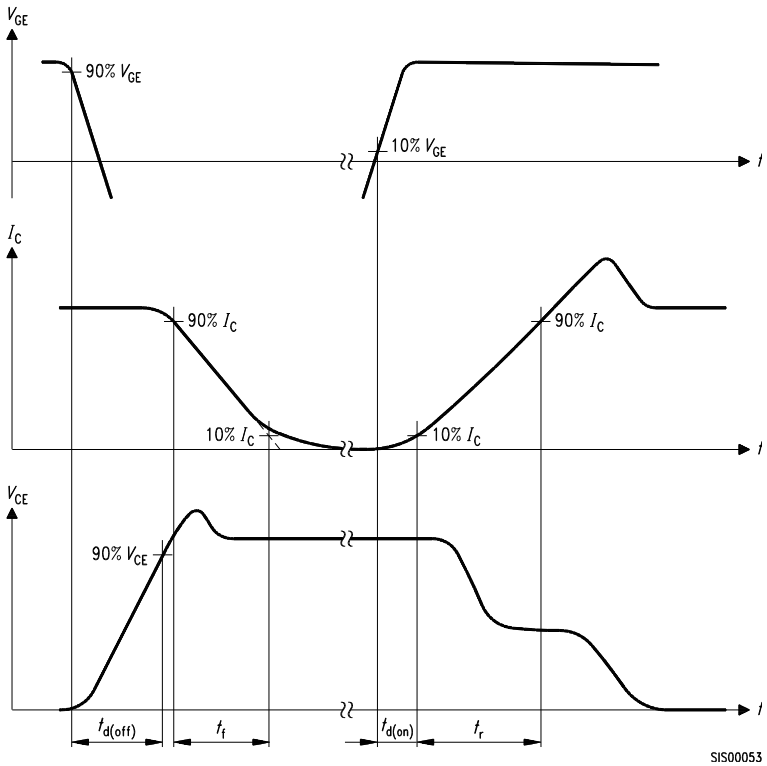


Figure A. Definition of switching times

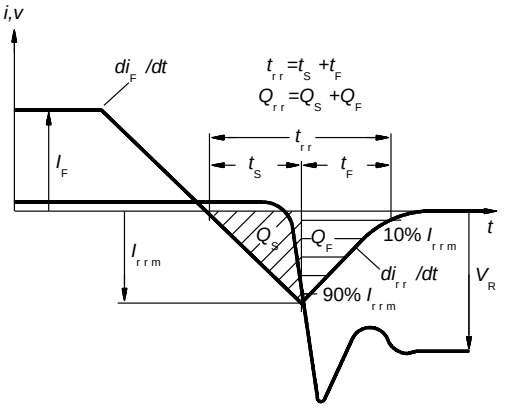


Figure C. Definition of diodes switching characteristics

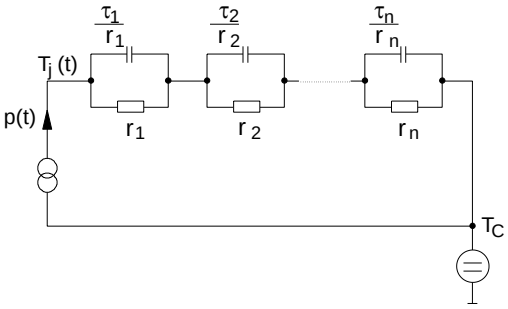


Figure D. Thermal equivalent circuit

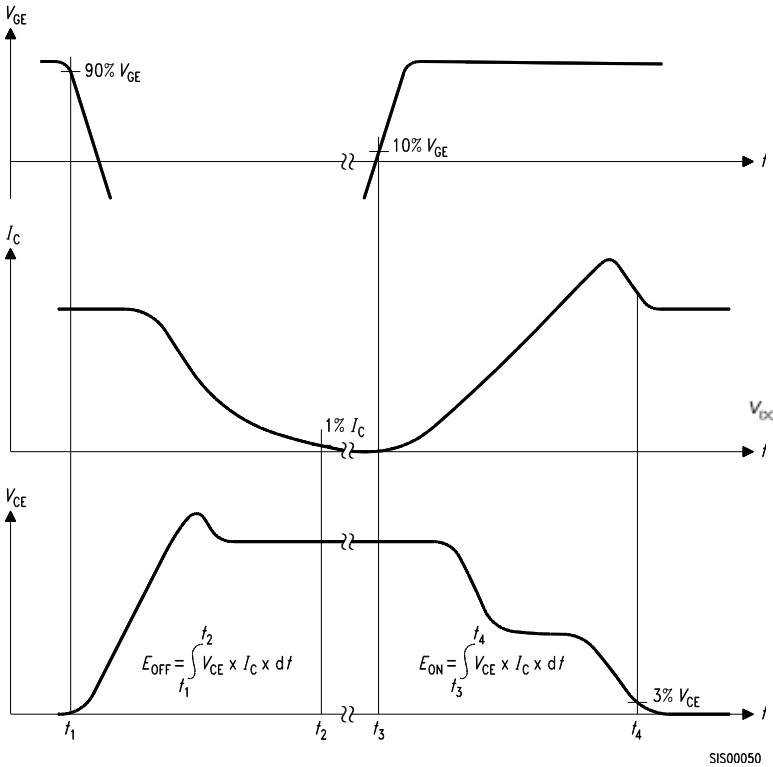


Figure B. Definition of switching losses

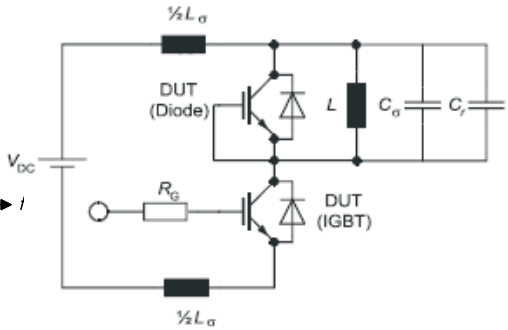


Figure E. Dynamic test circuit
Parasitic inductance L_σ,
Parasitic capacitor C_σ,
Relief capacitor C_r
(only for ZVT switching)

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