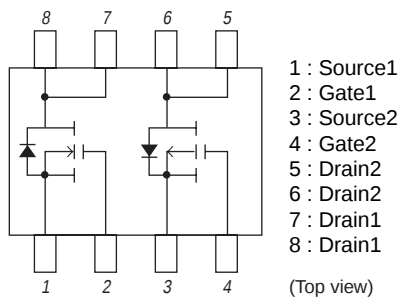


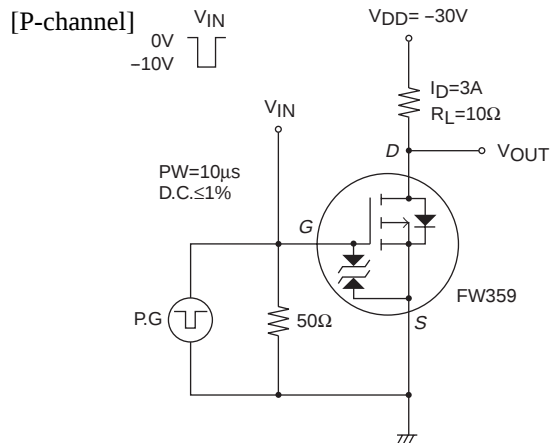
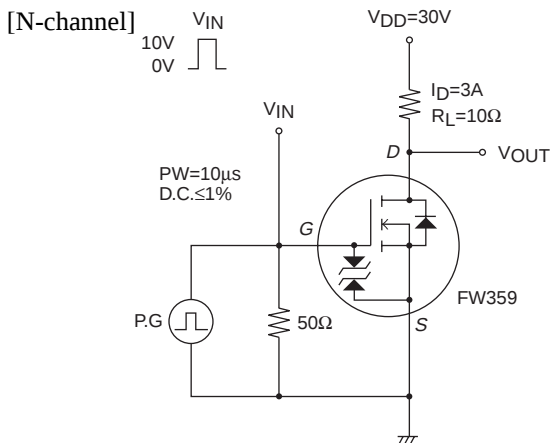
Continued from preceding page.

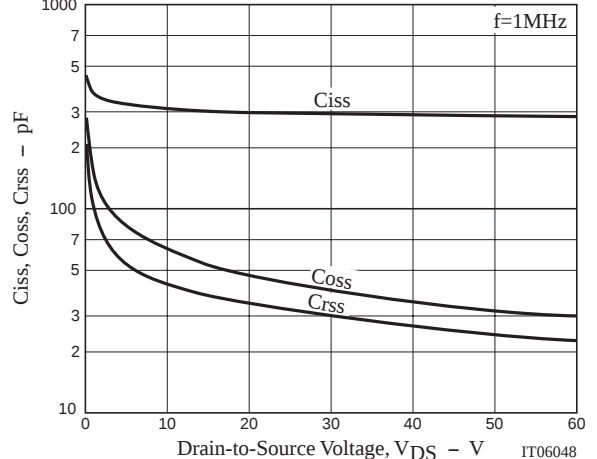
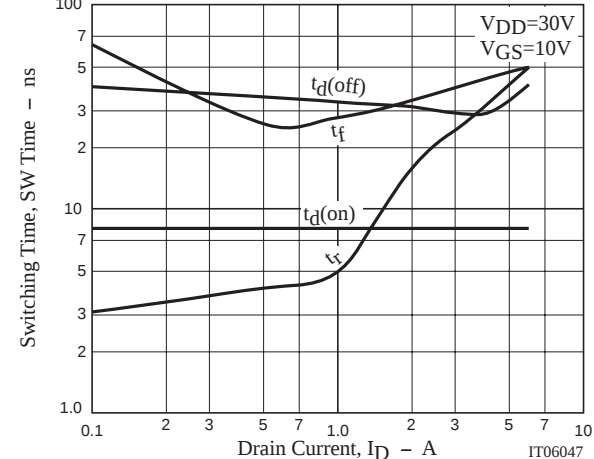
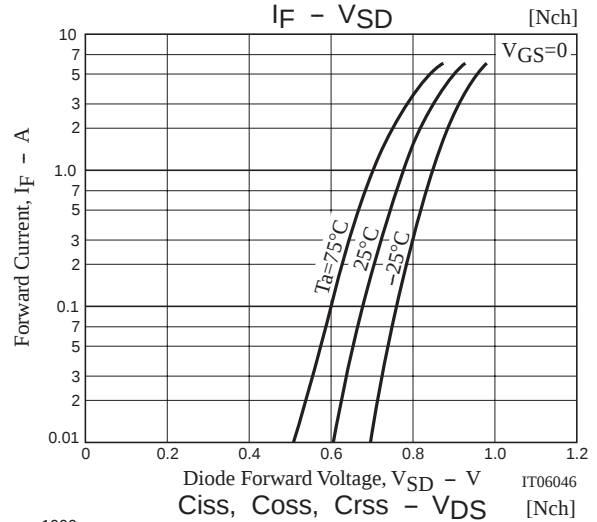
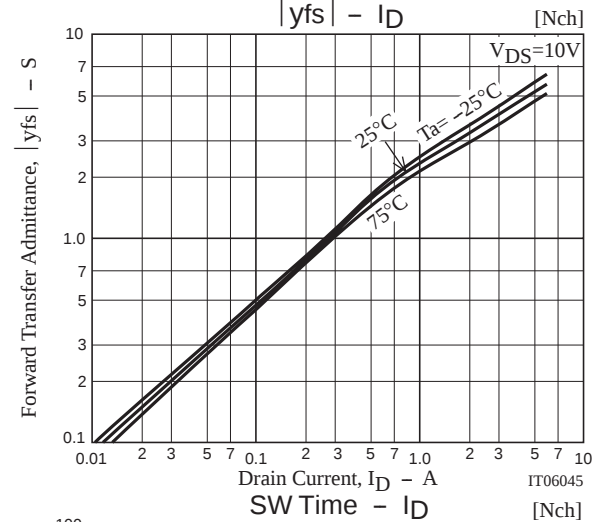
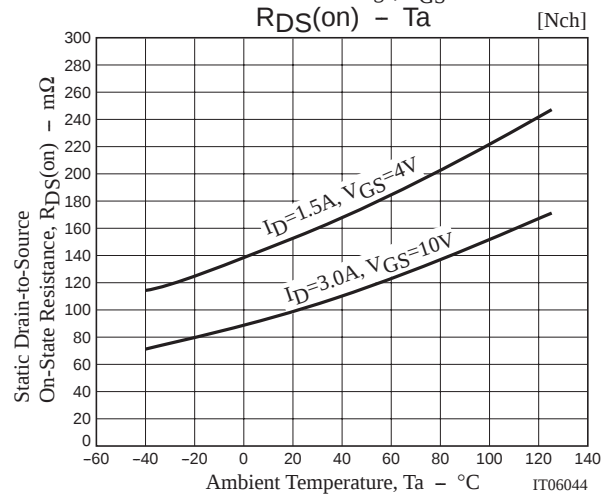
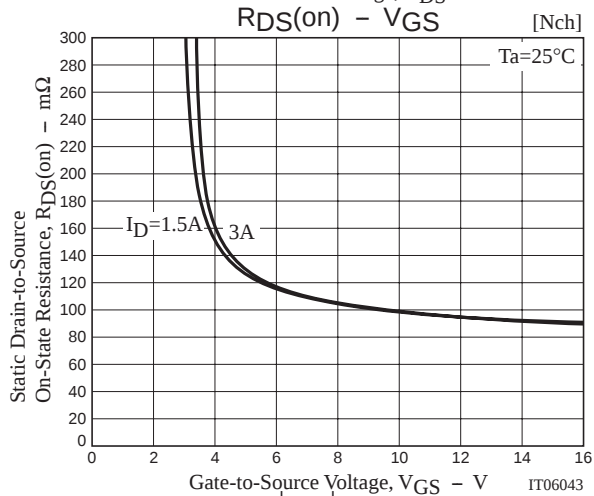
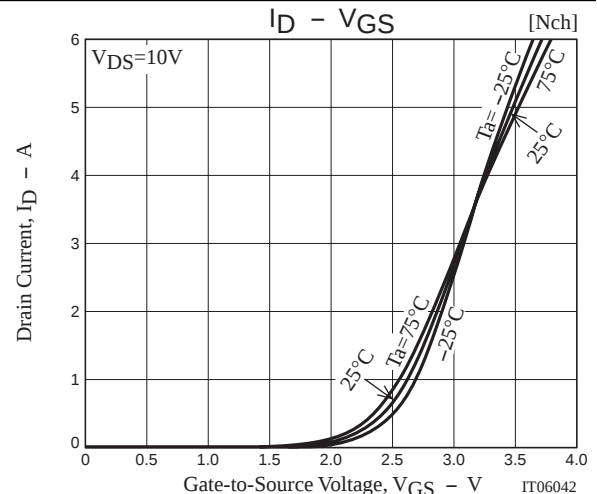
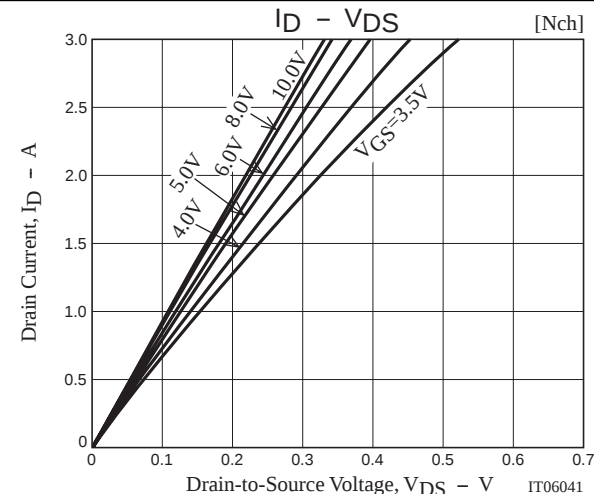
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=3A$	2.8	4		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=3A, V_{GS}=10V$		110	145	$m\Omega$
	$R_{DS(on)2}$	$I_D=1.5A, V_{GS}=4V$		150	215	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz$		300		pF
Output Capacitance	C_{oss}	$V_{DS}=20V, f=1MHz$		54		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=20V, f=1MHz$		34		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		8		ns
Rise Time	t_r	See specified Test Circuit.		23		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		30		ns
Fall Time	t_f	See specified Test Circuit.		40		ns
Total Gate Charge	Q_g	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		7.8		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		2.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		1.7		nC
Diode Forward Voltage	V_{SD}	$I_S=3A, V_{GS}=0$		0.86	1.2	V
[P-channel]						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-1mA, V_{GS}=0$	-60			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-60V, V_{GS}=0$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=-10V, I_D=-1mA$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=-10V, I_D=-3A$	4	5.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=-3A, V_{GS}=-10V$		110	145	$m\Omega$
	$R_{DS(on)2}$	$I_D=-1.5A, V_{GS}=-4V$		145	205	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS}=-20V, f=1MHz$		990		pF
Output Capacitance	C_{oss}	$V_{DS}=-20V, f=1MHz$		110		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=-20V, f=1MHz$		76		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		12		ns
Rise Time	t_r	See specified Test Circuit.		70		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		100		ns
Fall Time	t_f	See specified Test Circuit.		70		ns
Total Gate Charge	Q_g	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A$		22		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A$		4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A$		4		nC
Diode Forward Voltage	V_{SD}	$I_S=-3A, V_{GS}=0$		-0.86	-1.2	V

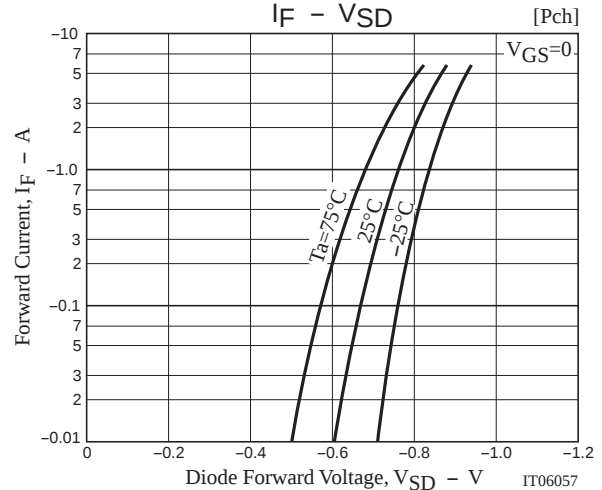
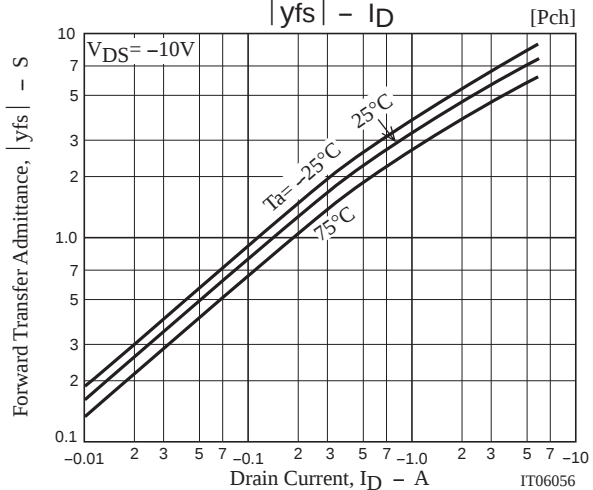
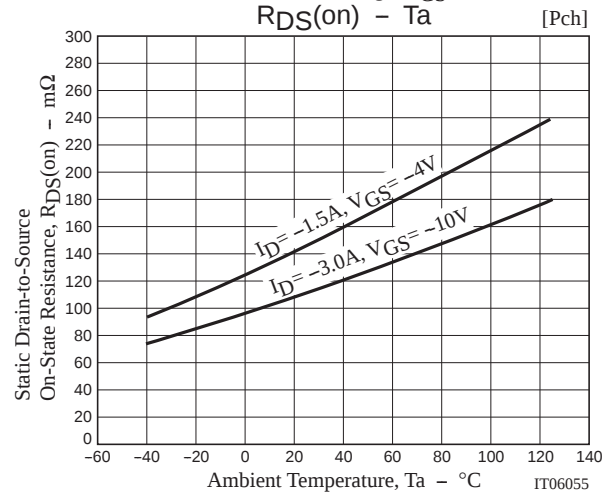
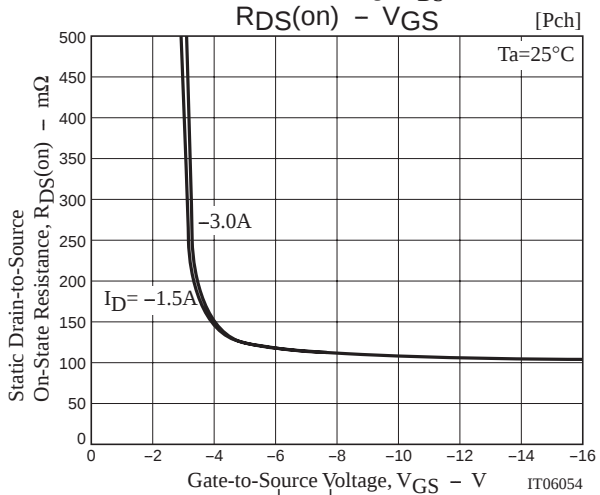
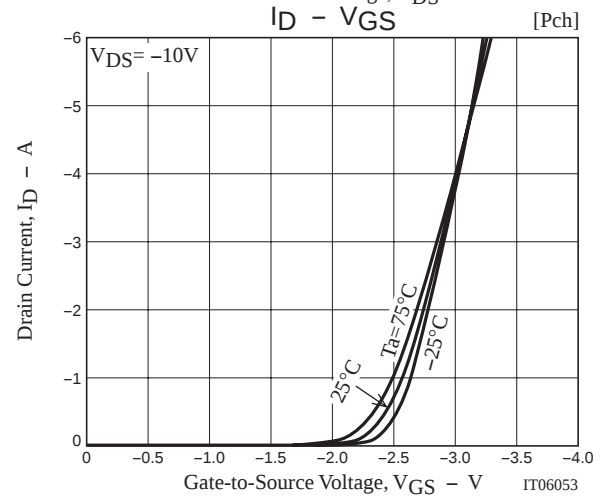
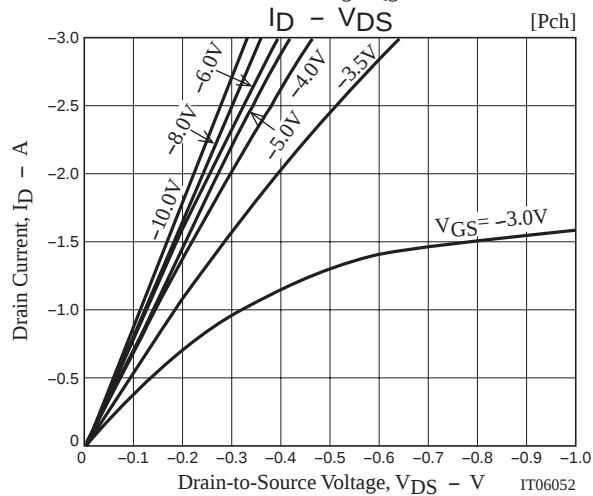
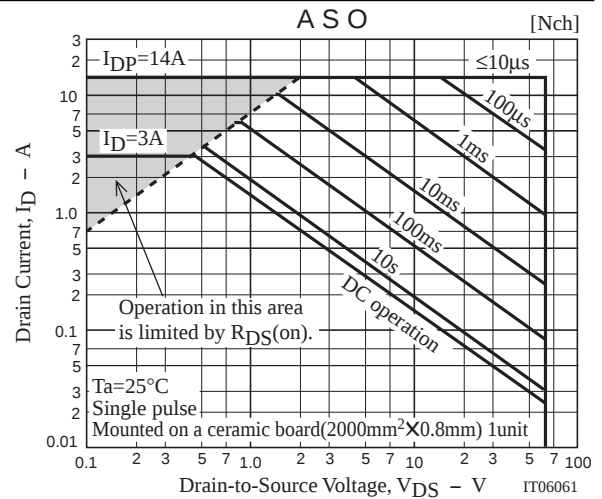
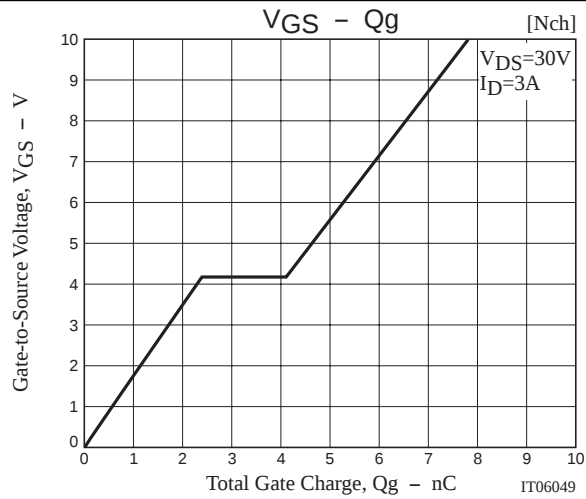
Electrical Connection

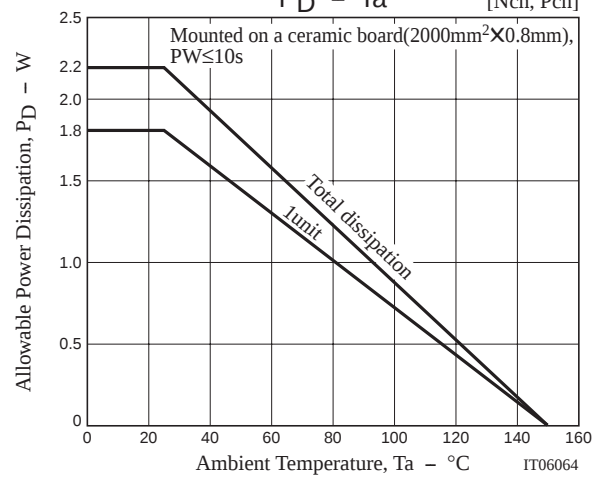
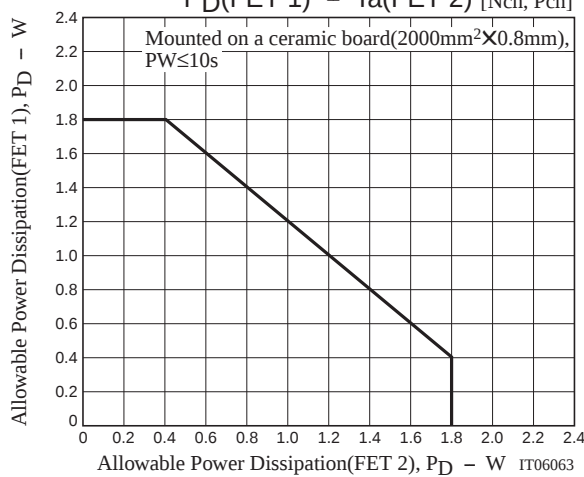
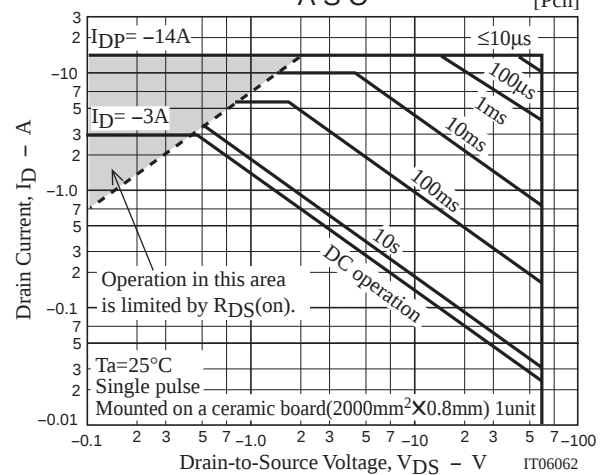
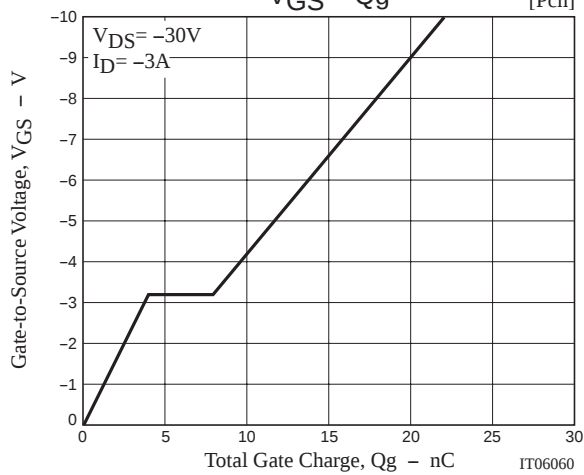
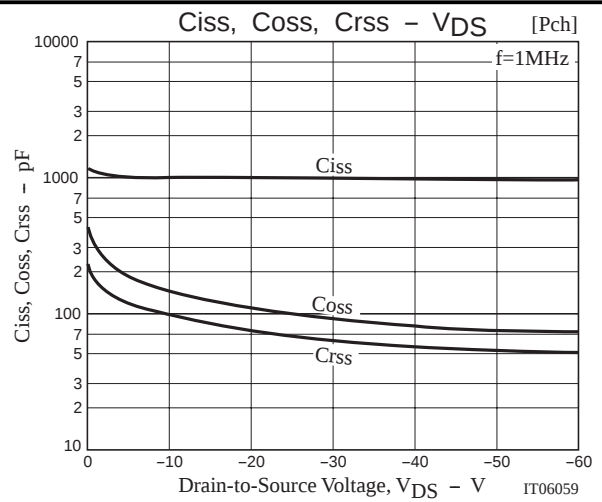
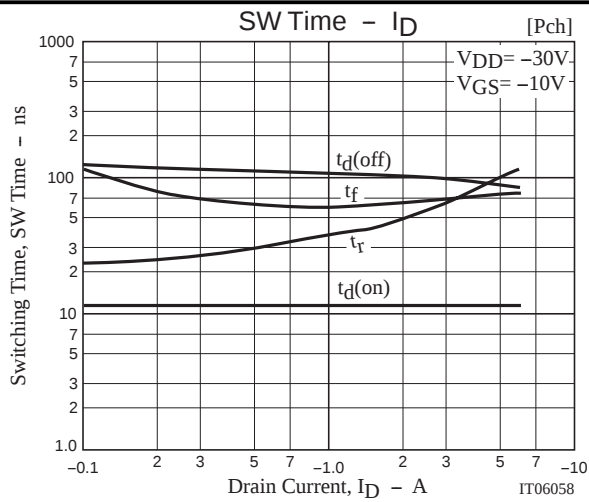


Switching Time Test Circuit









- Specifications of any and all SANYO products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Electric Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO products(including technical data,services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Electric Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of February, 2004. Specifications and information herein are subject to change without notice.