

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-60	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 6) V _{GS} = -10V	I _D	-4.5 -3.6	A
Maximum Body Diode Forward Current (Note 6)	I _S	-2.1	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)	I _{DM}	-19	A
Avalanche Current (Notes 7) L = 0.1mH	I _{AS}	-17.6	A
Avalanche Energy (Notes 7) L = 0.1mH	E _{AS}	15.4	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	80	°C/W
		48	°C/W
Total Power Dissipation (Note 6)	P _D	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	61	°C/W
		37	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	6.4	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-60	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -48V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	100	nA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-1	—	-3	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	86	110	mΩ	V _{GS} = -10V, I _D = -4.5A
		—	98	130		V _{GS} = -4.5V, I _D = -3.5A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.2	V	V _{GS} = 0V, I _S = -1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	1030	—	pF	V _{DS} = -30V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	49.1	—		
Reverse Transfer Capacitance	C _{rss}	—	38.7	—		
Gate Resistance	R _G	—	13.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	—	9.5	—	nC	V _{DS} = -30V, I _D = -5A
Total Gate Charge (V _{GS} = -10V)	Q _g	—	19.4	—		
Gate-Source Charge	Q _{gs}	—	2.3	—		
Gate-Drain Charge	Q _{gd}	—	3.6	—		
Turn-On Delay Time	t _{D(on)}	—	3.7	—	ns	V _{GS} = -10V, V _{DS} = -30V, R _{GEN} = 6Ω, I _D = -5A
Turn-On Rise Time	t _r	—	6.3	—		
Turn-Off Delay Time	t _{D(off)}	—	58.7	—		
Turn-Off Fall Time	t _f	—	26.1	—		
Body Diode Reverse Recovery Time	t _{rr}	—	14.85	—	ns	I _S = -5A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{rr}	—	8.8	—	nC	I _S = -5A, dI/dt = 100A/μs

- Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. UIS in production with L = 0.1mH, starting T_A = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

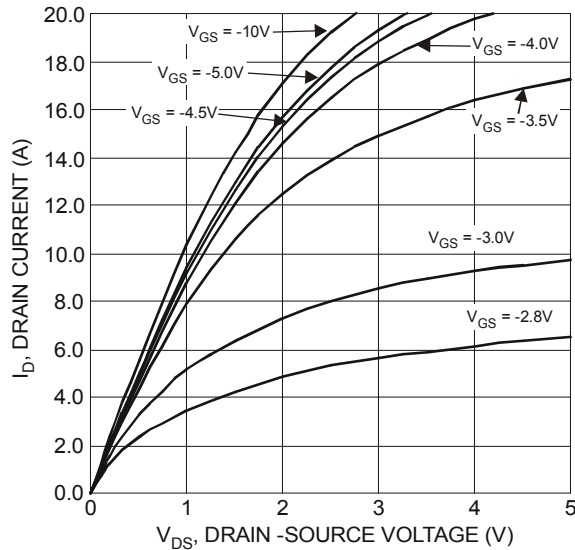


Figure 1 Typical Output Characteristics

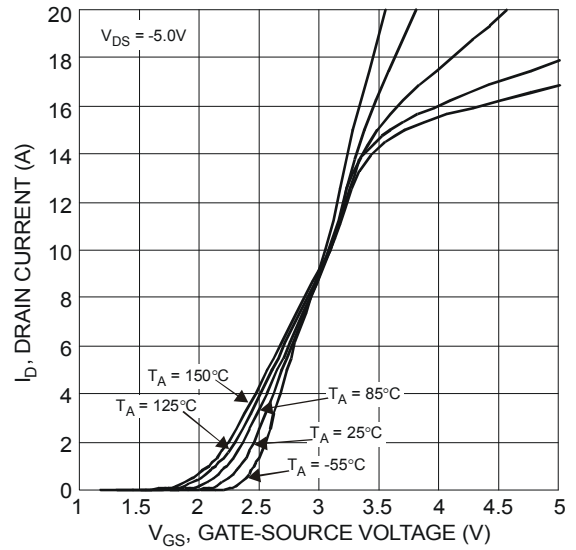


Figure 2 Typical Transfer Characteristics

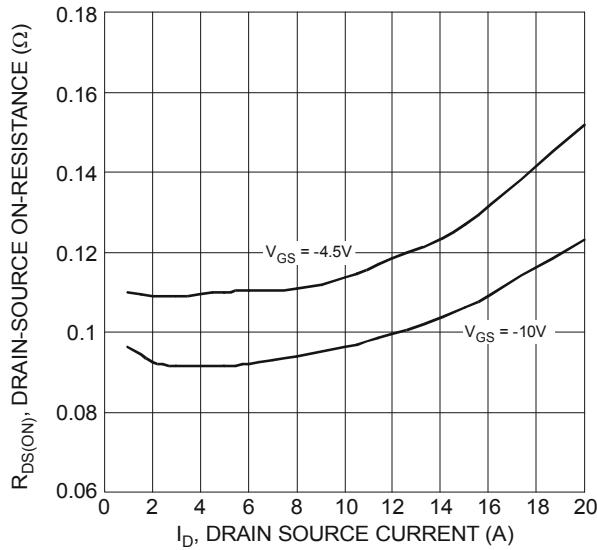


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

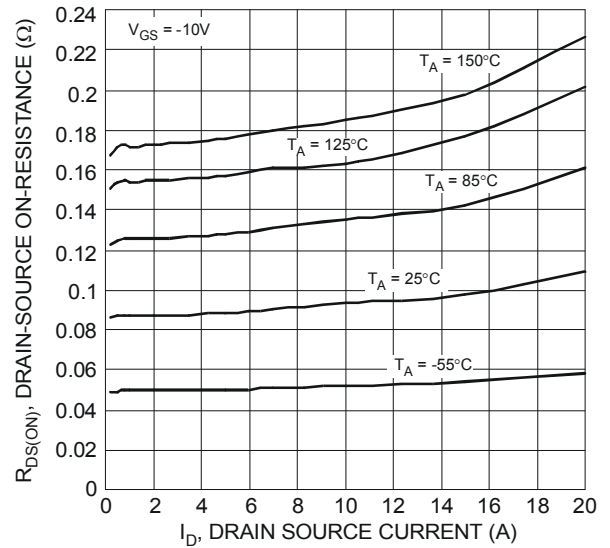


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

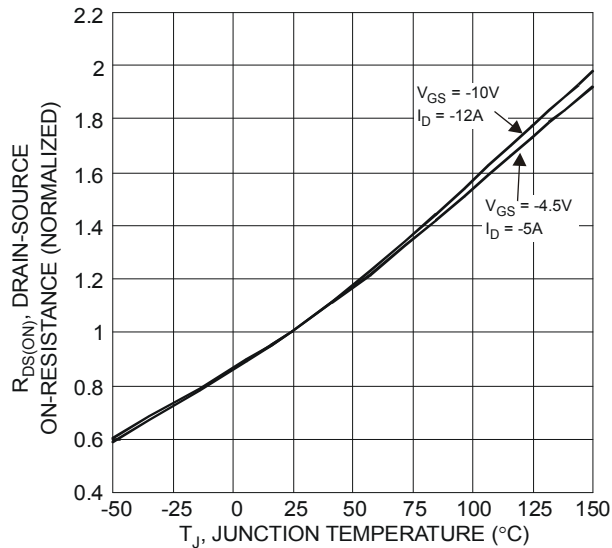


Figure 5 On-Resistance Variation with Temperature

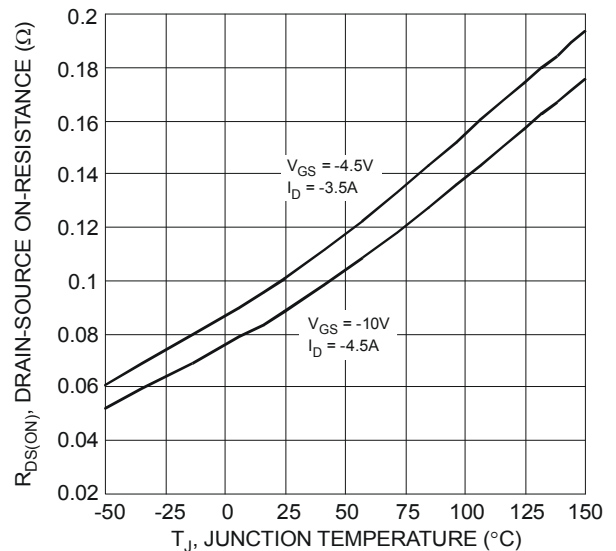


Figure 6 On-Resistance Variation with Temperature

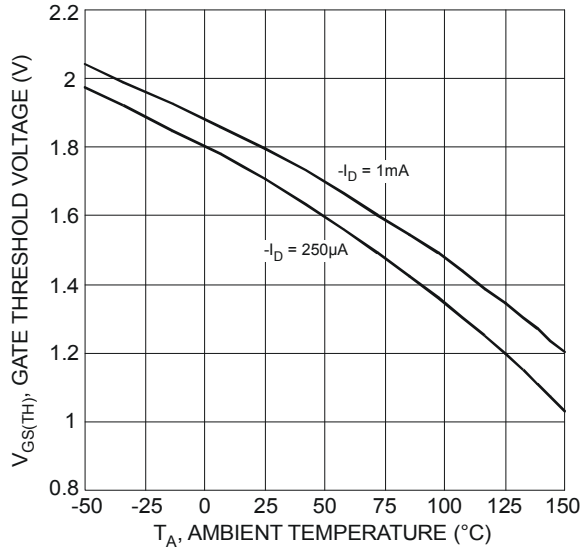


Figure 7 Gate Threshold Variation vs. Ambient Temperature

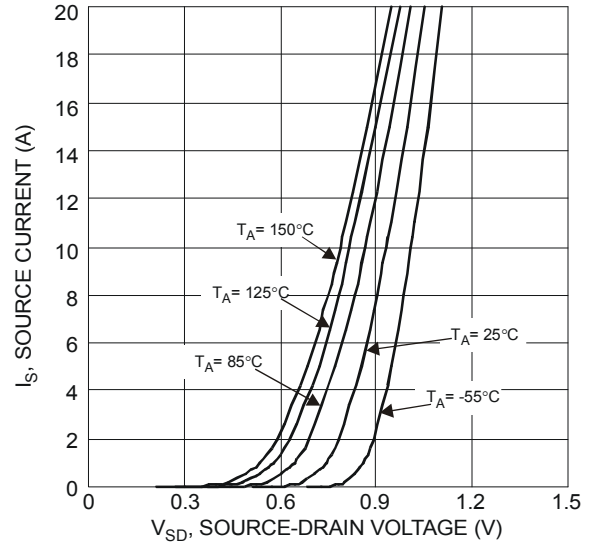


Figure 8 Diode Forward Voltage vs. Current

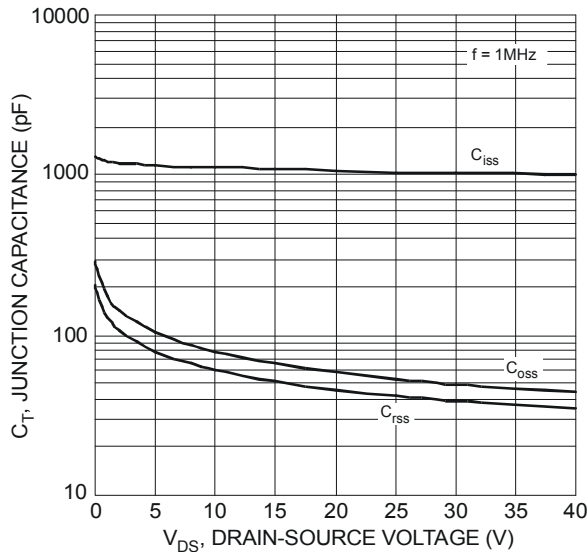


Figure 9 typical Junction Capacitance

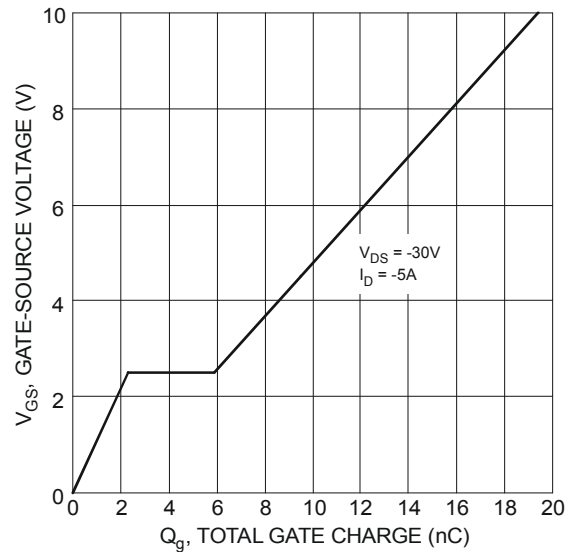


Figure 10 Gate-Charge Characteristics

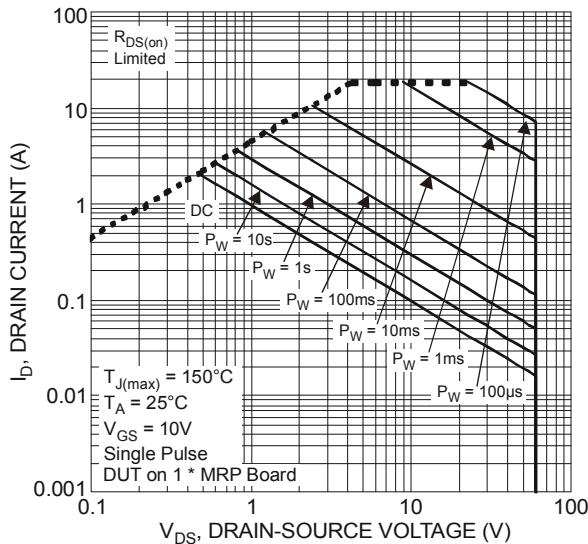


Figure 11 SOA, Safe Operation Area

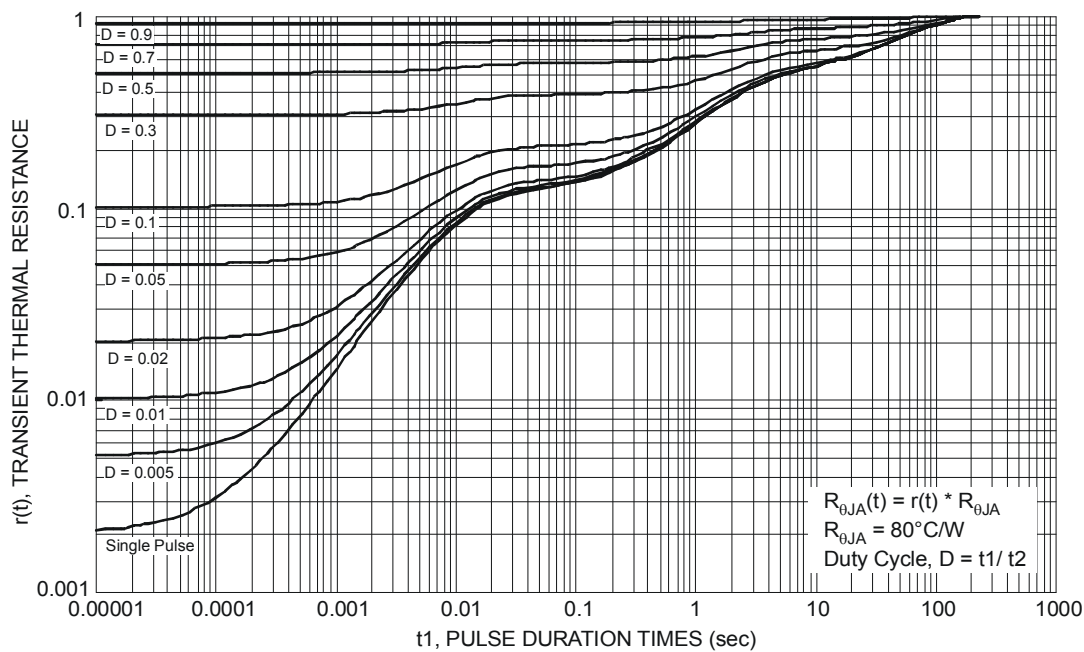
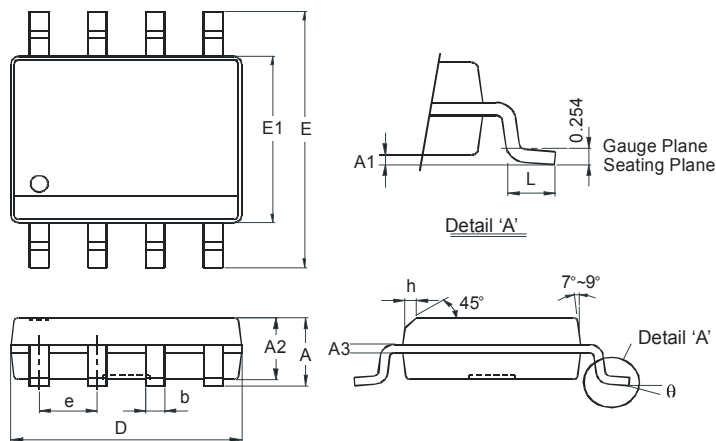


Figure 12 Transient Thermal Resistance

Package Outline Dimensions

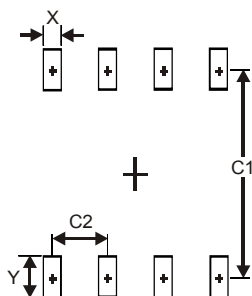
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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