ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter		Symbol	Limit	Unit
Reference to GND	V+		–0.3 to 5.0	V
	IN, COM, NC, NO[a]		–0.3 to (V+ + 0.3)	
Current (Any terminal except NO, NC or COM)			30	mA
Continuous Current (NO, NC, or COM)			±300	
Peak Current (Pulsed at 1 ms, 10 % duty cycle)			±500	
Storage Temperature (D Suffix)			–65 to 150	°C
Package Solder Reflow Conditions[d]	16-Pin QFN (3 x 3 mm)		250	
Power Dissipation (Packages)[b]	QFN-16[c]		1385	mW

Notes
a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
b. All leads welded or soldered to PC Board.
c. Derate 17.3 mW/°C above 70°C
d. Manual soldering with iron is not recommended for leadless components. The QFN is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper lip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

SPECIFICATIONS (V+ = 1.8 V)

Parameter	Symbol	Test Condition Otherwise Unless Specified V+ = 1.8 V, V_{IN} = 0.4 or 1.1 V[e]	Temp[a]	Limits –40 to 85°C Min[b]	Typ[c]	Max[b]	Unit
Analog Switch							
Analog Signal Range[d]	V_{NO}, V_{NC}, V_{COM}		Full	0		V+	V
On-Resistance	r_{ON}	V+ = 1.8 V, V_{COM} = 0.2 V, I_{NO}, I_{NC} = 100 mA	Room		0.35	1.3	Ω
		V+ = 1.8 V, V_{COM} = 0.9 V, I_{NO}, I_{NC} = 100 mA			0.45		
			Full			1.4	
Digital Control							
Input High Voltage	V_{INH}		Full	1.1			V
Input Low Voltage	V_{INL}		Full			0.4	
Input Capacitance	C_{in}		Full		6		pF
Input Current	I_{INL} or I_{INH}	V_{IN} = 0 or V+	Full	–1		1	µA
Dynamic Characteristics							
Turn-On Time	t_{ON}	V_{NO} or V_{NC} = 1.5 V, R_L = 50 Ω, C_L = 35 pF	Romm Full		62	94 97	ns
Turn-Off Time	t_{OFF}		Room Full		24	52 55	
Break-Before-Make Time	t_d		Full	8			
Charge Injection[d]	Q_{INJ}	C_L = 1 nF, V_{GEN} = 0 V, R_{GEN} = 0 Ω	Room		66		pC
Off-Isolation[d]	OIRR	R_L = 50 Ω, C_L = 5 pF, f = 100 kHz	Room		–74		dB
Crosstalk[d]	X_{TALK}		Room		–74		
N_O, N_C Off Capacitance[d]	$C_{NO(off)}$	V_{IN} = 0 or V+, f = 1 MHz	Room		108		pF
	$C_{NC(off)}$		Room		108		
Channel-On Capacitance[d]	$C_{NO(on)}$		Room		240		
	$C_{NC(on)}$		Room		240		
Power Supply							
Power Supply Current	I+	V_{IN} = 0 or V+	Full			1.0	µA

SPECIFICATIONS (V+ = 3 V)

Parameter	Symbol	Test Condition Otherwise Unless Specified V+ = 3 V, ±10 %, V_{IN} = 0.5 or 1.4 V[e]	Temp[a]	Limits –40 to 85°C Min[b]	Typ[c]	Max[b]	Unit
Analog Switch							
Analog Signal Range[d]	V_{NO}, V_{NC}, V_{COM}		Full	0		V+	V
On-Resistance	r_{ON}	V+ = 2.7 V, V_{COM} = 0.2 V, I_{NO}, I_{NC} = 100 mA	Room		0.3	0.45	Ω
		V+ = 2.7 V, V_{COM} = 1.5 V, I_{NO}, I_{NC} = 100 mA	Room		0.25		
			Full			0.55	
r_{ON} Flatness[d]	r_{ON} Flatness	V+ = 2.7 V, V_{COM} = 0 to V+, I_{NO}, I_{NC} = 100 mA	Room		0.07	0.15	
r_{ON} Match[d]	Δr_{ON}		Room		0.05		
Switch Off Leakage Current	$I_{NO(off)}$, $I_{NC(offF)}$	V+ = 3.3 V, V_{NO}, V_{NC} = 0.3 V / 3.0 V, V_{COM} = 3.0 V / 0.3 V	Room Full	–1 –10		1 10	nA
	$I_{COM(off)}$		Room Full	–1 –10		1 10	
Channel-On Leakage Current	$I_{COM(on)}$	V+ = 3.3 V, V_{NO}, V_{NC} = V_{COM} = 0.3 V / 3.0 V	Room Full	–1 –10		1 10	
Digital Control							
Input High Voltage	V_{INH}		Full	1.4			V
Input Low Voltage	V_{INL}		Full			0.5	
Input Capacitance	C_{in}		Full		6		pF
Input Current	I_{INL} or I_{INH}	V_{IN} = 0 or V+	Full	–1		1	µA
Dynamic Characteristics							
Turn-On Time	t_{ON}	V_{NO} or V_{NC} = 1.5 V, R_L = 50 Ω, C_L = 35 pF	Romm Full		28	57 60	ns
Turn-Off Time	t_{OFF}		Room Full		17	45 47	
Break-Before-Make Time	t_d		Full	1			
Charge Injection[d]	Q_{INJ}	C_L = 1 nF, V_{GEN} = 0 V, R_{GEN} = 0 Ω	Room		160		pC
Off-Isolation[d]	OIRR	R_L = 50 Ω, C_L = 5 pF, f = 100 kHz	Room		–75		dB
Crosstalk[d]	X_{TALK}		Room		–75		
N_O, N_C Off Capacitance[d]	$C_{NO(off)}$	V_{IN} = 0 or V+, f = 1 MHz	Room		102		pF
	$C_{NC(off)}$		Room		102		
Channel-On Capacitance[d]	$C_{NO(on)}$		Room		234		
	$C_{NC(on)}$		Room		234		
Power Supply							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	V_{IN} = 0 or V+	Full			1.0	µA

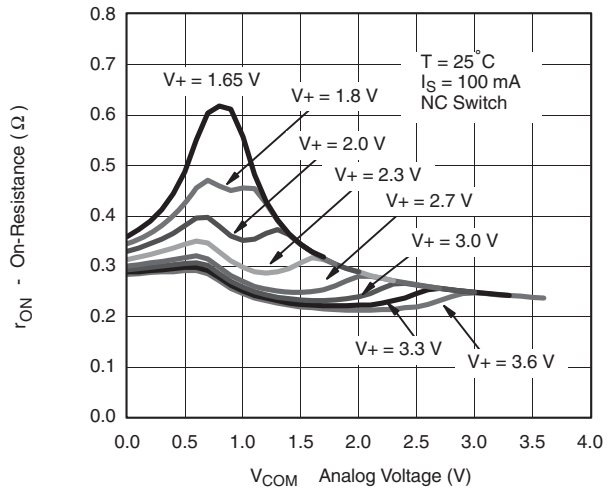
SPECIFICATIONS (V+ = 4.3 V)

Parameter	Symbol	Test Condition Otherwise Unless Specified V+ = 4.3 V, V_{IN} = 0.5 or 1.6 V[e]	Temp[a]	Limits –40 to 85°C Min[b]	Typ[c]	Max[b]	Unit
Analog Switch							
Analog Signal Range[d]	V_{NO}, V_{NC}, V_{COM}		Full	0		V+	V
On-Resistance	r_{ON}	V+ = 4.3 V, V_{COM} = 0.5 V, I_{NO}, I_{NC} = 100 mA	Room		0.29	0.43	Ω
		V+ = 4.3 V, V_{COM} = 2.1 V, I_{NO}, I_{NC} = 100 mA			0.21		
			Full			0.53	
r_{ON} Flatness[d]	r_{ON} Flatness	V+ = 4.3 V, V_{COM} = 0 to V+, I_{NO}, I_{NC} = 100 mA	Room		0.07	0.15	
r_{ON} Match[d]	Δr_{ON}		Room		0.05		
Switch Off Leakage Current[d]	$I_{NO(off)}$, $I_{NC(offF)}$	V+ = 4.3 V, V_{NO}, V_{NC} = 0.3 V / 4.0 V, V_{COM} = 4.0 V / 0.3 V	Room Full	–10 –100		10 100	nA
	$I_{COM(off)}$		Room Full	–10 –100		10 100	
Channel-On Leakage Current[d]	$I_{COM(on)}$	V+ = 4.3 V, V_{NO}, V_{NC} = V_{COM} = 3.0 V / 4.0 V	Room Full	–10 –100		10 100	
Digital Control							
Input High Voltage	V_{INH}		Full	1.6			V
Input Low Voltage	V_{INL}		Full			0.5	
Input Capacitance	C_{in}		Full		6		pF
Input Current	I_{INL} or I_{INH}	V_{IN} = 0 or V+	Full	–1		1	µA
Dynamic Characteristics							
Charge Injection[d]	Q_{INJ}	C_L = 1 nF, V_{GEN} = 0 V, R_{GEN} = 0 Ω	Room		320		pC
Off-Isolation[d]	OIRR	R_L = 50 Ω, C_L = 5 pF, f = 100 kHz	Room		–73		dB
Crosstalk[d]	X_{TALK}		Room		–73		
N_O, N_C Off Capacitance[d]	$C_{NO(off)}$	V_{IN} = 0 or V+, f = 1 MHz	Room		100		pF
	$C_{NC(off)}$		Room		100		
Channel-On Capacitance[d]	$C_{NO(on)}$		Room		230		
	$C_{NC(on)}$		Room		230		
Power Supply							
Power Supply Range	V+					4.3	V
Power Supply Current	I+	V_{IN} = 0 or V+	Full			1.0	µA

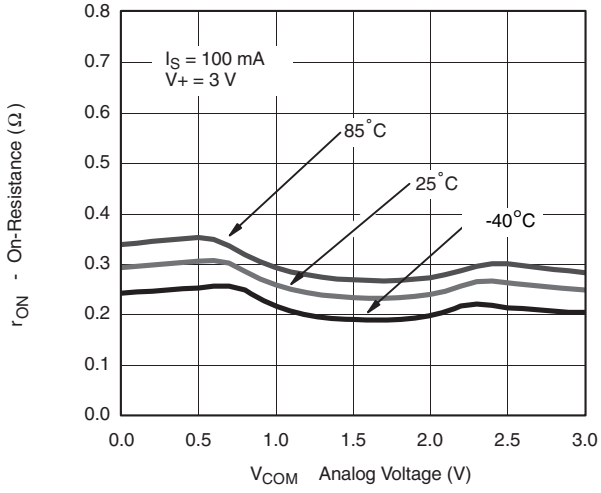
Notes
a. Room = 25°C, Full = as determined by the operating suffix.
b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
c. Typical values are for design aid only, not guaranteed nor subject to production testing.
d. Guarantee by design, not subjected to production test.
e. V_{IN} = input voltage to perform proper function.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

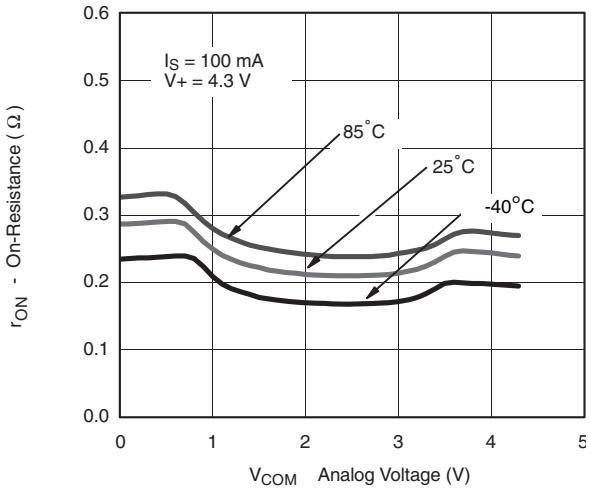
TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



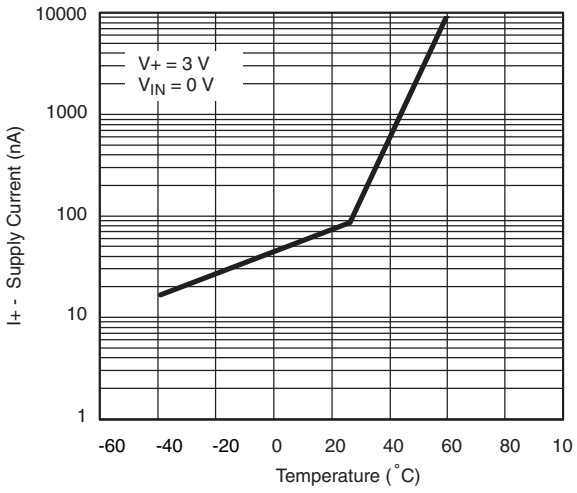
r_{ON} vs. V_{COM} and Supply Voltage



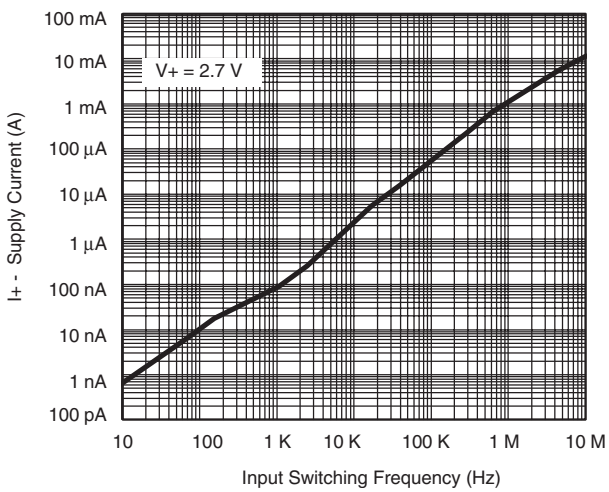
r_{ON} vs. Analog Voltage and Temperature



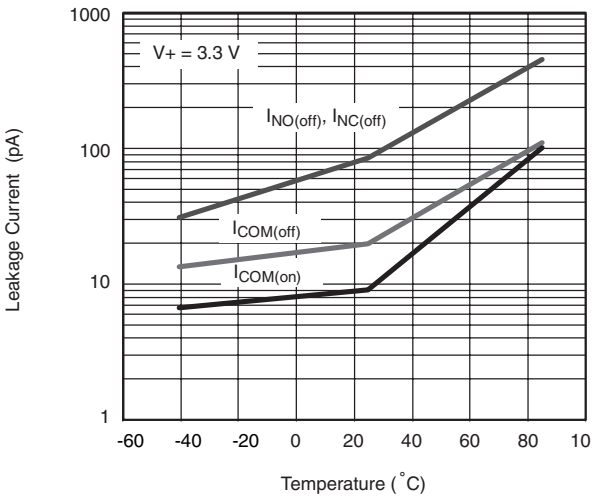
r_{ON} vs. Analog Voltage and Temperature



Supply Current vs. Temperature

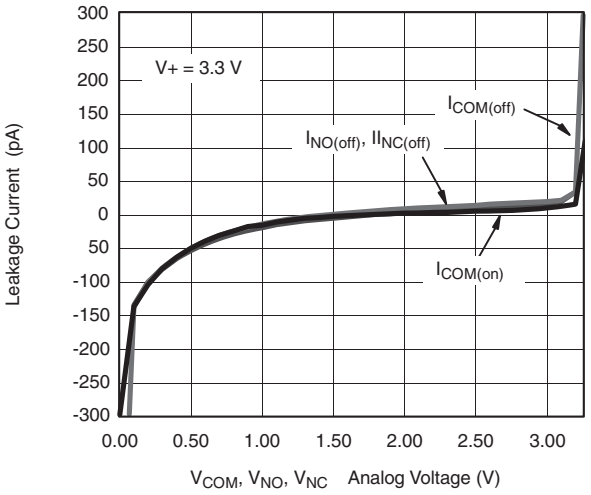


Supply Current vs. Input Switching Frequency

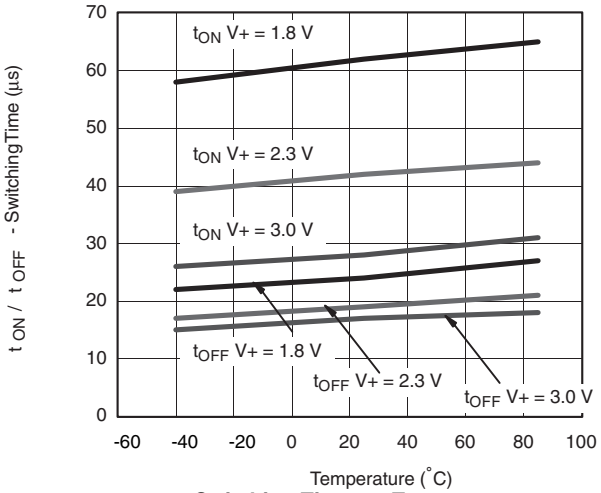


Supply Current vs. Temperature

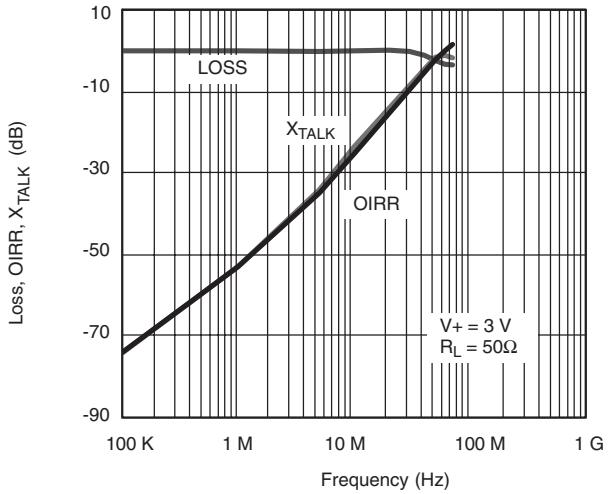
TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



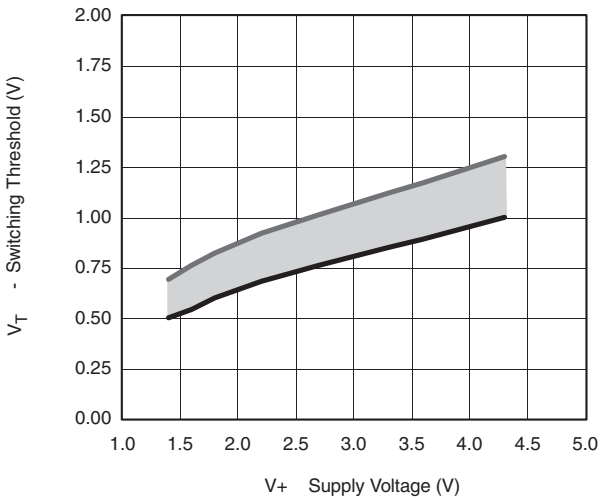
Leakage vs. Analog Voltage



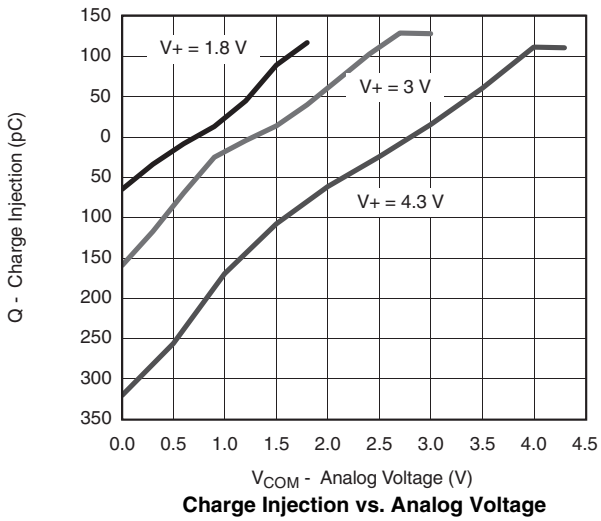
Switching Time vs. Temperature



Insertion Loss, Off-Isolation
Crosstalk vs. Frequency

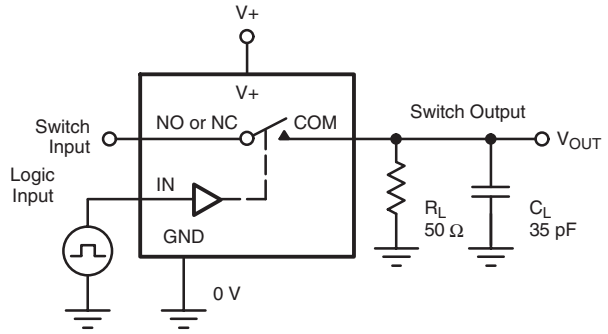


ISwitching Threshold vs. Supply Voltage



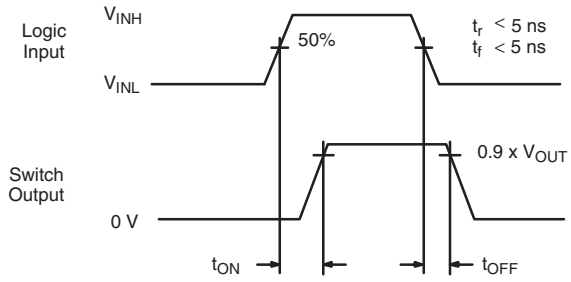
Charge Injection vs. Analog Voltage

TEST CIRCUITS



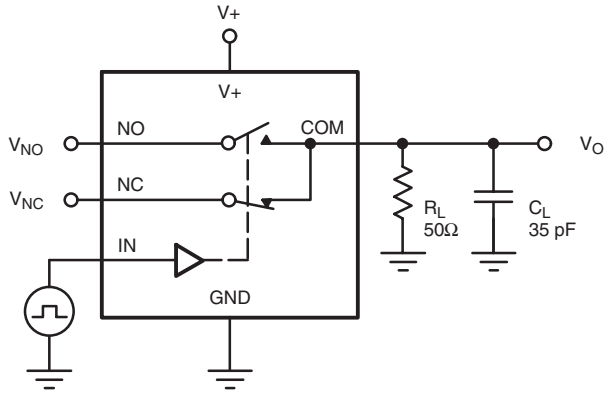
C_L (includes fixture and stray capacitance)

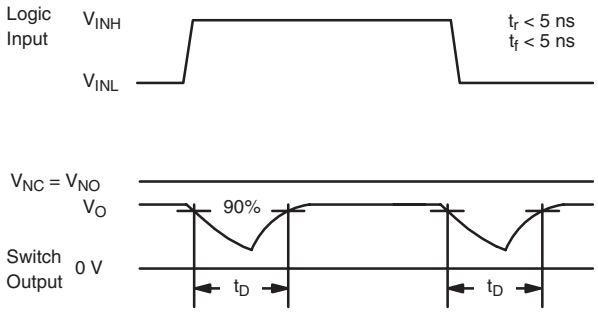
$$V_{OUT} = V_{COM}\left(\frac{R_L}{R_L + R_{ON}}\right)$$



Logic "1" = Switch On
Logic input waveforms inverted for switches that have the opposite logic sense.

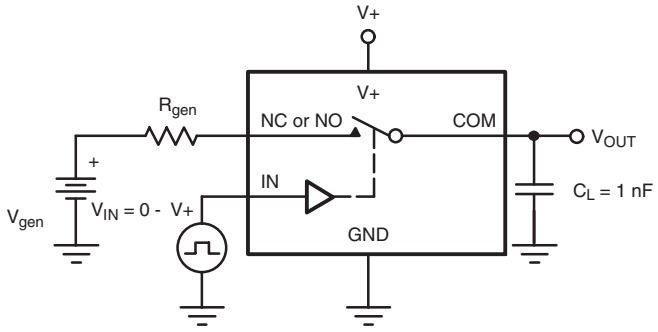
Figure 1. Switching Time

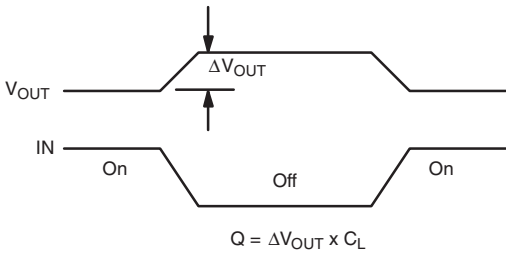




C_L (includes fixture and stray capacitance)

Figure 2. Break-Before-Make Interval





$Q = \Delta V_{OUT} \times C_L$

IN depends on switch configuration: input polarity determined by sense of switch.

Figure 3. Charge Injection

TEST CIRCUITS

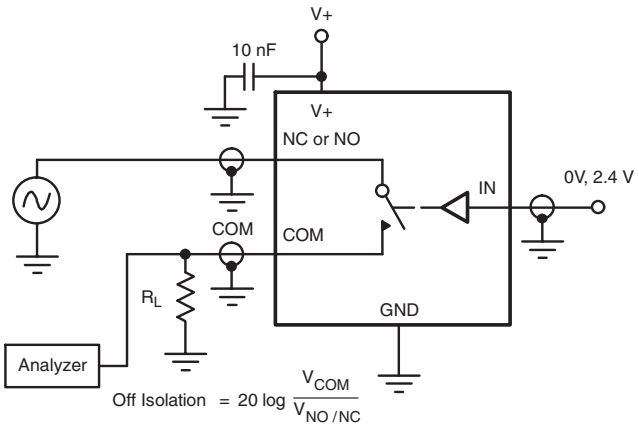


Figure 4. Off-Isolation

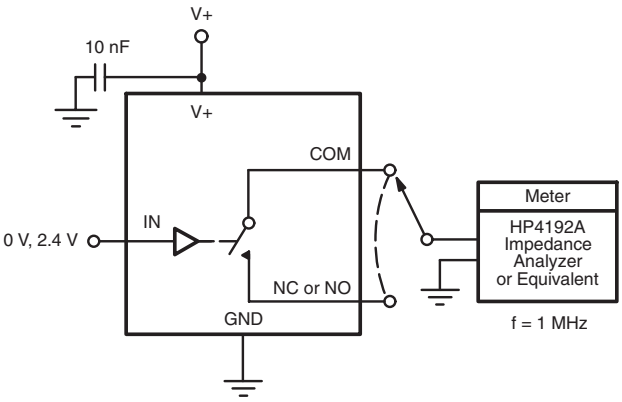


Figure 5. Channel Off/On Capacitance

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