

### **Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic P/N		Symbol	Value	Unit	
Supply Voltage		Vcc	50	V	
	DDTC123JLP		-5 to +12		
Input Voltage	DDTC143ZLP	VIN	-5 to +30	V	
	DDTC114YLP		-5 to +40		
	DDTC123JLP		100	mA	
Output Voltage	DDTC143ZLP	I <sub>O</sub>	100		
	DDTC114YLP		70		
Maximum Collector Current		I <sub>C(MAX)</sub>	100	mA	

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	$P_{D}$	250	mW
Power Deration above +25°C	P <sub>der</sub>	2	mW/°C
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	$T_J,T_STG$	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	P/N	Symbol	Min	Тур	Max	Unit	Test Condition
Off Characteristics (Note 6)							
Collector-Base Breakdown Voltage		BV <sub>CBO</sub>	50	_	_	V	$I_C = 50\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (N	Note 7)	BV <sub>CEO</sub>	50	_	_	V	$I_C = 2mA, I_B = 0$
Emitter-Base Breakdown Voltage (Note	7)	BV <sub>EBO</sub>	4.5	_	_	V	$I_E = 50\mu A, I_C = 0$
Collector Cutoff Current (Note 7)		I <sub>CEX</sub>	_	_	0.5	μΑ	$V_{CE} = 50V$ , $V_{EB(OFF)} = 3.0V$
Base Cutoff Current (I <sub>BEX</sub> )		I <sub>BL</sub>	_	_	0.5	μΑ	$V_{CE} = 50V, V_{EB(OFF)} = 3.0V$
Collector-Base Cut Off Current		I <sub>CBO</sub>	_	_	0.5	μΑ	$V_{CB} = 50V, I_{E} = 0$
Collector-Emitter Cut Off Current, IO(OFF	·)	I <sub>CEO</sub>	_	_	0.5	μA	V <sub>CE</sub> = 50V, I <sub>B</sub> = 0
Emitter-Base Cut Off Current	,	I <sub>EBO</sub>	_	_	0.5	mA	$V_{EB} = 5V, I_{C} = 0$
Input-Off Voltage		V <sub>I(OFF)</sub>	0.5	_	_	V	V <sub>CE</sub> = 5V, I <sub>C</sub> = 100μA
On Characteristics (Note 6)		V- /		·	·		
	DDTC123JLP		_	_	0.85		
Base-Emitter Turn-On Voltage (Note 7)	DDTC143ZLP	$V_{BE(ON)}$	_	_	0.85	V	$V_{CE} = 5V$ , $I_C = 2mA$
	DDTC114YLP		_	_	0.95		
Base-Emitter Saturation Voltage (Note	DDTC123JLP	V <sub>BE(SAT)</sub>		_	0.98	V	I <sub>C</sub> = 10mA, I <sub>B</sub> = 1mA
7)	DDTC143ZLP		_	_	0.998		
,	DDTC114YLP			—	0.98		
Input-On Voltage		V <sub>I(ON)</sub>		_	1.1	V	$V_0 = 0.3V, I_C = 5mA$
	DDTC123JLP	l <sub>1</sub>		—	7.2	mA	V <sub>I</sub> = 5V
Input Current	DDTC143ZLP			_	1.5		
	DDTC114YLP				7.2		
			50	_	_	_	$V_{CE} = 5V$ , $I_C = 1mA$
		_	70	_	—		$V_{CE} = 5V$ , $I_C = 2mA$
DC Current Gain	DC Current Gain		125	_	_	_	$V_{CE} = 5V$ , $I_C = 5mA$
		-	150	_	—		$V_{CE} = 5V$ , $I_C = 10mA$
			180	_	_		$V_{CE} = 5V$ , $I_C = 50mA$
Collector-Emitter Saturation Voltage		V	_	_	0.15	V	$I_C = 10mA$ , $I_B = 1mA$
		V <sub>CE</sub> (SAT)	_	_	0.2	V	$I_C = 50 \text{mA}, I_B = 5 \text{mA}$
Output On Voltage (Same as V <sub>CE(SAT)</sub> )		$V_{O(ON)}$	_	—	0.3		$I_J = 2.5 \text{mA}, I_O = 50 \text{mA}$
Input Resistor +/-30%		∆R1	-30	_	30	%	_
Resistor Ratio		Δ (R2/R1)	-20	_	-20	%	_
Small Signal Characteristics							
Transition Frequency (gain bandwidth product)		$f_{T}$		250		MHz	$V_{CE} = 10V$ , $I_{E} = 5mA$ , $f = 100MHz$

Notes:

 <sup>5.</sup> For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition. The entire exposed collector pad is attached to the heatsink.
6. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.
7. Guaranteed by design.



#### **Derating Curve** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

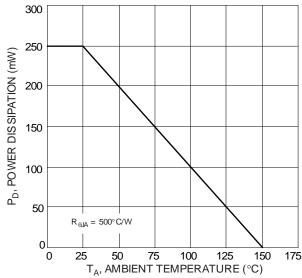
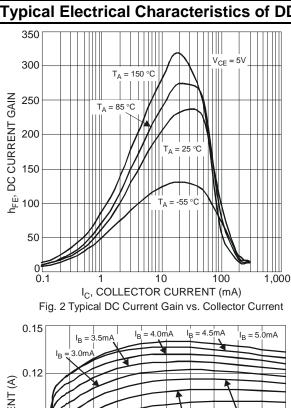
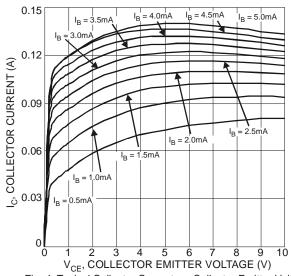


Fig. 1 Power Dissipation vs. Ambient temperature (Note 5)



## Typical Electrical Characteristics of DDTC123JLP (@TA = +25°C, unless otherwise specified.)





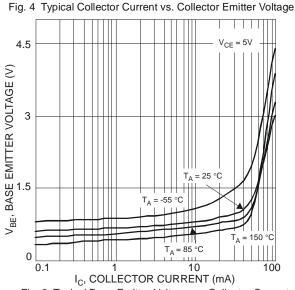


Fig. 6 Typical Base Emitter Voltage vs. Collector Current

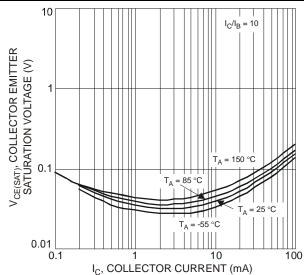


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

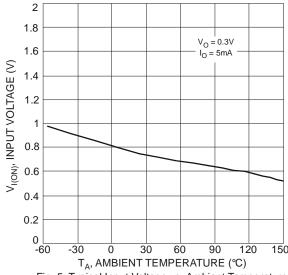
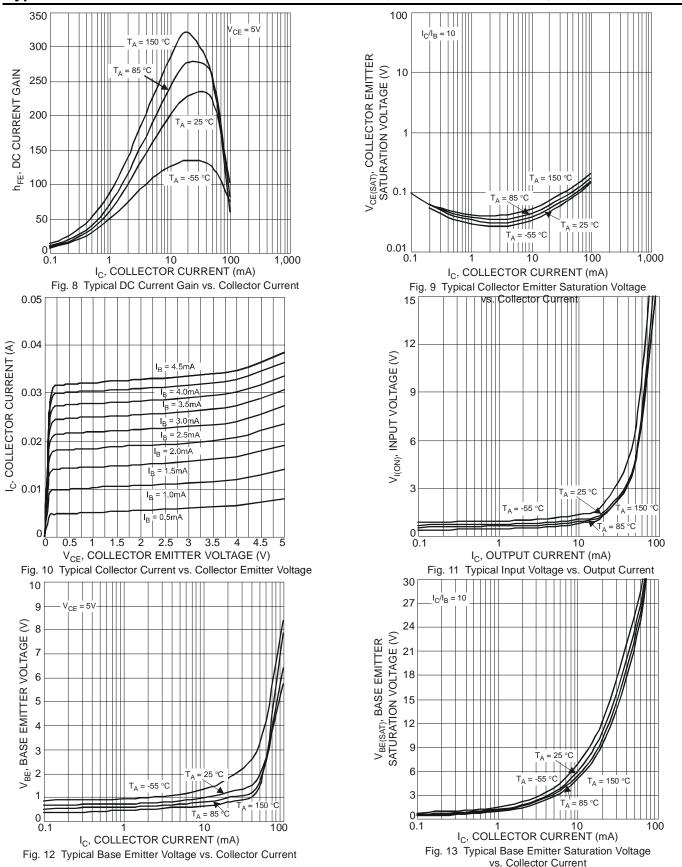


Fig. 7 Typical Base Emitter Saturation Voltage vs. Collector Current



#### Typical Electrical Characteristics of DDTC143ZLP (@TA = +25°C, unless otherwise specified.)





## Typical Electrical Characteristics of DDTC114YLP (@TA = +25°C, unless otherwise specified.)

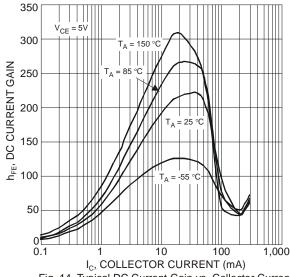


Fig. 14 Typical DC Current Gain vs. Collector Current

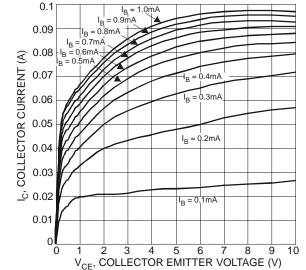


Fig. 16 Typical Collector Current vs. Collector Emitter Voltage

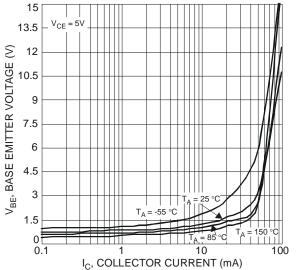


Fig. 18 Typical Base Emitter Voltage vs. Collector Current

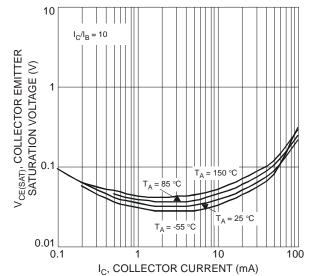


Fig. 15 Typical Collector Emitter Saturation Voltage vs. Collector Current

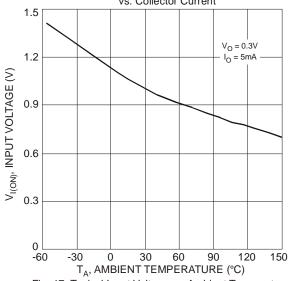


Fig. 17 Typical Input Voltage vs. Ambient Temperature

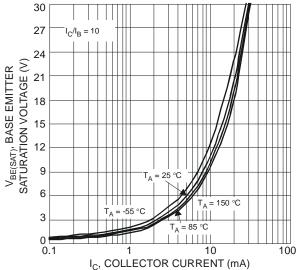
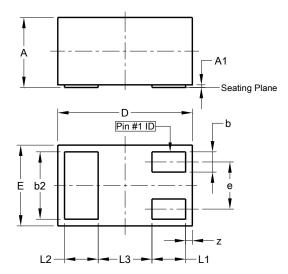


Fig. 19 Typical Base Emitter Saturation Voltage vs. Collector Current



## **Package Outline Dimensions**

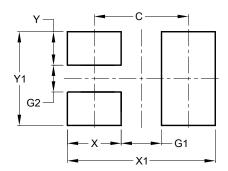
Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.



X1-DFN1006-3					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
е	ī	-	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	ı	-	0.40		
Z	0.02	0.08	0.05		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70



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