

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	-50	V
Drain-Gate Voltage (Note 6)		V <sub>DGR</sub>	-50	V
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	±20	V
Drain Current (Note 6)	Continuous	ID	-130	mA
Pulsed Drain Current (Note 6)		I <sub>DM</sub>	-1	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	200	mW
Thermal Resistance, Junction to Ambient	R <sub>0JA</sub>	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

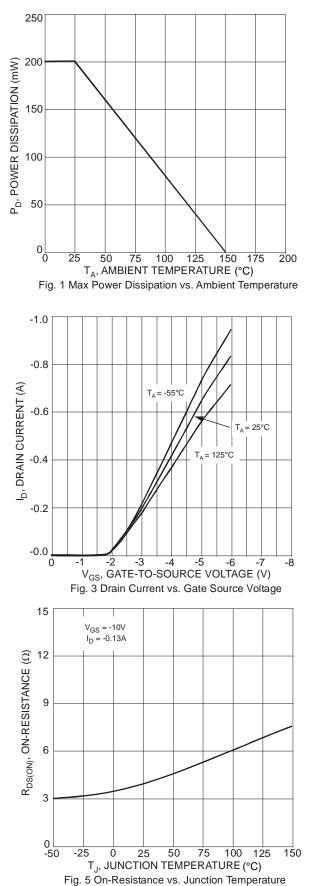
#### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

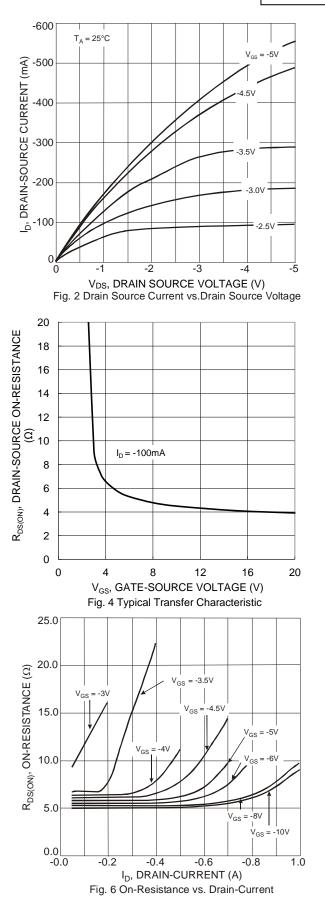
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-50	-75		V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-1 -2 -100	μΑ μΑ nA		
Gate-Body Leakage	I <sub>GSS</sub>	_		±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}, I_D = -1mA$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	6	10	Ω	V <sub>GS</sub> = -5V, I <sub>D</sub> = -0.1A	
Forward Transconductance	<b>g</b> fs	0.05			S	V <sub>DS</sub> = -25V, I <sub>D</sub> = -0.1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss			45	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss			25	pF		
Reverse Transfer Capacitance	Crss	_		12	pF		
SWITCHING CHARACTERISTICS (Note 8)							
Turn-On Delay Time	t <sub>D(ON)</sub>	_	10		ns	$V_{DD} = -30V, I_D = -0.27A,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	18		ns	$R_{GEN} = 50\Omega, V_{GS} = -10V$	

Notes: 6. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Incorporated's suggested pad layout document, which can be found on our website at http://www.diodes.com/package-outlines.html. 7. Short duration pulse test used to minimize self-heating effect.

8. Guarantee by design. Not subject to production testing.

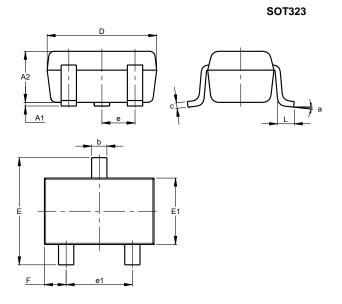








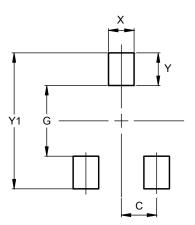
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.25	0.40	0.30		
c	0.10	0.18	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
e1	1.20	1.40	1.30		
F	0.375	0.475	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323

Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500

# **Package Outline Dimensions**

BSS84W Document number: DS30205 Rev. 17 - 2 Downloaded from Arrow.com.



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