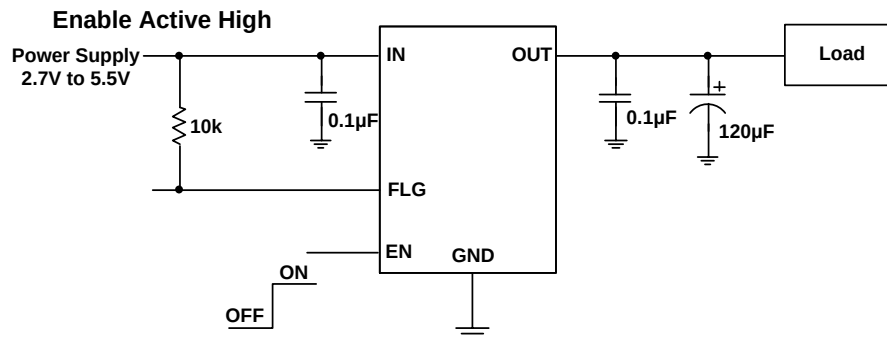


## Typical Applications Circuit



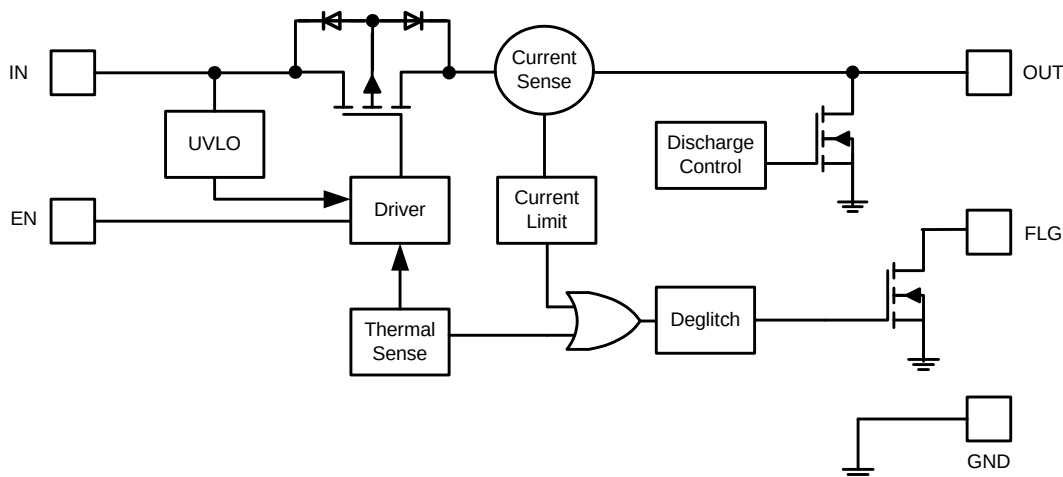
### Available Options

| Part Number | Channel | Enable Pin (EN) | Recommended Maximum Continuous Load Current (A) | Typical Current Limit (A) | Package                                                           |
|-------------|---------|-----------------|-------------------------------------------------|---------------------------|-------------------------------------------------------------------|
| AP2301      | 1       | Active Low      | 2A                                              | 2.5A                      | SO-8<br>MSOP-8<br>MSOP-8EP<br>U-DFN3030-8 (Type E)<br>U-DFN2020-6 |
| AP2311      | 1       | Active High     |                                                 |                           |                                                                   |

## Pin Descriptions

| Pin Name    | Pin Number   |                                |             | Function                                                                                                                                                              |
|-------------|--------------|--------------------------------|-------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|             | SO-8, MSOP-8 | MSOP-8EP, U-DFN3030-8 (Type E) | U-DFN2020-6 |                                                                                                                                                                       |
| GND         | 1            | 1                              | 2           | Ground                                                                                                                                                                |
| IN          | 2, 3         | 2, 3                           | 1           | Voltage Input Pin; Connect a 0.1µF or larger ceramic capacitor from IN to GND as close as possible. (all IN pins must be tied together externally)                    |
| EN          | 4            | 4                              | 3           | Enable input, active low (AP2301) or active high (AP2311)                                                                                                             |
| FLG         | 5            | 5                              | 4           | Over-temperature and over-current fault reporting with 7ms deglitch; active low open-drain output. FLG is disabled for 7ms after turn-on.                             |
| OUT         | 6, 7         | 6, 7                           | 5           | Voltage Output Pin All OUT pins must be tied together externally.                                                                                                     |
| NC          | 8            | 8                              | 6           | No Internal Connection; recommend tie to OUT pins                                                                                                                     |
| Exposed Pad | —            | Exposed Pad                    | Exposed Pad | Exposed pad.<br>It should be externally connected to GND and thermal mass for enhanced thermal impedance. It should not be used as electrical ground conduction path. |

## Functional Block Diagram



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Symbol                             | Parameter                          | Ratings                                | Unit |
|------------------------------------|------------------------------------|----------------------------------------|------|
| ESD HBM                            | Human Body Model ESD Protection    | 2                                      | kV   |
| ESD MM                             | Machine Model ESD Protection       | 200                                    | V    |
| V <sub>IN</sub>                    | Input Voltage (Note 4)             | -0.3 to 6.5                            | V    |
| V <sub>OUT</sub>                   | Output Voltage (Note 4)            | -0.3 to (V <sub>IN</sub> + 0.3) or 6.5 | V    |
| V <sub>EN</sub> , V <sub>FLG</sub> | Enable Voltage (Note 4)            | -0.3 to (V <sub>IN</sub> + 0.3) or 6.5 | V    |
| I <sub>LOAD</sub>                  | Maximum Continuous Load Current    | Internal Limited                       | A    |
| T <sub>J(MAX)</sub>                | Maximum Junction Temperature       | +150                                   | °C   |
| T <sub>ST</sub>                    | Storage Temperature Range (Note 5) | -65 to +150                            | °C   |

Notes: 4. All voltages referred to GND pin. Maximums are the lower of (V<sub>IN</sub> + 0.3) and 6.5V.  
5. UL Recognized Rating from -30°C to +70°C (Diodes qualified T<sub>ST</sub> from -65°C to +150°C).

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time. Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.

## Recommended Operating Conditions (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Symbol           | Parameter                     | Min | Max             | Unit |
|------------------|-------------------------------|-----|-----------------|------|
| V <sub>IN</sub>  | Input Voltage                 | 2.7 | 5.5             | V    |
| I <sub>OUT</sub> | Output Current                | 0   | 2               | A    |
| V <sub>IL</sub>  | EN Input Logic Low Voltage    | 0   | 0.8             | V    |
| V <sub>IH</sub>  | EN Input Logic High Voltage   | 2   | V <sub>IN</sub> | V    |
| T <sub>A</sub>   | Operating Ambient Temperature | -40 | +85             | °C   |

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, V<sub>IN</sub> = +5V, C<sub>IN</sub> = 0.1μF, C<sub>L</sub> = 1μF, unless otherwise specified.)

| Symbol               | Parameter                              | Test Conditions                                                                            |                                | Min  | Typ  | Max  | Unit |
|----------------------|----------------------------------------|--------------------------------------------------------------------------------------------|--------------------------------|------|------|------|------|
| V <sub>UVLO</sub>    | Input UVLO                             | V <sub>IN</sub> Rising                                                                     |                                | 1.6  | 2.0  | 2.4  | V    |
| ΔV <sub>UVLO</sub>   | Input UVLO Hysteresis                  | V <sub>IN</sub> Decreasing                                                                 |                                | —    | 50   | —    | mV   |
| I <sub>SHDN</sub>    | Input Shutdown Current                 | Disabled, OUT = Open                                                                       |                                | —    | 0.1  | 1    | μA   |
| I <sub>Q</sub>       | Input Quiescent Current                | Enabled, OUT = Open                                                                        |                                | —    | 60   | 100  | μA   |
| I <sub>LEAK</sub>    | Input Leakage Current                  | Disabled, OUT Grounded                                                                     |                                | —    | 0.1  | 1    | μA   |
| I <sub>REV</sub>     | Reverse Leakage Current                | Disabled, V <sub>IN</sub> = 0V, V <sub>OUT</sub> = 5V, I <sub>REV</sub> at V <sub>IN</sub> |                                | —    | 0.01 | 1    | μA   |
| R <sub>DS(ON)</sub>  | Switch On-resistance                   | V <sub>IN</sub> = 5V, I <sub>OUT</sub> = 2.0A                                              | T <sub>A</sub> = +25°C         | —    | 70   | 84   | mΩ   |
|                      |                                        |                                                                                            | -40°C ≤ T <sub>A</sub> ≤ +85°C | —    | —    | 105  |      |
|                      |                                        | V <sub>IN</sub> = 3.3V, I <sub>OUT</sub> = 2.0A                                            | T <sub>A</sub> = +25°C         | —    | 90   | 108  |      |
|                      |                                        |                                                                                            | -40°C ≤ T <sub>A</sub> ≤ +85°C | —    | —    | 135  |      |
| I <sub>LIMIT</sub>   | Over-Load Current Limit (Note 6)       | V <sub>IN</sub> = 5V, V <sub>OUT</sub> = 4.5V                                              | -40°C ≤ T <sub>A</sub> ≤ +85°C | 2.05 | 2.50 | 2.85 | A    |
| I <sub>TRIG</sub>    | Current Limiting Trigger Threshold     | Output Current Slew Rate (<100A/s)                                                         |                                | —    | 2.5  | —    | A    |
| I <sub>SHORT</sub>   | Short-Circuit Current Limit            | Enabled into Short Circuit                                                                 |                                | —    | 2.75 | —    | A    |
| t <sub>SHORT</sub>   | Short-Circuit Response Time            | V <sub>OUT</sub> = 0V to I <sub>OUT</sub> = I <sub>LIMIT</sub> (OUT Shorted to Ground)     |                                | —    | 2    | —    | μs   |
| V <sub>IL</sub>      | EN Input Logic Low Voltage             | V <sub>IN</sub> = 2.7V to 5.5V                                                             |                                | —    | —    | 0.8  | V    |
| V <sub>IH</sub>      | EN Input Logic High Voltage            | V <sub>IN</sub> = 2.7V to 5.5V                                                             |                                | 2    | —    | —    | V    |
| I <sub>LEAK-EN</sub> | EN Input Leakage                       | V <sub>IN</sub> = 5V, V <sub>EN</sub> = 0V and 5.5V                                        |                                | —    | 0.01 | 1    | μA   |
| I <sub>LEAK-O</sub>  | Output Leakage Current                 | Disabled, V <sub>OUT</sub> = 0V                                                            |                                | —    | 0.5  | 1    | μA   |
| t <sub>D(ON)</sub>   | Output Turn-on Delay Time              | C <sub>L</sub> = 1μF, R <sub>LOAD</sub> = 5Ω                                               |                                | —    | 0.1  | —    | ms   |
| t <sub>R</sub>       | Output Turn-on Rise Time               | C <sub>L</sub> = 1μF, R <sub>LOAD</sub> = 5Ω                                               |                                | —    | 0.6  | 1.5  | ms   |
| t <sub>D(OFF)</sub>  | Output Turn-off Delay Time             | C <sub>L</sub> = 1μF, R <sub>LOAD</sub> = 5Ω                                               |                                | —    | 0.1  | —    | ms   |
| t <sub>F</sub>       | Output Turn-off Fall Time              | C <sub>L</sub> = 1μF, R <sub>LOAD</sub> = 5Ω                                               |                                | —    | 0.05 | 0.1  | ms   |
| R <sub>FLG</sub>     | FLG Output FET On-resistance           | I <sub>FLG</sub> = 10mA                                                                    |                                | —    | 20   | 40   | Ω    |
| I <sub>FOH</sub>     | FLG Off Current                        | V <sub>FLG</sub> = 5V                                                                      |                                | —    | 0.01 | 1    | μA   |
| t <sub>Blank</sub>   | FLG Blanking Time                      | Assertion or Deassertion due to Overcurrent and Over-temperature Condition                 |                                | 4    | 7    | 15   | ms   |
| t <sub>DIS</sub>     | Discharge Time                         | C <sub>L</sub> = 1μF, V <sub>IN</sub> = 5V, Disabled to V <sub>OUT</sub> < 0.5V            |                                | —    | 0.6  | —    | ms   |
| R <sub>DIS</sub>     | Discharge Resistance (Note 7)          | V <sub>IN</sub> = 5V, Disabled, I <sub>OUT</sub> = 1mA                                     |                                | —    | 100  | —    | Ω    |
| T <sub>SHDN</sub>    | Thermal Shutdown Threshold             | Enabled                                                                                    |                                | —    | +140 | —    | °C   |
| T <sub>HYS</sub>     | Thermal Shutdown Hysteresis            | —                                                                                          |                                | —    | +20  | —    | °C   |
| θ <sub>JA</sub>      | Thermal Resistance Junction-to-Ambient | SO-8 (Note 8)                                                                              |                                | —    | 96   | —    | °C/W |
|                      |                                        | MSOP-8 (Note 8)                                                                            |                                | —    | 130  | —    | °C/W |
|                      |                                        | MSOP-8EP (Note 9)                                                                          |                                | —    | 92   | —    | °C/W |
|                      |                                        | U-DFN3030-8 (Type E) (Note 9)                                                              |                                | —    | 84   | —    | °C/W |
|                      |                                        | U-DFN2020-6 (Note 10)                                                                      |                                | —    | 90   | —    | °C/W |

- Notes:
- Pulse-testing techniques maintain junction temperature close to ambient temperature; thermal effects must be taken into account separately.
  - The discharge function is active when the device is disabled (when enable is de-asserted or during power-up power-down when V<sub>IN</sub> < V<sub>UVLO</sub>). The discharge function offers a resistive discharge path for the external storage capacitor for limited time.
  - Device mounted on 2" x 2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
  - Device mounted on 2" x 2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane.
  - Device mounted on 1" x 1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground.

## Typical Performance Characteristics

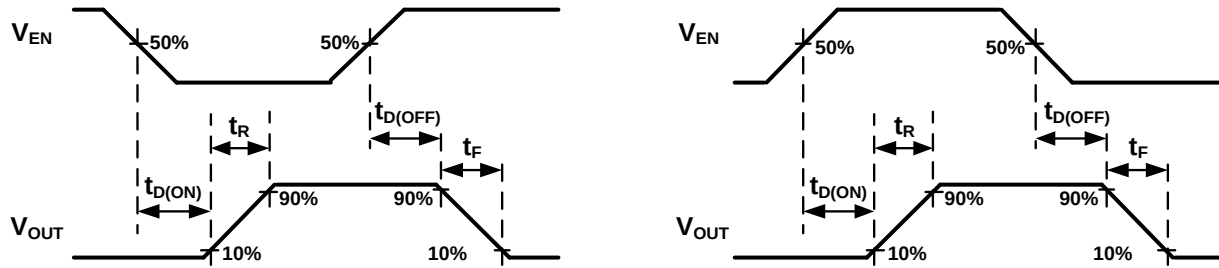
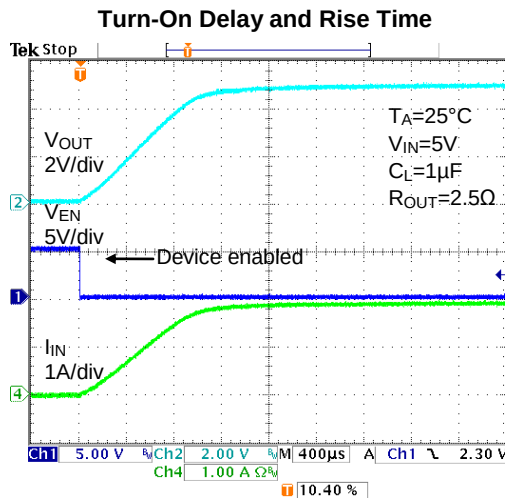
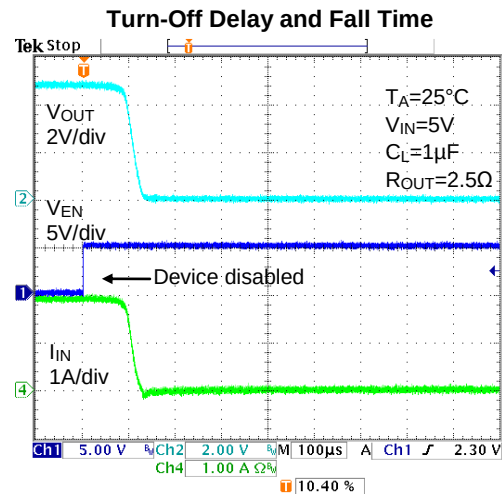


Figure 1. Voltage Waveforms: AP2301 (Left), AP2311 (Right)

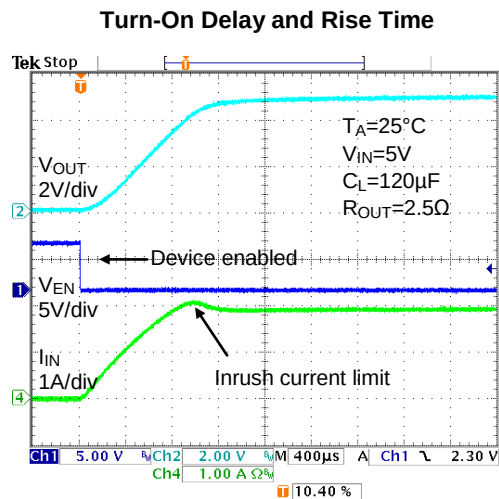
All Enable Plots are for Enable Active Low



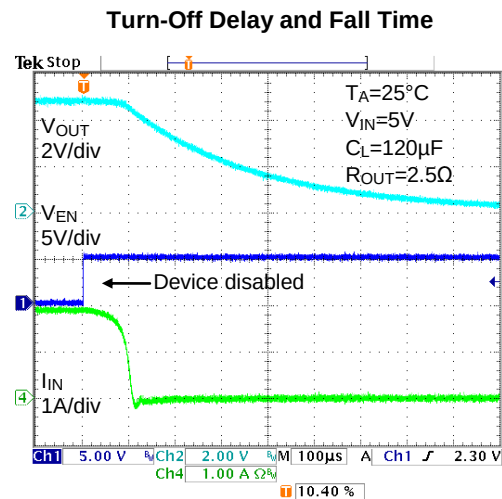
11 Mar 2010  
14:38:39



11 Mar 2010  
14:39:12



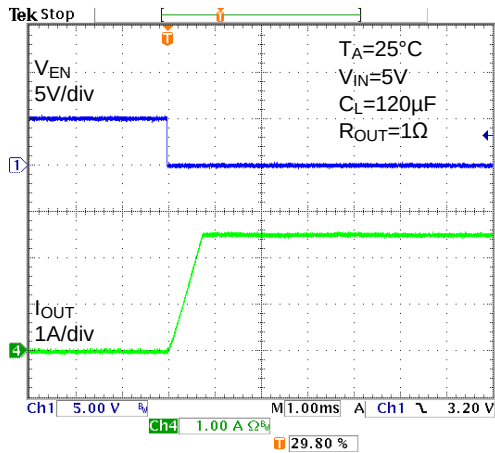
11 Mar 2010  
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11 Mar 2010  
14:42:45

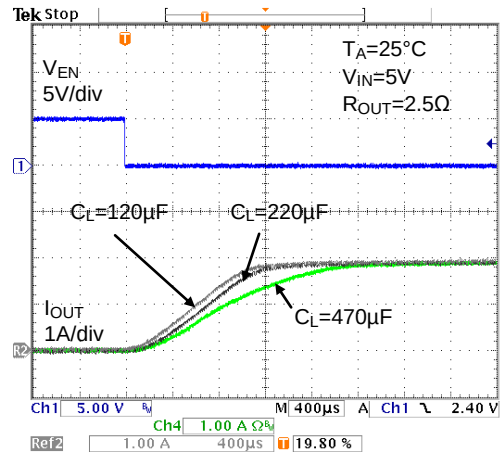
# Typical Performance Characteristics (Cont.)

Device Enabled Into Short-Circuit



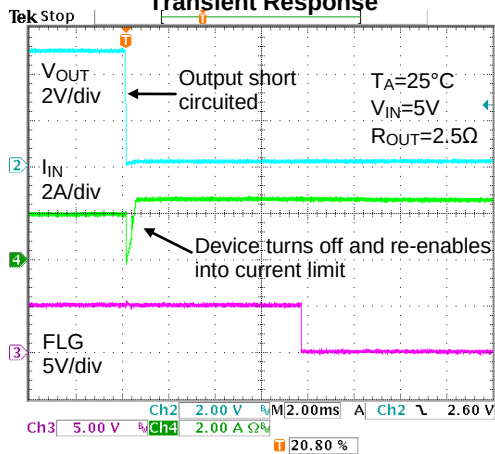
11 Mar 2010  
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Inrush Current



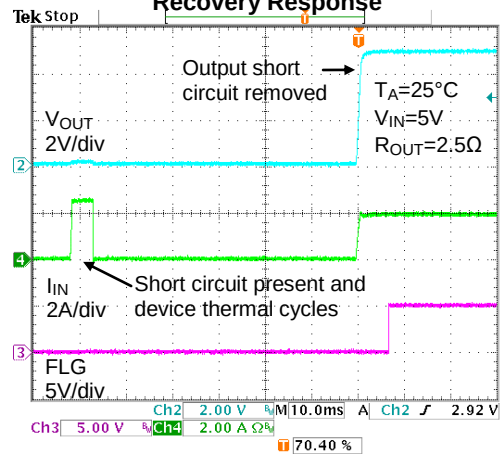
11 Mar 2010  
17:31:57

Full-Load to Short-Circuit Transient Response



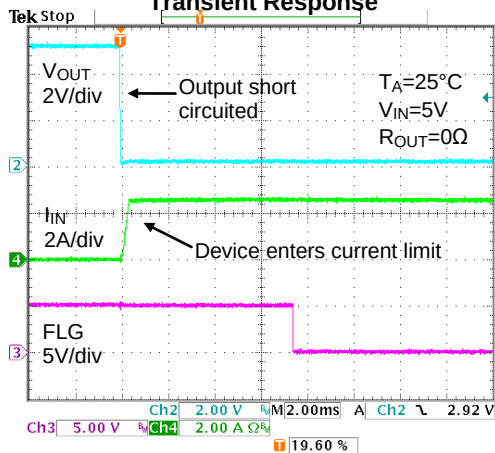
12 Mar 2010  
15:32:01

Short-Circuit to Full-Load Recovery Response



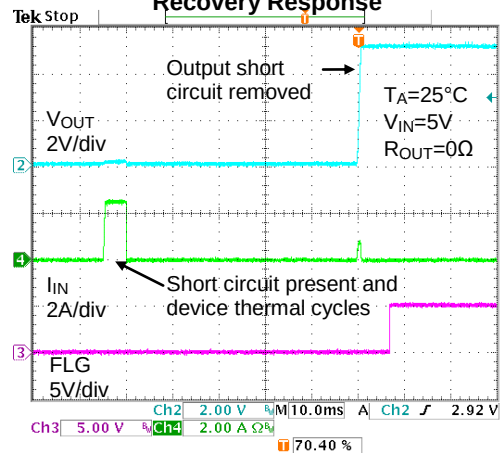
12 Mar 2010  
16:00:41

No-Load to Short-Circuit Transient Response



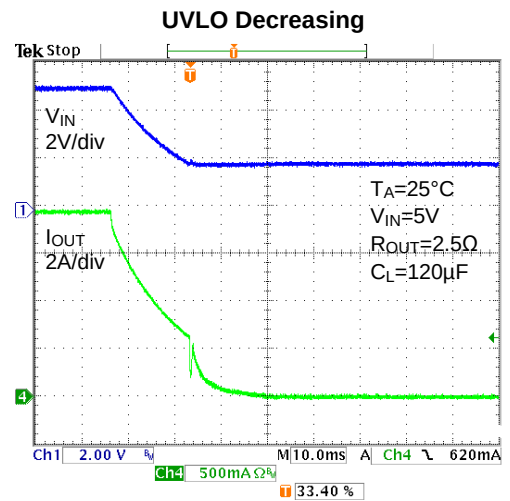
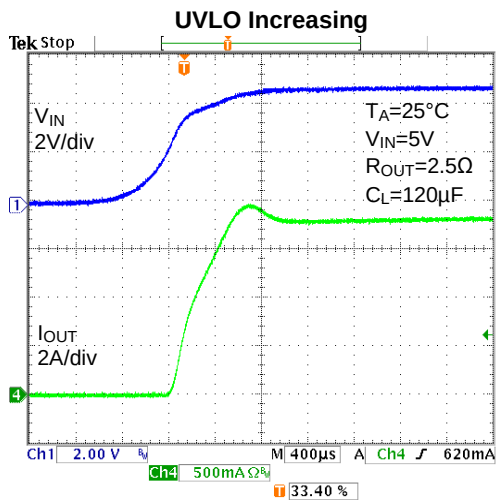
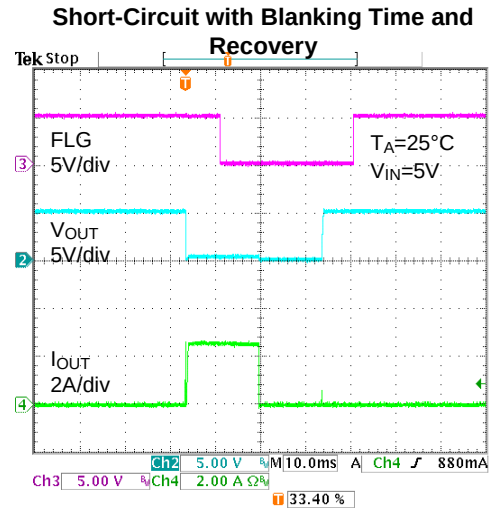
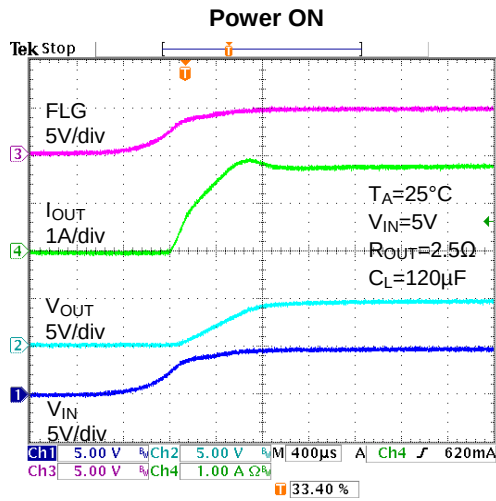
12 Mar 2010  
16:31:34

Short-Circuit to No-Load Recovery Response

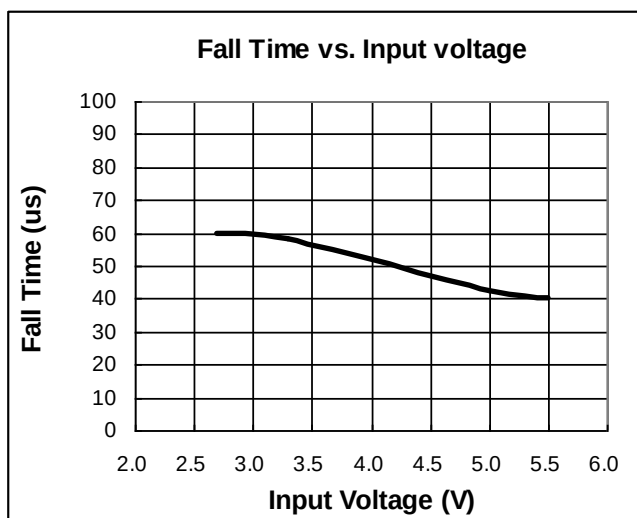
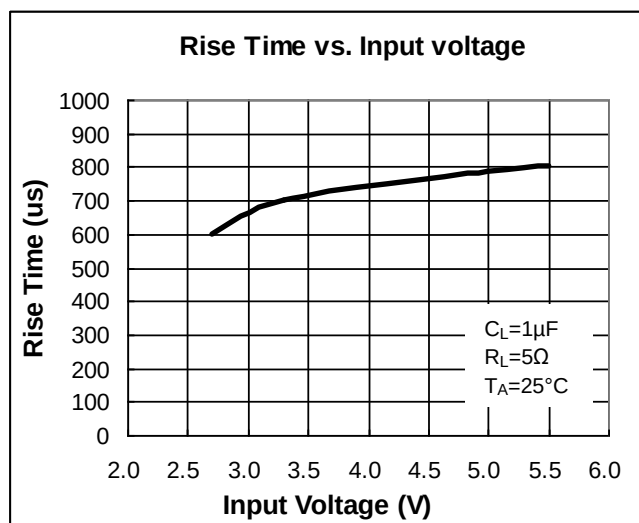
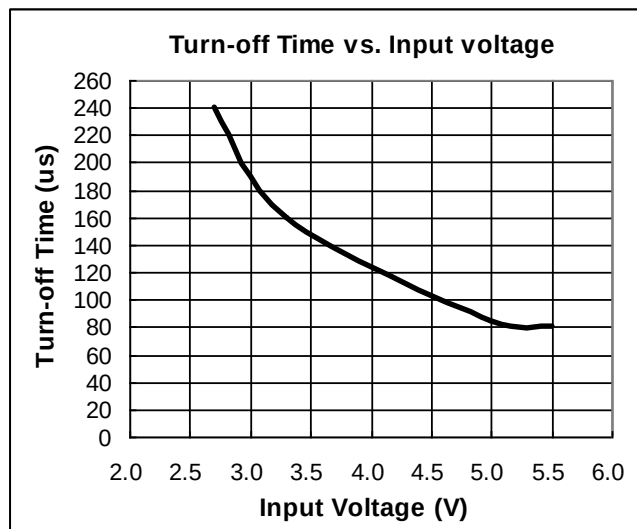
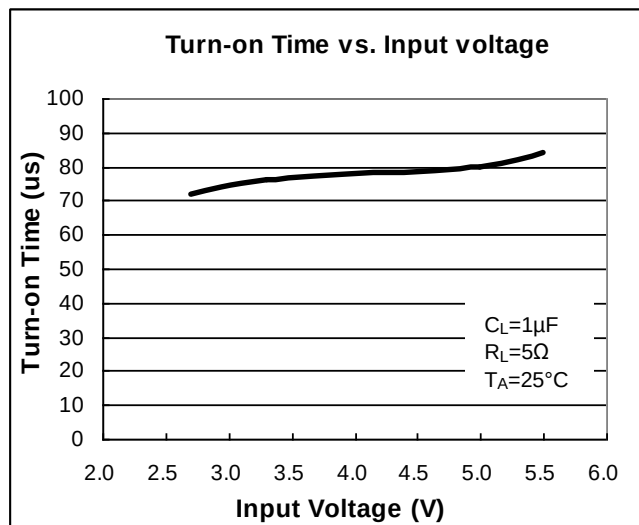


12 Mar 2010  
16:47:07

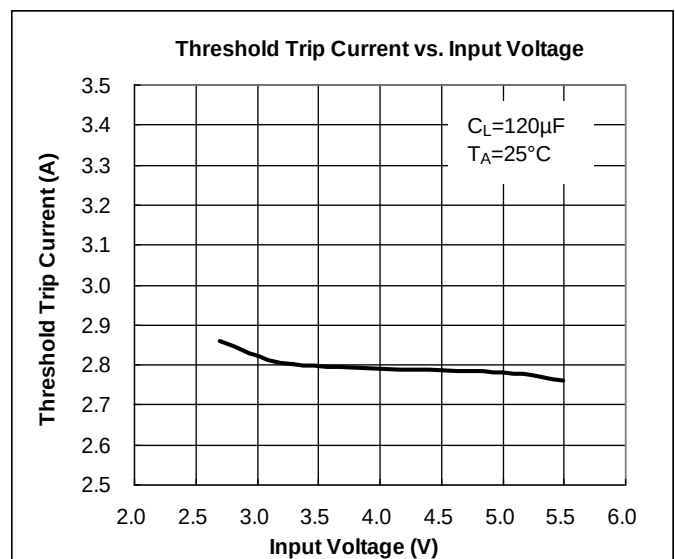
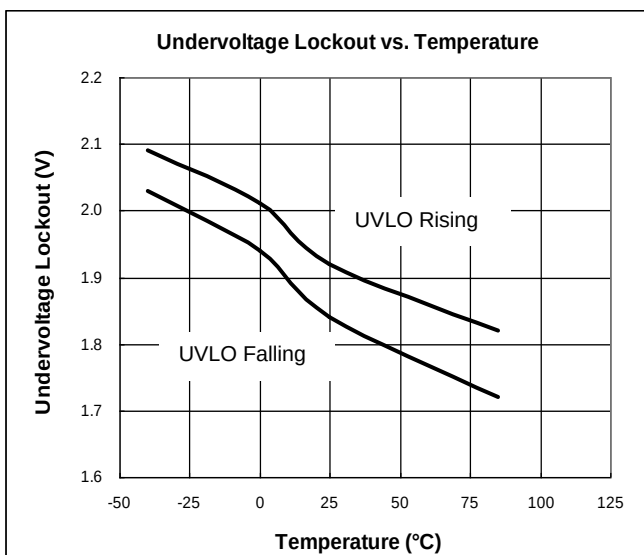
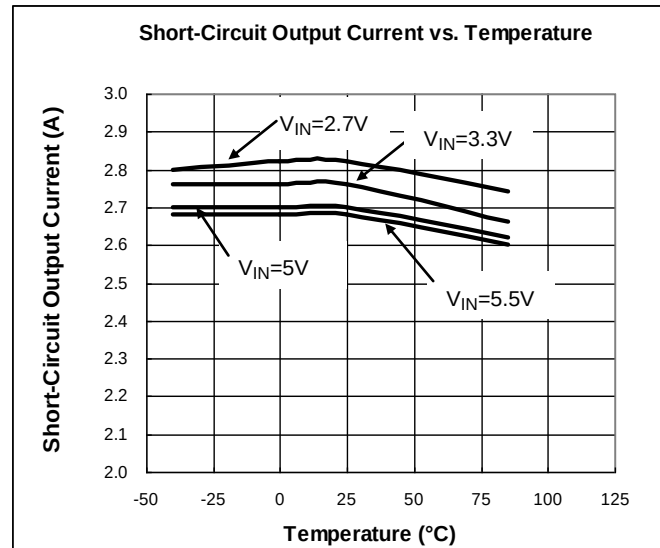
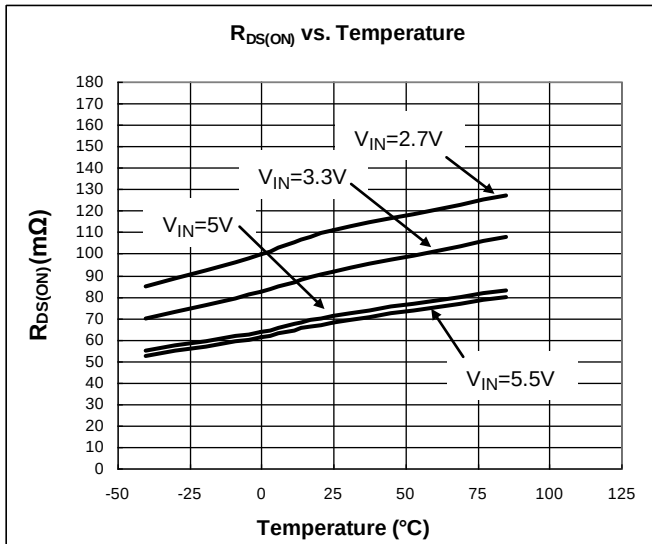
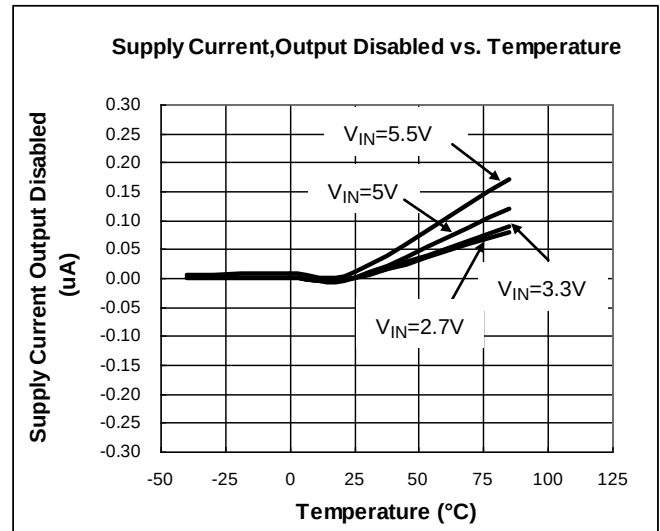
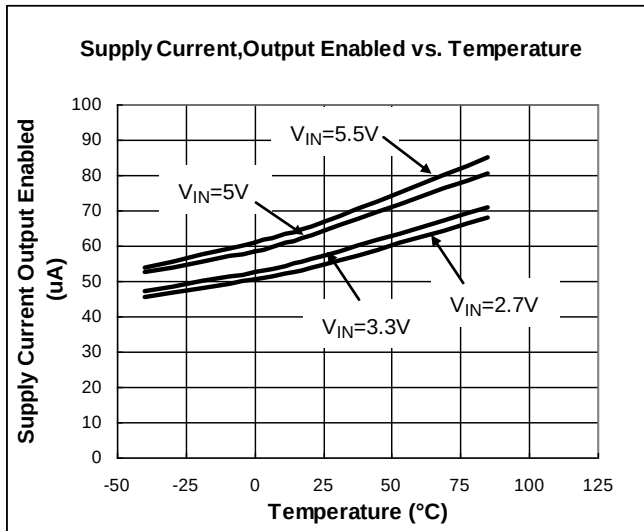
# Typical Performance Characteristics (Cont.)



# Typical Performance Characteristics (Cont.)



Typical Performance Characteristics (Cont.)





## Application Information

### Power Supply Considerations

A 0.1µF to 2.2µF X7R or X5R ceramic bypass capacitor placed between IN and GND, close to the device, is recommended. When an external power supply is used, or an additional ferrite bead is added to the input, high inrush current may cause voltage spikes higher than the device maximum input rating during short circuit condition. In this case a 2.2µF or bigger capacitor is recommended. Placing a high-value electrolytic capacitor on the input and output pin(s) is recommended when the output load is heavy. This precaution reduces power-supply transients that may cause ringing on the input. Additionally, bypassing the output with a 0.1µF to 1.0µF ceramic capacitor improves the immunity of the device to short circuit transients.

### Over-Current and Short Circuit Protection

An internal sensing FET is employed to check for over-current conditions. Unlike current-sense resistors, sense FETs do not increase the series resistance of the current path. When an over-current condition is detected, the device maintains a constant output current and reduces the output voltage accordingly. Complete shutdown occurs only if the fault stays long enough to activate thermal limiting.

Three possible overload conditions can occur. In the first condition, the output has been shorted to GND before the device is enabled or before  $V_{IN}$  has been applied. The AP2301/AP2311 senses the short circuit and immediately clamps output current to a certain safe level namely  $I_{LIMIT}$ .

In the second condition, an output short or an overload occurs while the device is enabled. At the instance the overload occurs, higher inrush current may flow for a very short period of time before the current limit function can react. The input capacitor(s) rapidly discharge through the device, activating current limit circuitry. Protection is achieved by momentarily opening the P-MOS high-side power switch and then gradually turning it on. After the current limit function has tripped (reached the over-current trip threshold), the device switches into current limiting mode and the current is clamped at  $I_{LIMIT}$ .

In the third condition, the load has been gradually increased beyond the recommended operating current. The current is permitted to rise until the current-limit threshold ( $I_{TRIP}$ ) is reached or until the thermal limit of the device is exceeded. The AP2301/AP2311 is capable of delivering current up to the current-limit threshold without damaging the device. Once the threshold has been reached, the device switches into its current limiting mode and is set at  $I_{LIMIT}$ .

### FLG Response

When an over-current or over-temperature shutdown condition is encountered, the FLG open-drain output goes active low after a nominal 7ms deglitch timeout. The FLG output remains low until both over-current and over-temperature conditions are removed. Connecting a heavy capacitive load to the output of the device can cause a momentary over-current condition, which does not trigger the FLG due to the 7ms deglitch timeout. The AP2301/AP2311 is designed to eliminate false over-current reporting without the need of external components to remove unwanted pulses.

### Power Dissipation and Junction Temperature

The low on-resistance of the internal MOSFET allows the small surface-mount packages to pass large current. Using the maximum operating ambient temperature ( $T_A$ ) and  $R_{DS(ON)}$ , the power dissipation can be calculated by:

$$P_D = R_{DS(ON)} \times I^2$$

Finally, calculate the junction temperature:

$$T_J = P_D \times R_{\theta JA} + T_A$$

Where:

$T_A$  = Ambient temperature °C

$R_{\theta JA}$  = Thermal resistance

$P_D$  = Total power dissipation

### Thermal Protection

Thermal protection prevents the IC from damage when heavy-overload or short-circuit faults are present for extended periods of time. The AP2301/AP2311 implements a thermal sensing to monitor the operating junction temperature of the power distribution switch. Once the die temperature rises to approximately +140°C due to excessive power dissipation in an over-current or short-circuit condition the internal thermal sense circuitry turns the power switch off, thus preventing the power switch from damage. Hysteresis is built into the thermal sense circuit allowing the device to cool down approximately +20°C before the switch turns back on. The switch continues to cycle in this manner until the load fault or input power is removed. The FLG open-drain output is asserted when an over-temperature shutdown or over-current occurs with 7ms deglitch.

## Application Information (Cont.)

### Under-Voltage Lockout (UVLO)

Under-voltage lockout function (UVLO) keeps the internal power switch from being turned on until the power supply has reached at least 2V, even if the switch is enabled. Whenever the input voltage falls below approximately 2V, the power switch is quickly turned off. This facilitates the design of hot-insertion systems where it is not possible to turn off the power switch before input power is removed.

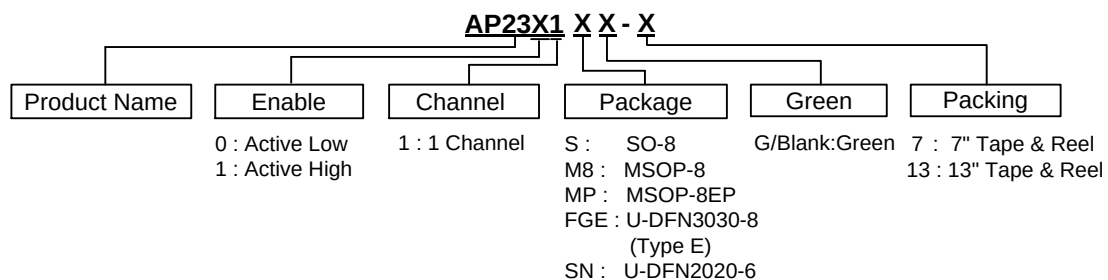
### Discharge Function

The discharge function of the device is active when enable is disabled or de-asserted. The discharge function with the N-MOS power switch implementation is activated and offers a resistive discharge path for the external storage capacitor. This is designed for discharging any residue of the output voltage when either no external output resistance or load resistance is present at the output.

### Dual-Purpose Port Applications

The AP2301/AP2311 is not recommended for use in dual-purpose port applications in which a single port is used for data communication between the host and peripheral devices while simultaneously maintaining a charge to the battery of the peripheral device. An example of such a non-recommended application is a shared HDMI/MHL (Mobile High-definition Link) port that allows streaming video between an HDTV or set-top box and a smartphone or tablet while maintaining a charge to the smartphone or tablet battery. Since the AP2301/AP2311 includes an embedded discharge feature that discharges the output load of the device when the device is disabled, the batteries of the connected peripheral device will be subject to continual discharge whenever the AP2301/AP2311 is disabled. An overstress condition to the device's discharge MOS transistor may result. In addition, if the output of the AP2301/AP2311 is subjected to a constant voltage that would be present during a dual-purpose port application such as MHL, an overstress condition to the device may result.

## Ordering Information

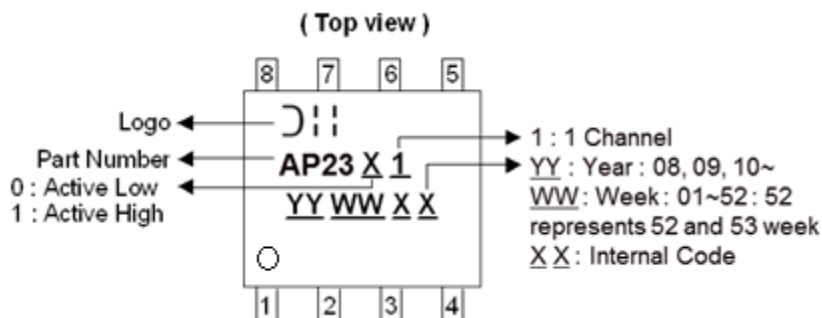


| Part Number  | Package (Note 12)    | Package Code | 7"/13" Tape and Reel Quantity | Status (Note 11) |
|--------------|----------------------|--------------|-------------------------------|------------------|
| AP2301SG-13  | SO-8                 | S            | 2500                          | In Production    |
| AP2301SN-7   | U-DFN2020-6          | SN           | 3000                          | In Production    |
| AP2301MPG-13 | MSOP-8EP             | MP           | 2500                          | In Production    |
| AP2301FGEG-7 | U-DFN3030-8 (Type E) | FGE          | 3000                          | In Production    |
| AP2311SG-13  | SO-8                 | S            | 2500                          | In Production    |
| AP2311SN-7   | U-DFN2020-6          | SN           | 3000                          | In Production    |
| AP2311M8G-13 | MSOP-8               | M8           | 2500                          | In Production    |
| AP2311MPG-13 | MSOP-8EP             | MP           | 2500                          | In Production    |
| AP2311FGEG-7 | U-DFN3030-8 (Type E) | FGE          | 3000                          | In Production    |

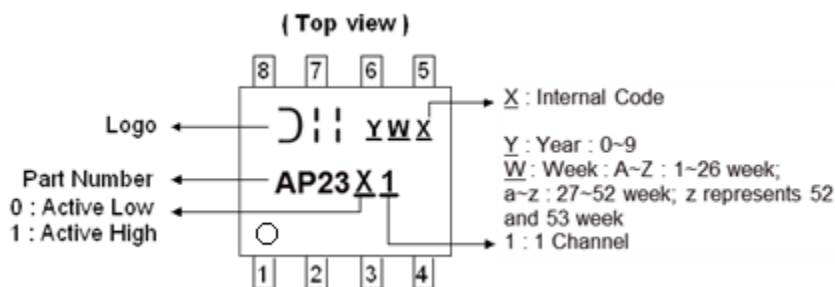
Notes: 11. AP2301M8G-13 is End of Life (EOL) and recommended alternative is AP2301MPG-13 and AP2311M8G-13.  
12. For packaging details, go to our website at: <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information (Note 11)

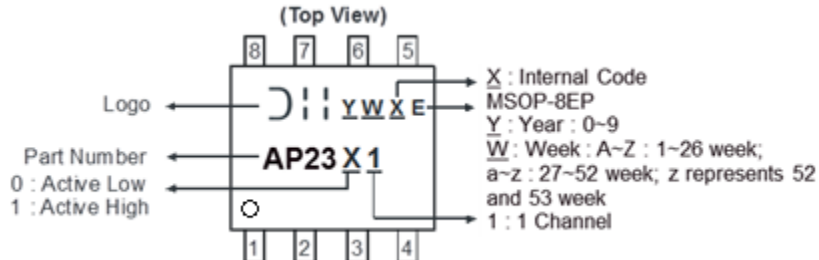
### (1) SO-8



### (2) MSOP-8

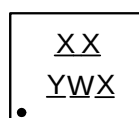


### (3) MSOP-8EP



### (4) U-DFN3030-8 (Type E)

#### ( Top View )



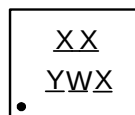
XX : Identification Code  
Y : Year : 0~9  
W : Week : A~Z : 1~26 week;  
a~z : 27~52 week; z represents 52 and 53 week  
X : A~Z : Green

| Part Number  | Package              | Identification Code |
|--------------|----------------------|---------------------|
| AP2301FGEG-7 | U-DFN3030-8 (Type E) | BB                  |
| AP2311FGEG-7 | U-DFN3030-8 (Type E) | BC                  |

**Marking Information (Cont.)**

(5) U-DFN2020-6

( Top View )


XX : Identification Code

Y : Year : 0~9

W : Week : A~Z : 1~26 week;  
 a~z : 27~52 week; z represents  
 52 and 53 week

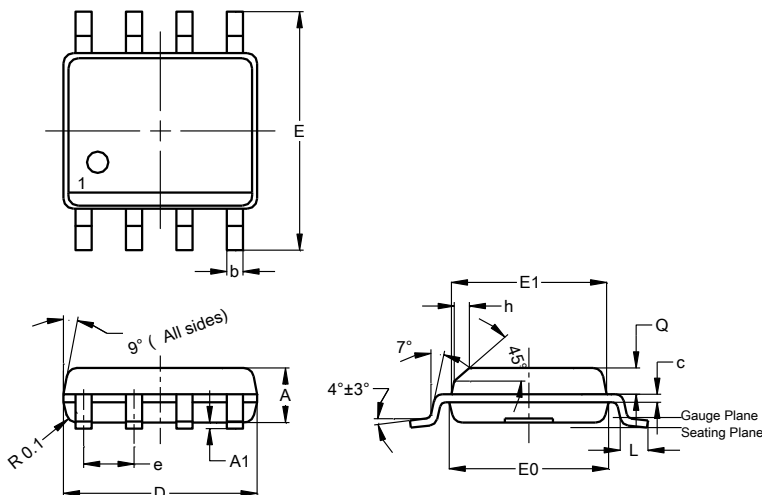
X : A~Z : Internal Code

| Part Number | Package     | Identification Code |
|-------------|-------------|---------------------|
| AP2301SN-7  | U-DFN2020-6 | DB                  |
| AP2311SN-7  | U-DFN2020-6 | DC                  |

**Package Outline Dimensions**

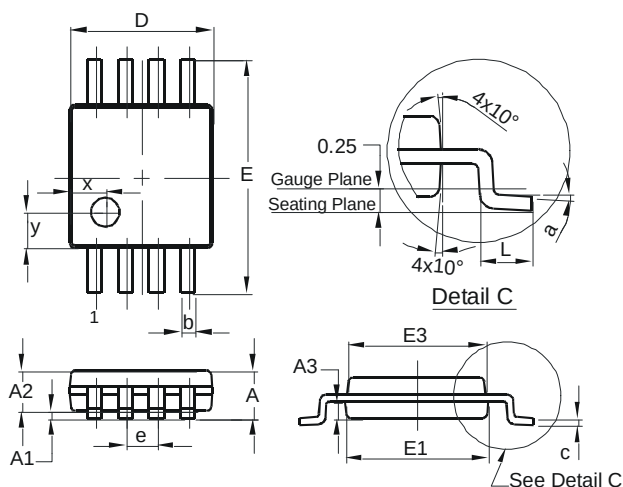
 Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: SO-8



| SO-8                 |      |      |      |
|----------------------|------|------|------|
| Dim                  | Min  | Max  | Typ  |
| A                    | 1.40 | 1.50 | 1.45 |
| A1                   | 0.10 | 0.20 | 0.15 |
| b                    | 0.30 | 0.50 | 0.40 |
| c                    | 0.15 | 0.25 | 0.20 |
| D                    | 4.85 | 4.95 | 4.90 |
| E                    | 5.90 | 6.10 | 6.00 |
| E1                   | 3.80 | 3.90 | 3.85 |
| E0                   | 3.85 | 3.95 | 3.90 |
| e                    | --   | --   | 1.27 |
| h                    | --   | --   | 0.35 |
| L                    | 0.62 | 0.82 | 0.72 |
| Q                    | 0.60 | 0.70 | 0.65 |
| All Dimensions in mm |      |      |      |

(2) Package Type: MSOP-8

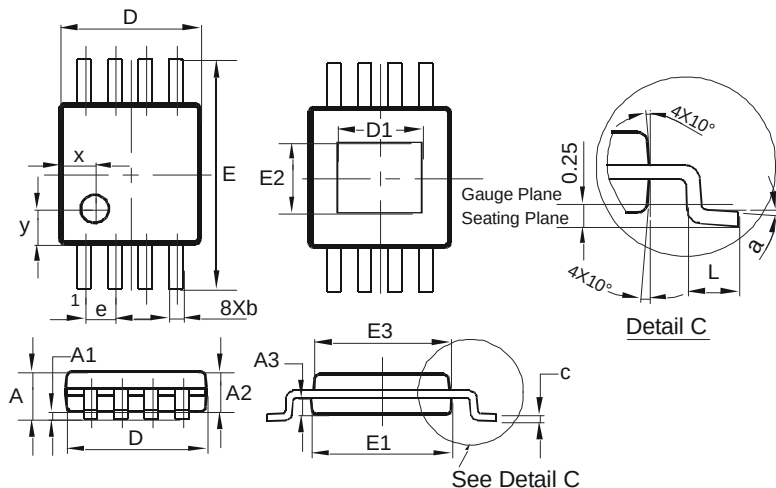


| MSOP-8               |      |      |       |
|----------------------|------|------|-------|
| Dim                  | Min  | Max  | Typ   |
| A                    | -    | 1.10 | -     |
| A1                   | 0.05 | 0.15 | 0.10  |
| A2                   | 0.75 | 0.95 | 0.86  |
| A3                   | 0.29 | 0.49 | 0.39  |
| b                    | 0.22 | 0.38 | 0.30  |
| c                    | 0.08 | 0.23 | 0.15  |
| D                    | 2.90 | 3.10 | 3.00  |
| E                    | 4.70 | 5.10 | 4.90  |
| E1                   | 2.90 | 3.10 | 3.00  |
| E3                   | 2.85 | 3.05 | 2.95  |
| e                    | -    | -    | 0.65  |
| L                    | 0.40 | 0.80 | 0.60  |
| a                    | 0°   | 8°   | 4°    |
| x                    | -    | -    | 0.750 |
| y                    | -    | -    | 0.750 |
| All Dimensions in mm |      |      |       |

# Package Outline Dimensions (Cont.)

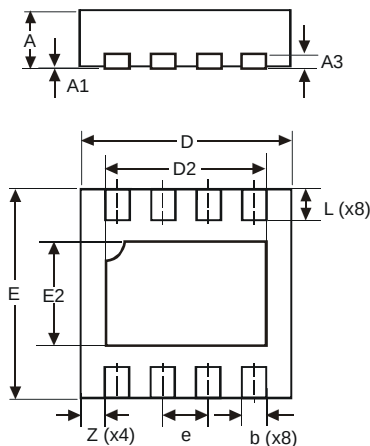
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

## (3) Package Type: MSOP-8EP



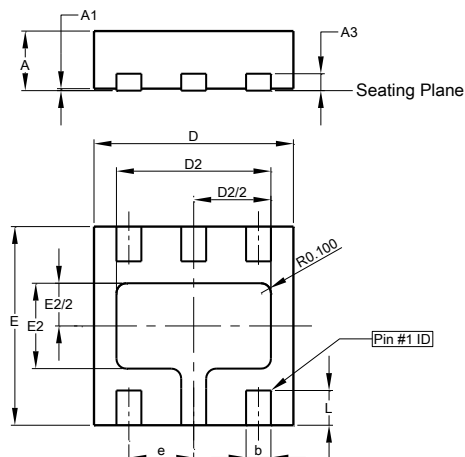
| MSOP-8EP             |      |      |       |
|----------------------|------|------|-------|
| Dim                  | Min  | Max  | Typ   |
| A                    | -    | 1.10 | -     |
| A1                   | 0.05 | 0.15 | 0.10  |
| A2                   | 0.75 | 0.95 | 0.86  |
| A3                   | 0.29 | 0.49 | 0.39  |
| b                    | 0.22 | 0.38 | 0.30  |
| c                    | 0.08 | 0.23 | 0.15  |
| D                    | 2.90 | 3.10 | 3.00  |
| D1                   | 1.60 | 2.00 | 1.80  |
| E                    | 4.70 | 5.10 | 4.90  |
| E1                   | 2.90 | 3.10 | 3.00  |
| E2                   | 1.30 | 1.70 | 1.50  |
| E3                   | 2.85 | 3.05 | 2.95  |
| e                    | -    | -    | 0.65  |
| L                    | 0.40 | 0.80 | 0.60  |
| a                    | 0°   | 8°   | 4°    |
| x                    | -    | -    | 0.750 |
| y                    | -    | -    | 0.750 |
| All Dimensions in mm |      |      |       |

## (4) Package Type: U-DFN3030-8 (Type E)



| U-DFN3030-8<br>Type E |      |      |      |
|-----------------------|------|------|------|
| Dim                   | Min  | Max  | Typ  |
| A                     | 0.57 | 0.63 | 0.60 |
| A1                    | 0    | 0.05 | 0.02 |
| A3                    | -    | -    | 0.15 |
| b                     | 0.20 | 0.30 | 0.25 |
| D                     | 2.95 | 3.05 | 3.00 |
| D2                    | 2.15 | 2.35 | 2.25 |
| E                     | 2.95 | 3.05 | 3.00 |
| e                     | -    | -    | 0.65 |
| E2                    | 1.40 | 1.60 | 1.50 |
| L                     | 0.30 | 0.60 | 0.45 |
| Z                     | -    | -    | 0.40 |
| All Dimensions in mm  |      |      |      |

## (5) Package Type: U-DFN2020-6

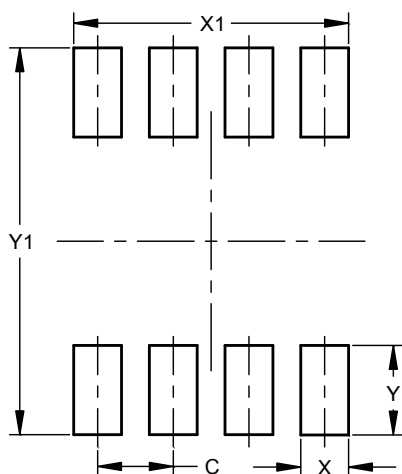


| U-DFN2020-6          |      |       |      |
|----------------------|------|-------|------|
| Dim                  | Min  | Max   | Typ  |
| A                    | 0.57 | 0.63  | 0.60 |
| A1                   | 0    | 0.05  | 0.03 |
| A3                   | -    | -     | 0.15 |
| b                    | 0.20 | 0.30  | 0.25 |
| D                    | 1.95 | 2.075 | 2.00 |
| D2                   | 1.45 | 1.65  | 1.55 |
| e                    | -    | -     | 0.65 |
| E                    | 1.95 | 2.075 | 2.00 |
| E2                   | 0.76 | 0.96  | 0.86 |
| L                    | 0.30 | 0.40  | 0.35 |
| All Dimensions in mm |      |       |      |

## Suggested Pad Layout

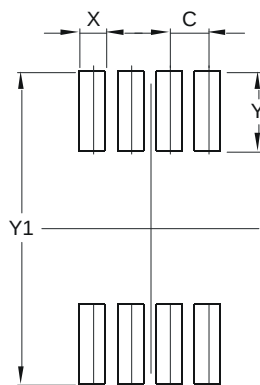
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (1) Package Type: SO-8



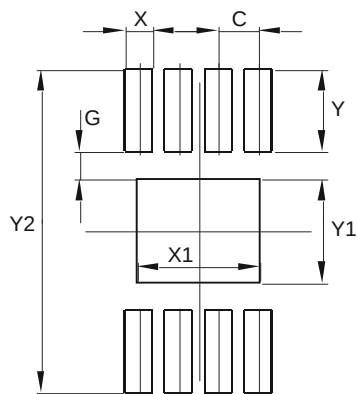
| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 1.27          |
| X          | 0.802         |
| X1         | 4.612         |
| Y          | 1.505         |
| Y1         | 6.50          |

### (2) Package Type: MSOP-8



| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 0.650         |
| X          | 0.450         |
| Y          | 1.350         |
| Y1         | 5.300         |

### (3) Package Type: MSOP-8EP

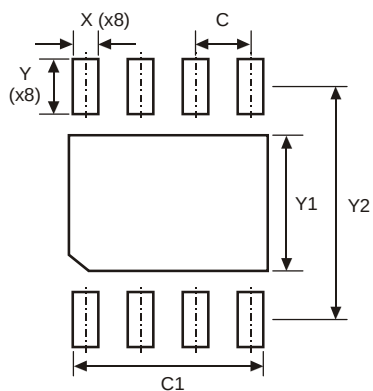


| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 0.650         |
| G          | 0.450         |
| X          | 0.450         |
| X1         | 2.000         |
| Y          | 1.350         |
| Y1         | 1.700         |
| Y2         | 5.300         |

## Suggested Pad Layout (Cont.)

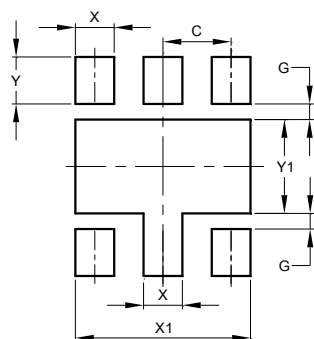
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (4) Package Type: U-DFN3030-8 (Type E)



| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 0.65          |
| C1         | 2.35          |
| X          | 0.30          |
| Y          | 0.65          |
| Y1         | 1.60          |
| Y2         | 2.75          |

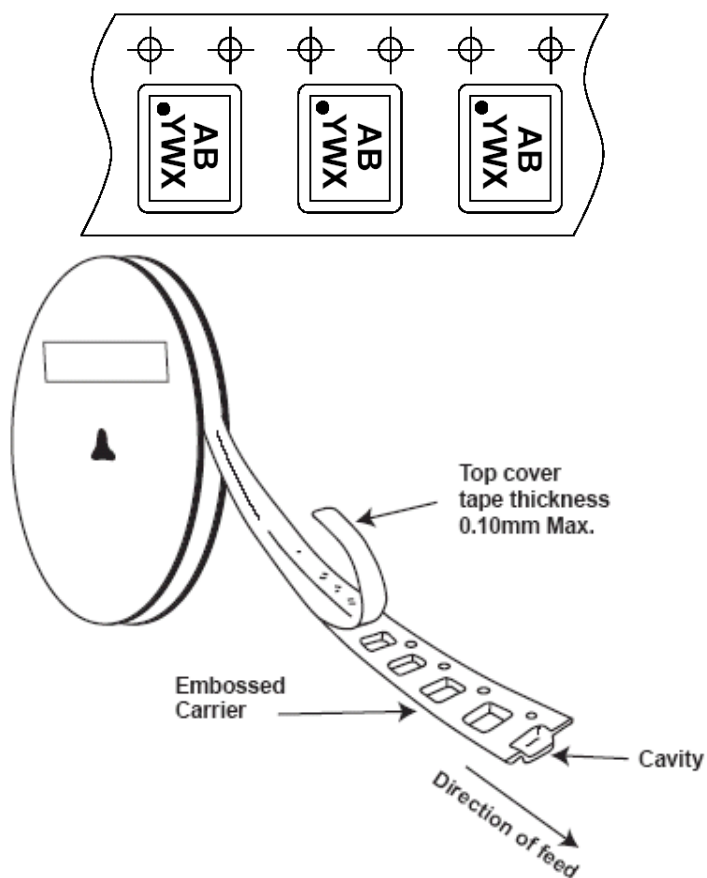
### (5) Package Type: U-DFN2020-6



| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 0.65          |
| G          | 0.15          |
| X          | 0.37          |
| X1         | 1.67          |
| Y          | 0.45          |
| Y1         | 0.90          |

## Taping Orientation (Note 13)

For U-DFN2020-6 and U-DFN3030-8 (Type E)



Note: 13. The taping orientation of the other package type can be found on our website at <http://www.diodes.com/datasheets/ap02007.pdf>



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