# VS-MBRA120TRPbF

# Vishay High Power Products Schottky Rectifier, 1.0 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 25 °C	0.42	0.45	- V	
		2 A		0.46	0.52		
		1 A	- T <sub>J</sub> = 100 °C	0.33	0.37		
		2 A		0.39	0.45		
		1 A	- T <sub>J</sub> = 125 °C	0.30	0.35		
		2 A		0.36	0.43		
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.015	0.2	mA	
		T <sub>J</sub> = 100 °C		2.0	6.0		
		T <sub>J</sub> = 125 °C		7.0	20		
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		110	-	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	-	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> - 10 000		V/µs			

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 65 to 150	°C		
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation	35	°C/W		
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80			
Approximate weight			0.07	g		
			0.002	oz.		
Device marking		Case style SMA (similar D-64)	V12A			

#### Notes

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ 

<sup>(2)</sup> Mounted 1" square PCB



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 $\dot{D} = 0.20$ D = 0.25

D = 0.33 D = 0.50D = 0.75

DC

## Schottky Rectifier, 1.0 A Vishay High Power Products

Square wave

See note (1)

0.4

0.6 0.8

I<sub>F(AV)</sub> - Average Forward Current (A)

1.0 1.2 1.4 1.6

0.2

155

150

145

140

135

130

0

Allowable Case Temperature (°C)

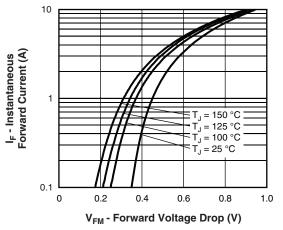
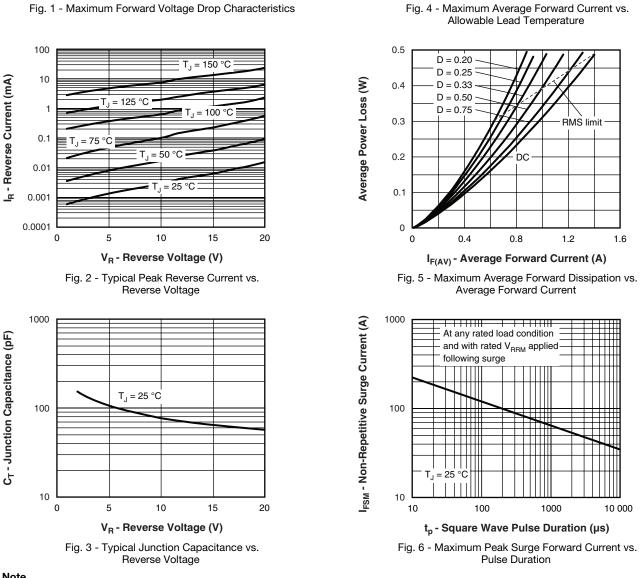


Fig. 1 - Maximum Forward Voltage Drop Characteristics





<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

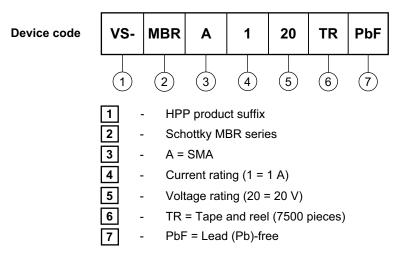
 $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} \times I_{R} (1 - D);$ 

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### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95018			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

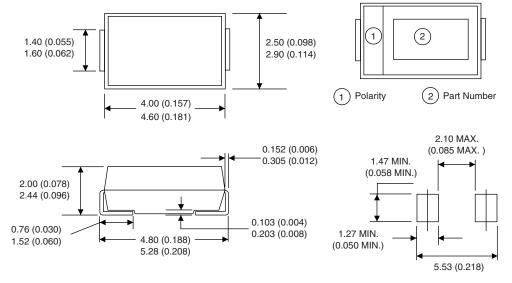


### **Outline Dimensions**

### Vishay High Power Products

**SMA** 

### **DIMENSIONS** in millimeters (inches)



Soldering pad



Vishay

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