# **Absolute Maximum Rating**

Rating	Symbol	Value	Units
Peak Pulse Power (tp = $10/1000\mu$ s), T= $25^{\circ}$ C	Р <sub>рк</sub>	300	W
Peak Pulse Current (tp = 10/1000µs), T=25°C	I <sub>PP</sub>	12	А
Peak Pulse Current (tp = $8/20\mu$ s), T= $25^{\circ}$ C	I <sub>PP</sub>	100	А
ESD per IEC 61000-4-2 (Air) <sup>(2)</sup> ESD per IEC 61000-4-2 (Contact) <sup>(2)</sup>	V <sub>ESD</sub>	±20 ±15	kV
Operating Temperature	T <sub>J</sub>	-55 to +125	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

# **Electrical Characteristics (T=25°C unless otherwise specified)**

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>					12	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> = 1mA Pins 1, 2 or 3 to Pins 4, 5 and Tab⁴		13.3	15	18.5	V
Powerse Leakage Current	1	$V_{RWM} = 12V$	T = 25°C			0.1	
Reverse Leakage Current	$\begin{bmatrix} \mathbf{I}_{R} \\ \mathbf{Pins} \\ 1, 2 \\ \text{ or } 3 \\ \text{ to Pins } 4, 5 \\ \text{ and } Tab^{4} \end{bmatrix}$	T = 125°C			0.5	μΑ	
Clamping Voltage	V <sub>c</sub>	$t_p = 10/1000$ μs Pins 1, 2 or 3 to Pins 4, 5 and Tab <sup>4</sup>	I <sub>PP</sub> = 12A			25	V
Clamping Voltage	V <sub>c</sub>	$t_p = 8/20\mu s$ Pins 1, 2 or 3 to Pins 4, 5 and Tab <sup>4</sup>	I <sub>PP</sub> = 100A			40	V
Dynamic Resistance <sup>2,3</sup>	R <sub>dyn</sub>	tp = 0.2/100ns Pin 1 and 4			0.11		Ω
Junction Capacitance	C	$V_{R} = 0V$ , f = 1MHz Pins 1 or 3 to Pins 4, 5 and Tab <sup>4</sup>				12	pF

Notes

1) Measured with a 40dB attenuator, 50 Ohm scope input impedance, 2GHz bandwidth. ESD gun return path connected to ESD ground plane.

2) Transmission Line Pulse Test (TLP) Settings:  $t_p = 100ns$ ,  $t_r = 0.2ns$ ,  $I_{TLP}$  and  $V_{TLP}$  averaging window:  $t_1 = 70ns$  to  $t_2 = 90ns$ .

3) Dynamic resistance calculated from  $I_{TLP} = 4A$  to  $I_{TLP} = 16A$ 

4) Pin 4, 5 and center tab are internally connected.

# **Typical Characteristics**

Non-Repetitive Peak Pulse Power vs. Pulse Time(VBus Pin1)







Forward Voltage vs. Peak Pulse Current (t<sub>p</sub>=8/20µs)





Rev 5.0 8/24/2016 www.semtech.com



Clamping Voltage vs. Peak Pulse Current (tp=8/20µs)







Capacitance vs. Reverse Voltage (Data Line Pins 1 and 3)

# **Typical Characteristics (Continued)**









ESD Clamping (-8kV Contact per IEC 61000-4-2)



ESD Clamping (-8kV Contact per IEC 61000-4-2) (VBus Pin 2)





TClamp1202P Final Datasheet Revision Date

Rev 5.0 8/24/2016 www.semtech.com

## **Application Information** Device Connection for Protection of RS-485 Interfaces

EIA RS-485 specifies a  $\pm$ 7V ground difference between devices on the bus. This permits the bus voltage to range from  $\pm$ 12V (5V  $\pm$  7V) to  $\pm$ 7V (0  $\pm$  7V). Because of this requirement, each TClamp1202P can be configured to protect one line for common mode surges or one line pair for differential surges. The example below illustrates how to connect the TClamp1202P for common mode and differential mode protection on an RS-485 interface. For common mode protection, one of the TClamp1202P data pins (pin 1 or 3) is connected to the TX/RX line and the other is connected to ground. Pins 4 and 5 as well as the center tab are not connected. This effectively makes the device look like a bidirectional 12V TVS to the circuit. For differential mode protection, each of the device data pins (1 and 3) are connected to the transceiver RX/TX pins as shown. Again, Pins 4, 5 and the center tab are not connected.



## RS-485 Common Mode and Differential Mode Surge Protection

TClamp1202P	
Final Datasheet	Rev 5.0
Revision Date	8/24/2016

www.semtech.com

# **Outline Drawing - SLP2020P5**



## Land Pattern - SLP2020P5



TClamp1202P	
Final Datasheet	Rev 5.0
Revision Date	8/24/2016

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# **Marking Code**



Note: YYWW = Alphanumeric character date code

# **Tape and Reel Specification**



# **Ordering Information**

Part Number	Qty per Reel	Reel Size
TClamp1202P. TGT	10,000	13″



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TClamp1202P Final Datasheet 5.0 Revision date 8/24/2016