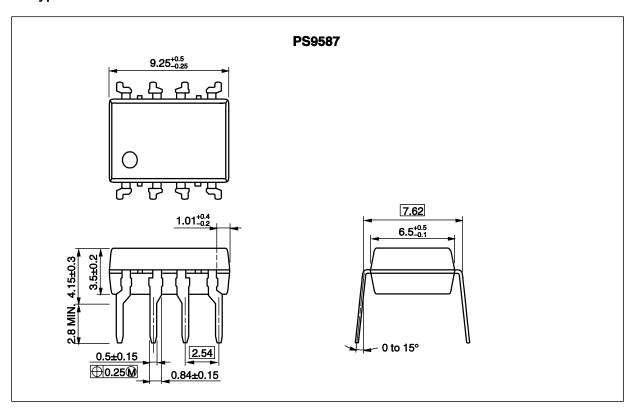
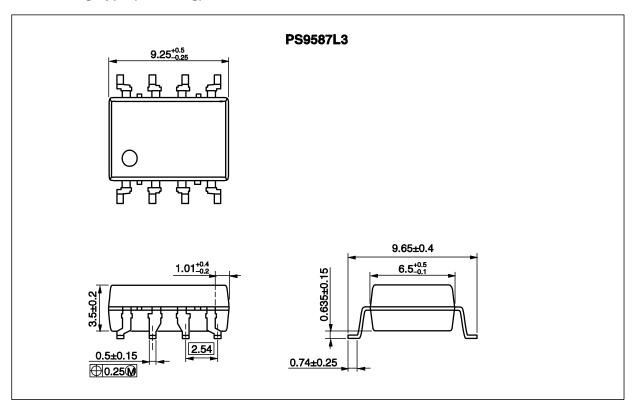
# <R> PACKAGE DIMENSIONS (UNIT: mm)

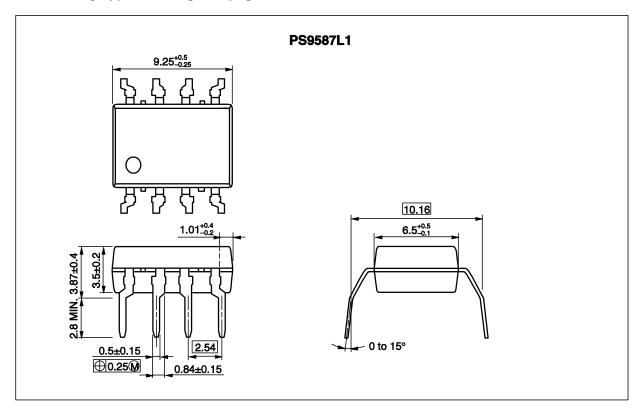
# **DIP Type**



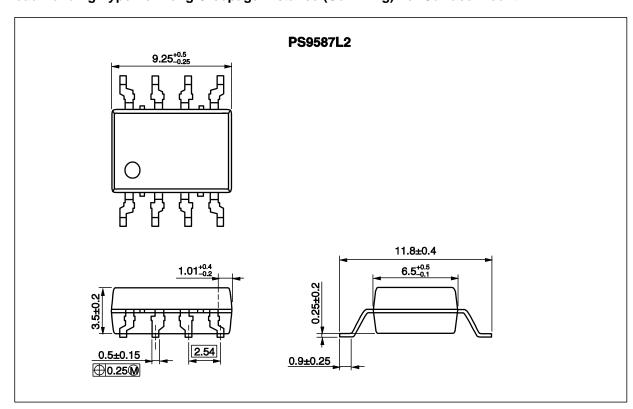
## Lead Bending Type (Gull-wing) For Surface Mount



## **Lead Bending Type For Long Creepage Distance**



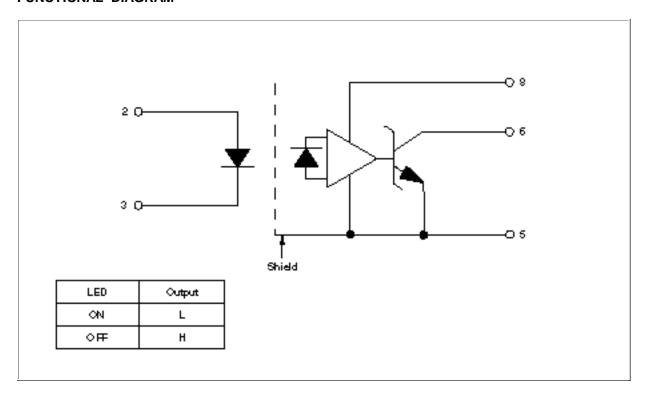
# Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount



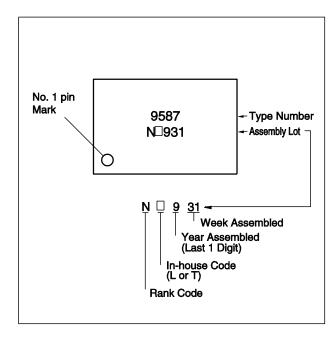
### PHOTOCOUPLER CONSTRUCTION

Parameter	PS9587, PS9587L3	PS9587L1, PS9587L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

## **FUNCTIONAL DIAGRAM**



### <R> MARKING EXAMPLE



### ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number* <sup>1</sup>
PS9587	PS9587-AX	Pb-Free	Magazine case 50 pcs	Standard products	PS9587
PS9587L1	PS9587L1-AX	(Ni/Pd/Au)		(UL, CSA, BSI,	PS9587L1
PS9587L2	PS9587L2-AX			SEMKO, NEMKO,	PS9587L2
PS9587L3	PS9587L3-AX			DEMKO, FIMKO	PS9587L3
PS9587L2-E3	PS9587L2-E3-AX		Embossed Tape 1 000 pcs/reel	approved)	PS9587L2
PS9587L3-E3	PS9587L3-E3-AX				PS9587L3
PS9587-V	PS9587-V-AX		Magazine case 50 pcs	DIN EN60747-5-2	PS9587
PS9587L1-V	PS9587L1-V-AX			(VDE0884 Part2)	PS9587L1
PS9587L2-V	PS9587L2-V-AX			Approved (Option)	PS9587L2
PS9587L3-V	PS9587L3-V-AX				PS9587L3
PS9587L2-V-E3	PS9587L2-V-E3-AX		Embossed Tape 1 000 pcs/reel		PS9587L2
PS9587L3-V-E3	PS9587L3-V-E3-AX				PS9587L3

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current*1	İF	30	mA
	Reverse Voltage	Vr	5	V
Detecto r	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	Output Current	lo	25	mA
	Power Dissipation*2	Pc	40	mW
Isolation	Voltage <sup>*3</sup>	BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		T <sub>stg</sub>	_55 to +125	°C

<sup>\*1</sup> Reduced to 0.3 mA/ $^{\circ}$ C at T<sub>A</sub> = 25 $^{\circ}$ C or more.

# RECOMMENDED OPERATING CONDITIONS (TA = 25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
High Level Input Current	lғн	6.3	10	12.0	mA
Low Level Input Voltage	V <sub>FL</sub>	0		0.8	V
Supply Voltage	Vcc	4.5	5.0	5.5	V
TTL ( $R_L = 1 \text{ k}\Omega$ , loads)	N			5	
Pull-up Resistance	R∟	330		4 k	Ω

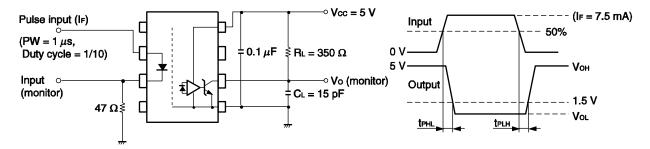
<sup>\*2</sup> Applies to output pin Vo (Collector pin). Reduced to 1.5 mW/ $^{\circ}$ C at T<sub>A</sub> = 65 $^{\circ}$ C or more.

<sup>\*3</sup> AC voltage for 1 minute at  $T_A = 25$ °C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.

# ELECTRICAL CHARACTERISTICS ( $T_A = -40 \text{ to } +85^{\circ}\text{C}$ , unless otherwise specified)

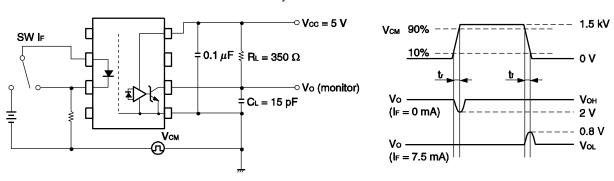
	Parameter	Symbol		Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25°C		1.4	1.65	1.8	V
	Reverse Current	IR	VR = 3 V, TA =	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25°C			10	μA
	Terminal Capacitance	Ct	V <sub>F</sub> = 0 V, f = 1	MHz, T <sub>A</sub> = 25°C		30	150	pF
Detector	High Level Output Current	Іон	Vcc = Vo = 5.5	5 V, VF = 0.8 V		1	100	μA
	Low Level Output Voltage*2	Vol	Vcc = 5.5 V, I <sub>F</sub>	= 5 mA, lo <sub>L</sub> = 13 mA		0.2	0.6	V
	High Level Supply Current	Іссн	Vcc = 5.5 V, I <sub>F</sub>	= 0 mA, Vo = Open		5	8	mA
	Low Level Supply Current	ICCL	Vcc = 5.5 V, IF	= 10 mA, Vo = Open		9	11	mA
Coupled	Threshold Input Current (H $\rightarrow$ L)	IFHL		T <sub>A</sub> = 25°C			3.3	mA
			Vcc = 5 V, Vo	= 0.8 V, $R_L$ = 350 $\Omega$		1.5	5	
	Isolation Resistance	Ri-o	Vio = 1 kVDC,	RH = 40 to 60%, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω
	Isolation Capacitance	Cl-o	V = 0 V, f = 1	MHz, T <sub>A</sub> = 25°C		0.9	5	pF
	Propagation Delay Time	<b>t</b> PHL	Vcc = 5 V,	T <sub>A</sub> = 25°C		35	75	ns
	$(H \rightarrow L)^{*3}$		VTHIL = VTHLH =	1.5 V,			100	
	Propagation Delay Time	<b>t</b> PLH	$R_L = 350 \Omega$ ,	T <sub>A</sub> = 25°C		45	75	ns
	$(L \rightarrow H)^{*3}$		IF = 7.5 mA, C	L = 15 pF			100	
	Rise Time	tr				20		ns
	Fall Time	t <sub>f</sub>				10		ns
	Pulse Width Distortion (PWD) *3	tphl-tplh				10	50	ns
	Propagation Delay Skew	<b>t</b> PSK					60	ns
	Common Mode Transient Immunity at High Level Output*4	СМн	$\label{eq:Vcc} \begin{aligned} \text{Vcc} &= 5 \text{ V, T}_{\text{A}} = 25^{\circ}\text{C, I}_{\text{F}} = 0 \text{ mA,} \\ \text{Vo}_{\text{(MIN.)}} &= 2 \text{ V, Vcm} = 1.5 \text{ kV, RL} = 350 \ \Omega \end{aligned}$		15			kV/µs
	Common Mode Transient Immunity at Low Level Output <sup>4</sup>		1	25°C, I <sub>F</sub> = 7.5 mA, V <sub>CM</sub> = 1.5 kV, R <sub>L</sub> = 350 $\Omega$	15			kV/μs

- \*1 Typical values at T<sub>A</sub> = 25°C
- \*2 Because Vo∟ of 2 V or more may be output when LED current is input and when output power supply is on and off, confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- \*3 Test circuit for propagation delay time



Remark CL includes probe and stray wiring capacitance.

\*4 Test circuit for common mode transient immunity



Remark CL includes probe and stray wiring capacitance.

### **USAGE CAUTIONS**

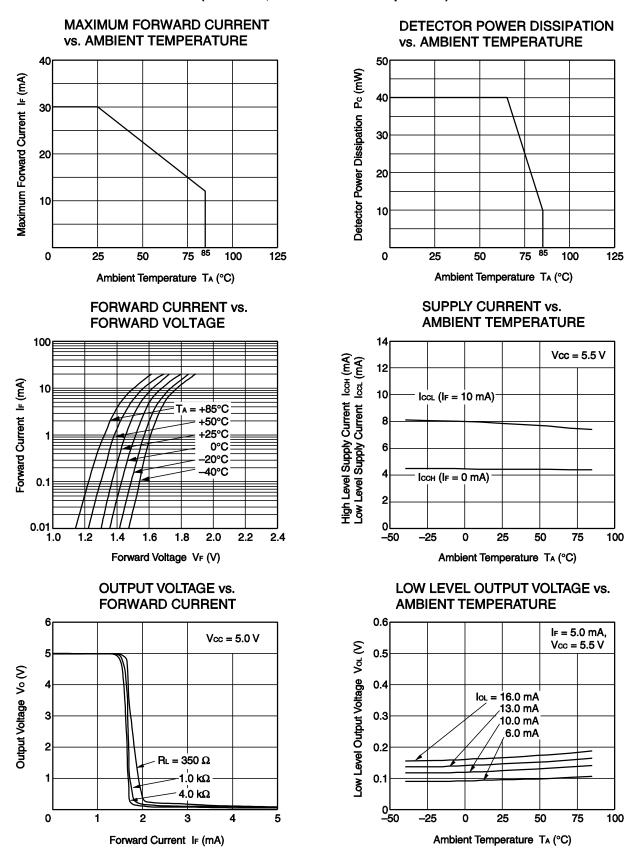
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1  $\mu$ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pins 1, 4 (which is an NC\*1 pin) can either be connected directly to the GND pin on the LED side or left open. Also, Pin 7 (which is an NC\*1 pin) can either be connected directly to the GND pin on the detector side or left open.

Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.

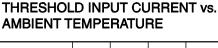
- \*1 NC: Non-Connection (No Connection)
- 4. Avoid storage at a high temperature and high humidity.

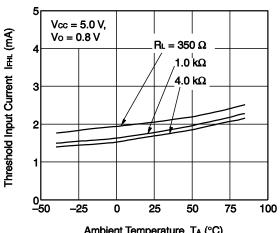
<R>

TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

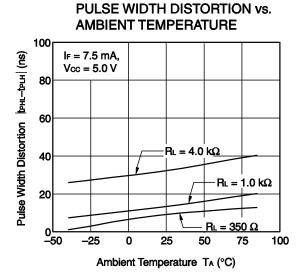


**Remark** The graphs indicate nominal characteristics.

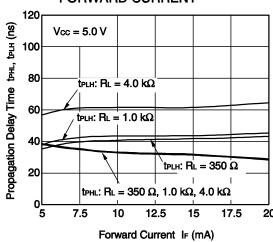




# Ambient Temperature T<sub>A</sub> (°C)

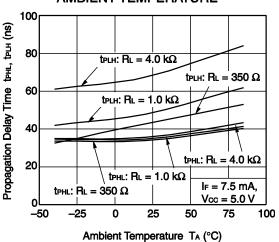


# PROPAGATION DELAY TIME vs. FORWARD CURRENT

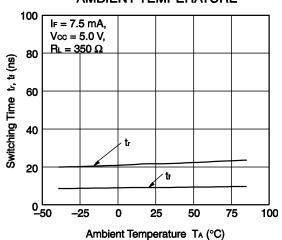


Remark The graphs indicate nominal characteristics.

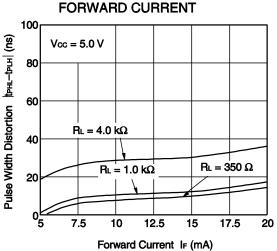
# PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



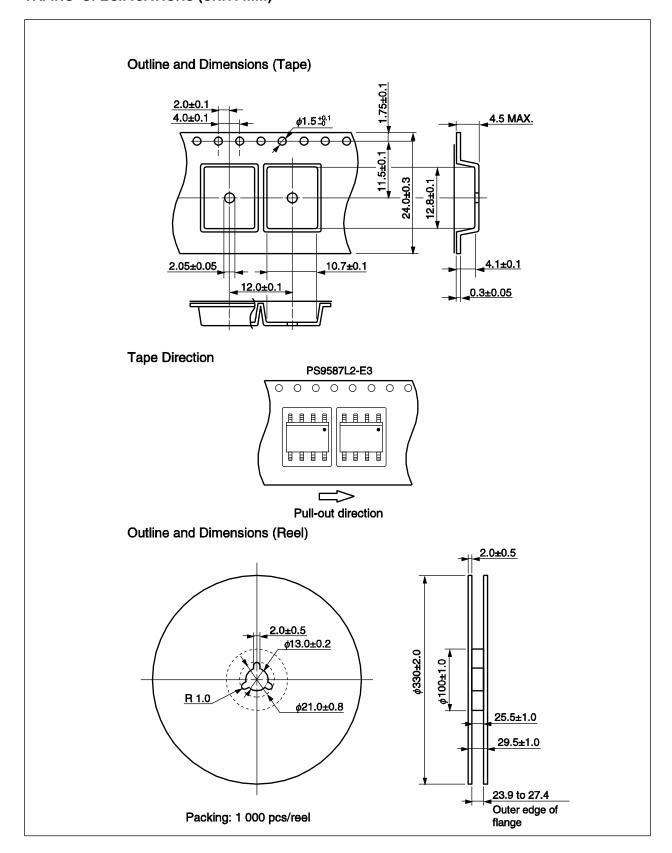
# SWITCHING TIME vs. AMBIENT TEMPERATURE

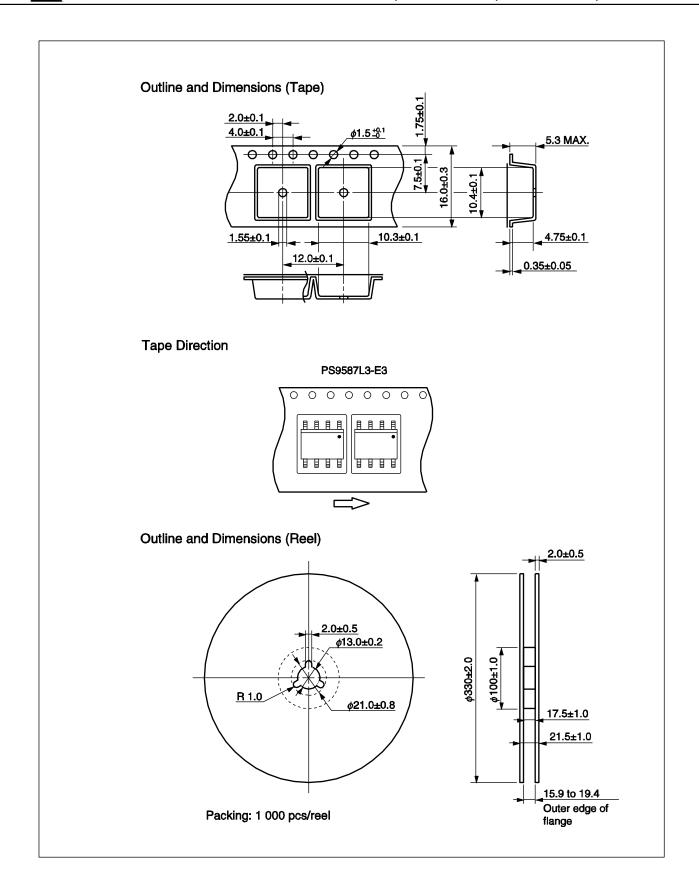


# PULSE WIDTH DISTORTION vs.

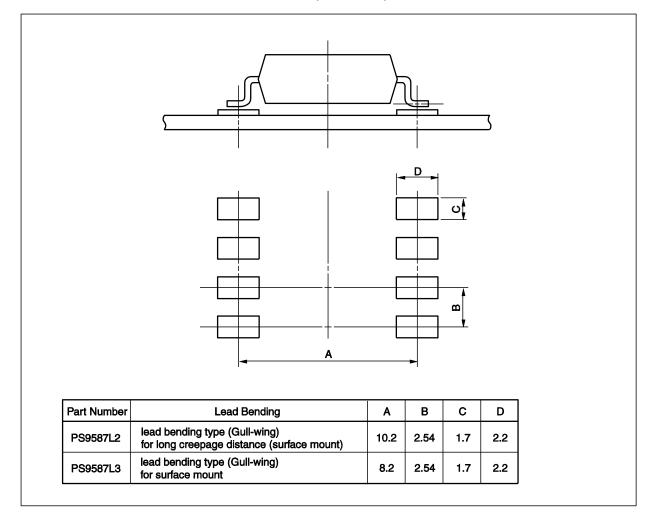


# TAPING SPECIFICATIONS (UNIT: mm)





# RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



### NOTES ON HANDLING

### 1. Recommended soldering conditions

### (1) Infrared reflow soldering

Peak reflow temperature
 260°C or below (package surface temperature)

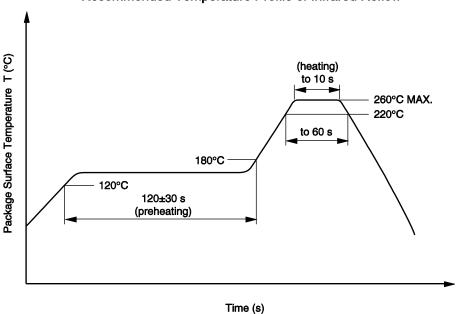
Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

Flux
 Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

### Recommended Temperature Profile of Infrared Reflow



### (2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

Number of times
 One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

### (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

### (4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

## 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between Vcc-GND at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

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# <R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IDRM}, P_d < 5 \ pC$	Ulorm Upr	1 130 1 695	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{\text{IORM}},  P_d < 5  pC$	Upr	2 119	$V_{peak}$
Highest permissible overvoltage	Utr	8 000	V <sub>peak</sub>
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +85	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at T}_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at T}_A \text{ MAX. at least } 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I <sub>F</sub> , Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
Vio = 500 V dc at TA = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

### Caution

**GaAs Products** 

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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