# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode <sup>[1]</sup>	1 2	1 1 2
2	А	anode	SOD123	sym001

<sup>[1]</sup> The marking bar indicates the cathode.

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMEG2005EGW	SOD123	Plastic surface-mounted package; 2 leads	SOD123			

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG2005EGW	G1

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	20	V
l <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 55 °C		-	0.5	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le 120$ °C; square wave	[1]	-	0.5	А
		$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le 145$ °C; square wave		-	0.5	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	7	Α
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	10	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	400	mW
			[1]	-	660	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	III II EE ali	[1] [2]	-	-	310	K/W
			[1] [3]	_	_	190	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[4]	-	-	29	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Soldering point of cathode tab.

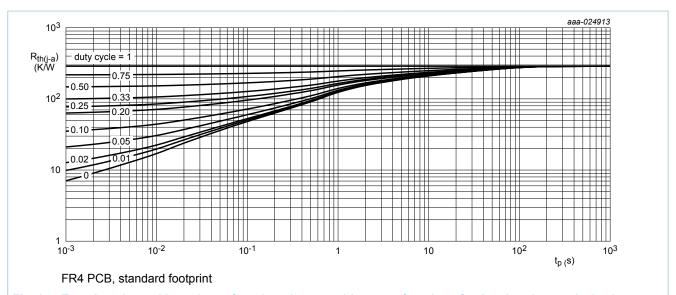


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

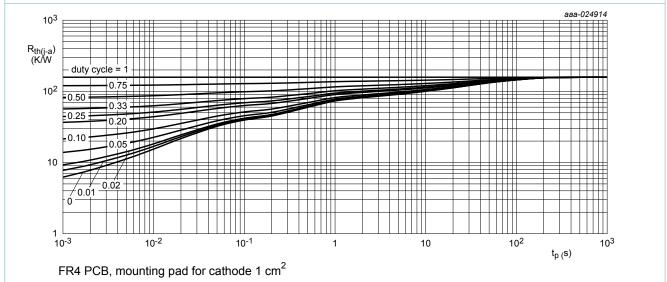


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 1 \text{ mA}; t_p \le 300 \text{ µs}; \delta \le 0.02 ;$ $T_j = 25 \text{ °C}$		20	-	-	V
V <sub>F</sub>	forward voltage	$I_F = 0.1 \text{ mA}; t_p \le 300 \text{ µs}; \delta \le 0.02 ;$ $T_j = 25 \text{ °C}$		-	90	130	mV
		$I_F = 1 \text{ mA}; t_p \le 300  \mu s; \delta \le 0.02 ; T_j = 25 °C$		-	150	190	mV
		$I_F$ = 10 mA; $t_p \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C		-	210	240	mV
		$I_F$ = 100 mA; $t_p \le 300 \ \mu s; \ \delta \le 0.02 \ ;  T_j = 25 °C$		-	280	330	mV
		$I_F$ = 500 mA; $t_p \le 300 \ \mu s; \ \delta \le 0.02 \ ;  T_j = 25 °C$		-	355	390	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	15	40	μA
		V <sub>R</sub> = 20 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	40	200	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>i</sub> = 25 °C		-	66	80	pF

#### [1] Very short test pulse to prevent junction self-heating.

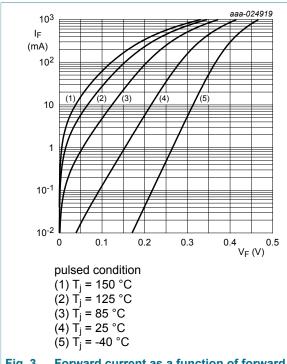


Fig. 3. Forward current as a function of forward voltage; typical values

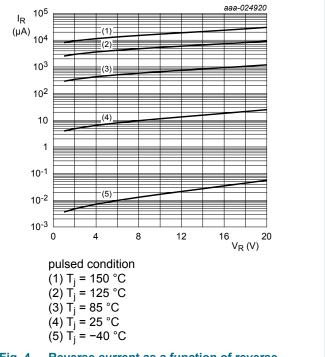
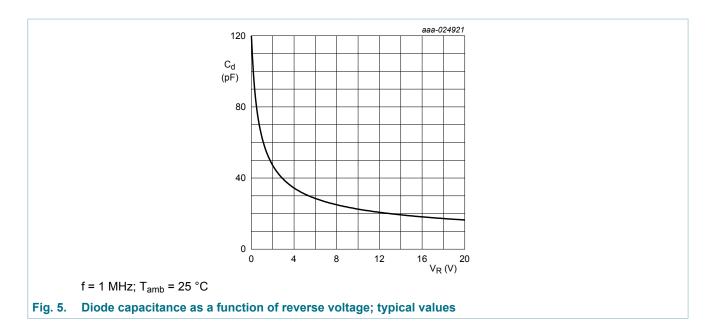
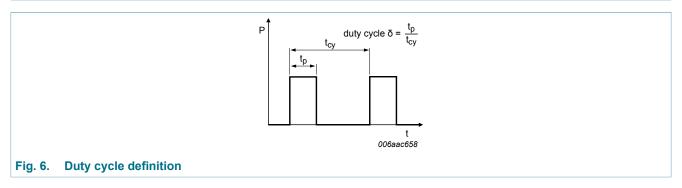


Fig. 4. Reverse current as a function of reverse voltage; typical values



## 11. Test information

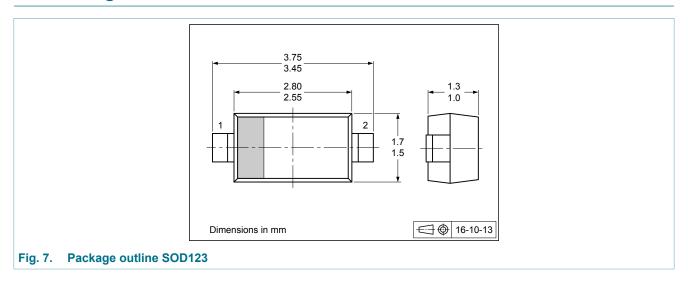


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

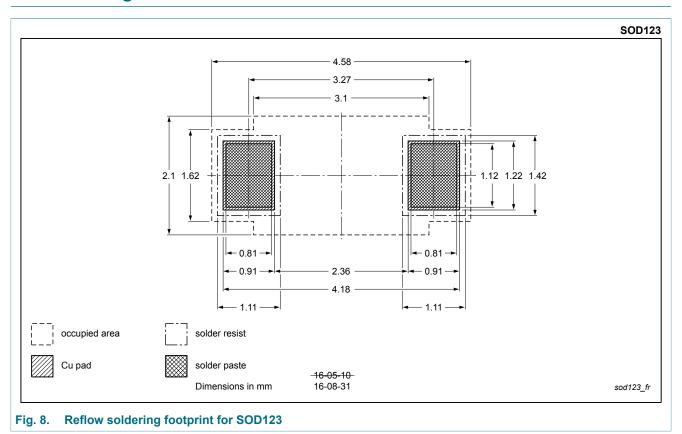
#### **Quality information**

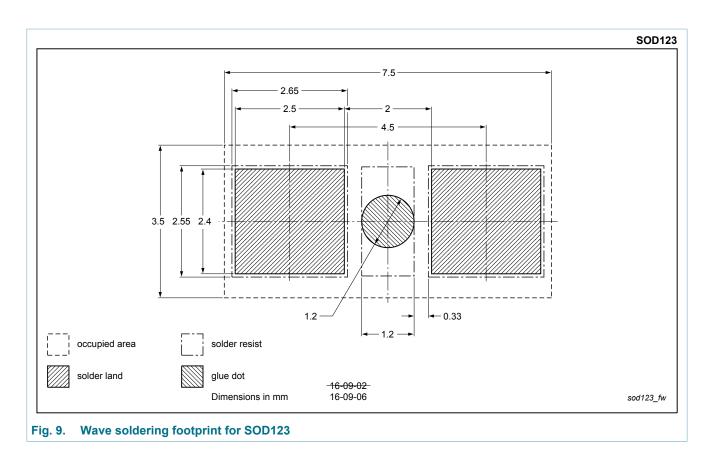
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



# 13. Soldering





# 14. Revision history

#### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2005EGW v.2	20161205	Product data sheet	-	PMEG2005EGW v.1
Modifications:	Product status chan	ged		
PMEG2005EGW v.1	20161122	Preliminary data sheet	-	-

# 15. Legal information

#### **Data sheet status**

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMEG2005EGW

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Nexperia PMEG2005EGW

20 V, 0.5 A low VF MEGA Schottky barrier rectifier

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	Features and benefits

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 05 December 2016

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