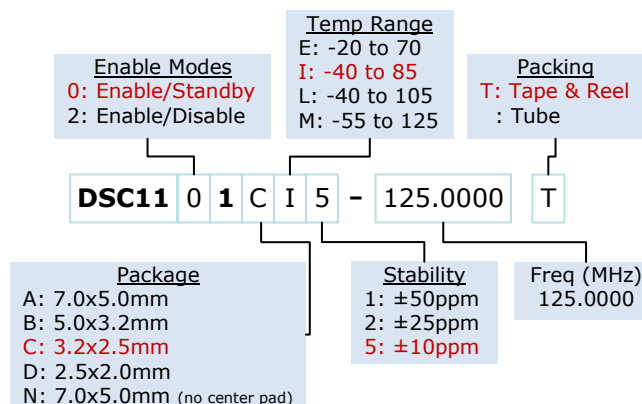


Absolute Maximum Ratings

Item	Min	Max	Unit	Condition
Supply Voltage	-0.3	+4.0	V	
Input Voltage	-0.3	$V_{DD}+0.3$	V	
Junction Temp	-	+150	°C	
Storage Temp	-55	+150	°C	
Soldering Temp	-	+260	°C	40sec max.
ESD	-		V	
HBM		4000		
MM		400		
CDM		1500		

Ordering Code



Note: 1000+ years of data retention on internal memory

Specifications

Parameter		Condition	Min.	Typ.	Max.	Unit
Supply Voltage ¹	V_{DD}		2.25		3.6	V
Supply Current	I_{DD}	EN pin low – output is disabled DSC1101 DSC1121		20	0.095 22	mA
Frequency Stability Ext Comm. & Ind. only All temp ranges All temp ranges	Δf	Includes frequency variations due to initial tolerance, temp. and power supply voltage			±10 ±25 ±50	ppm
Aging	Δf	1 year @25°C			±5	ppm
Startup Time ²	t_{SU}	T=25°C			5	ms
Input Logic Levels Input logic high Input logic low	V_{IH} V_{IL}		0.75x V_{DD} -		- 0.25x V_{DD}	V
Output Disable Time ³	t_{DA}				5	ns
Output Enable Time	t_{EN}	DSC1101 DSC1121			5 20	ms ns
Enable Pull-Up Resistor ⁴		Pull-up resistor exist		40		kΩ
CMOS Output						
Supply Current ⁴	I_{DD}	output is enabled $C_L=15pF$, $F_0=125$ MHz		31	35	mA
Output Logic Levels Output logic high Output logic low	V_{OH} V_{OL}	$I=\pm 6mA$	0.9x V_{DD} -		- 0.1x V_{DD}	V
Output Transition time ³ Rise Time Fall Time	t_R t_F	20% to 80% $C_L=15pF$		1.1 1.3	2 2	ns
Frequency	f_0	All temp range except Auto Auto temp range	2.3		170 100	MHz
Output Duty Cycle	SYM		45		55	%
Period Jitter	J_{PER}	$F_{out}=125MHz$		3		ps _{RMS}
Integrated Phase Noise	J_{PH}	200kHz to 20MHz @ 125MHz 100kHz to 20MHz @ 125MHz 12kHz to 20MHz @ 125MHz		0.3 0.38 1.7		ps _{RMS}

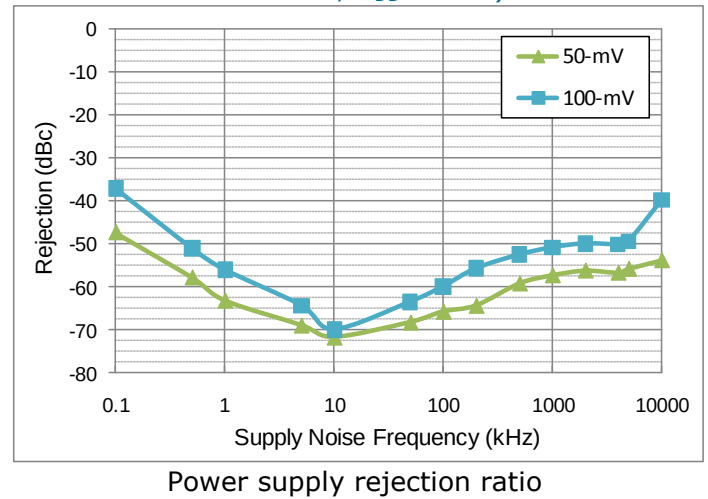
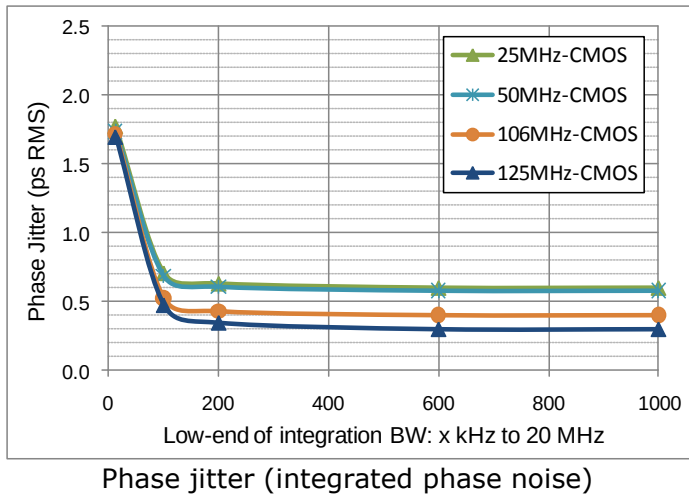
1. Pin 6 V_{DD} should be filtered with 0.1uF capacitor.

2. t_{SU} is time to 100PPM of output frequency after V_{DD} is applied and outputs are enabled.

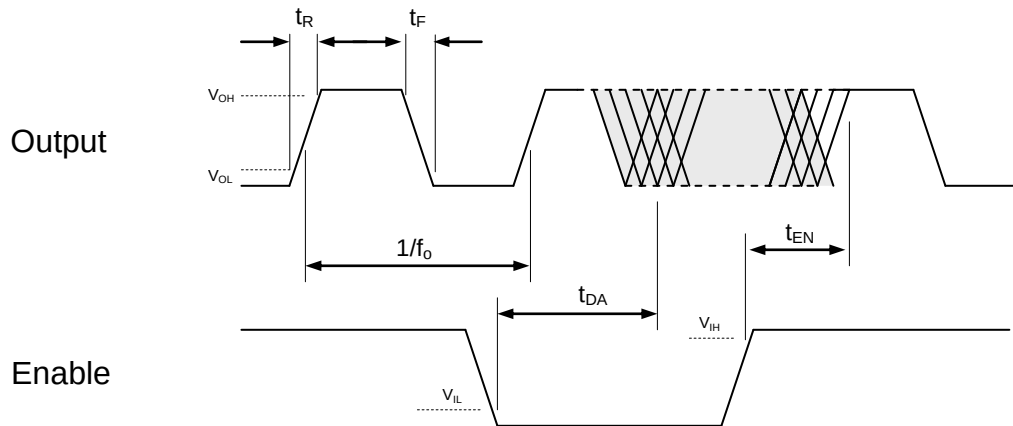
3. Output Waveform and Test Circuit figures below define the parameters.

4. Output is enabled if pad is floated or not connected.

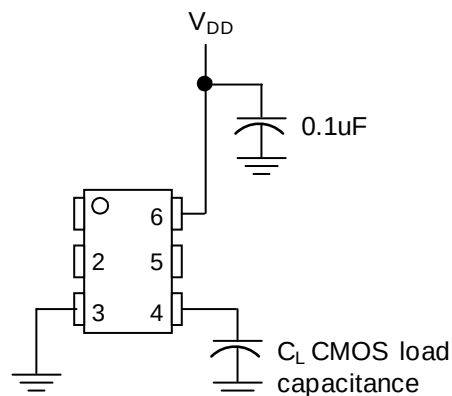
Nominal Performance Parameters (Unless specified otherwise: $T=25^{\circ}\text{C}$, $V_{DD}=3.3\text{ V}$)



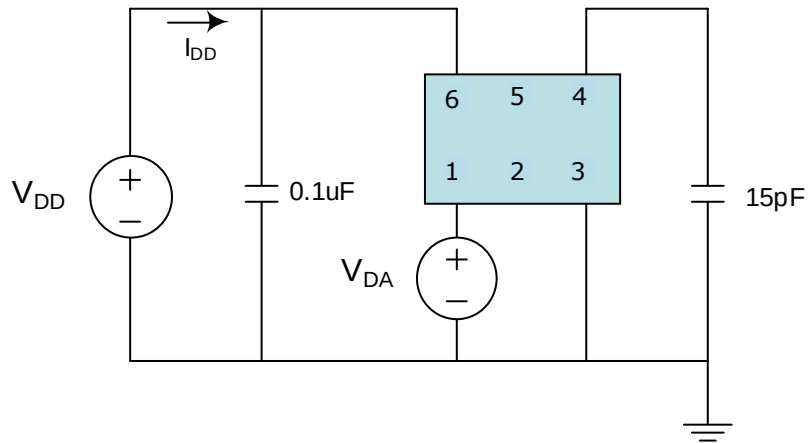
Output Waveform



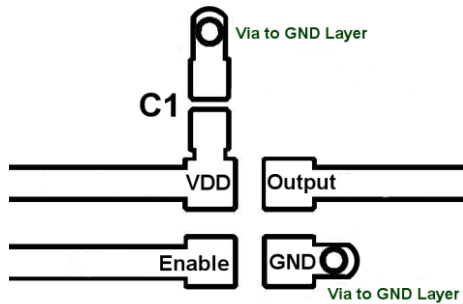
Typical Termination Scheme



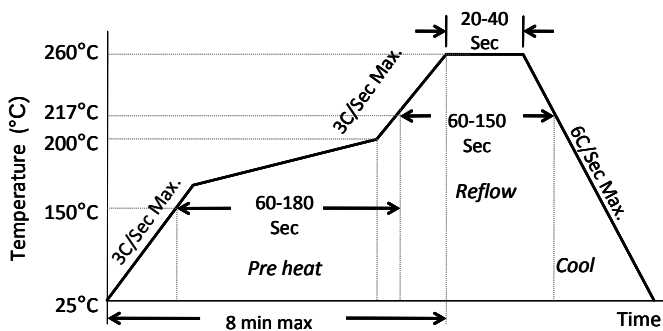
Test Circuit



Board Layout (recommended)



Solder Reflow Profile



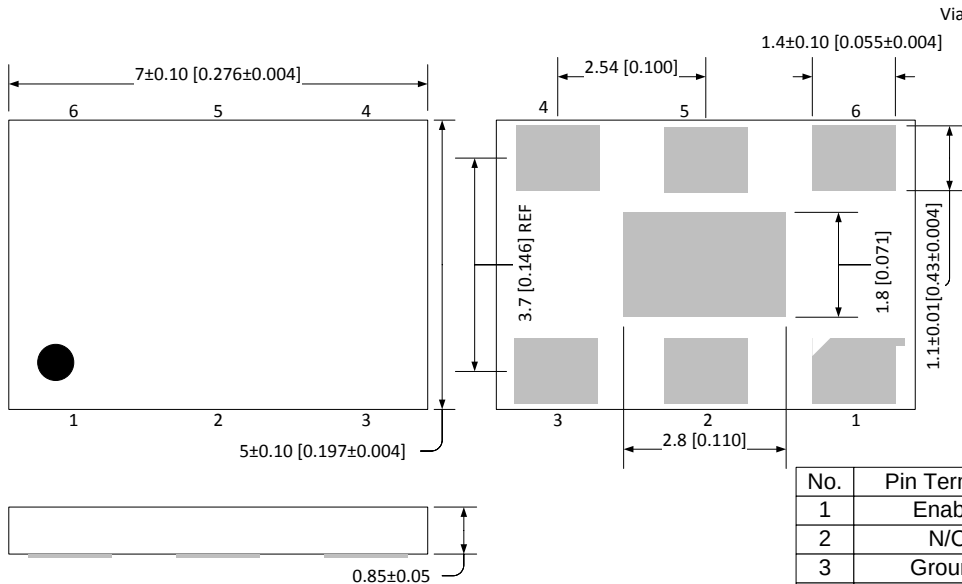
MSL 1 @ 260°C refer to JSTD-020C	
Ramp-Up Rate (200°C to Peak Temp)	3°C/Sec Max.
Preheat Time 150°C to 200°C	60-180 Sec
Time maintained above 217°C	60-150 Sec
Peak Temperature	255-260°C
Time within 5°C of actual Peak	20-40 Sec
Ramp-Down Rate	6°C/Sec Max.
Time 25°C to Peak Temperature	8 min Max.

Package Dimensions

7.0 x 5.0 mm Plastic Package

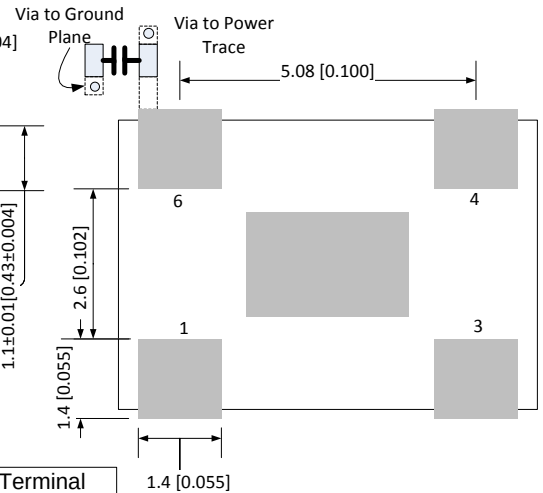
EXTERNAL DIMENSIONS

Units: mm [inches]



RECOMMENDED SOLDER PAD LAYOUT

Units: mm [inches]

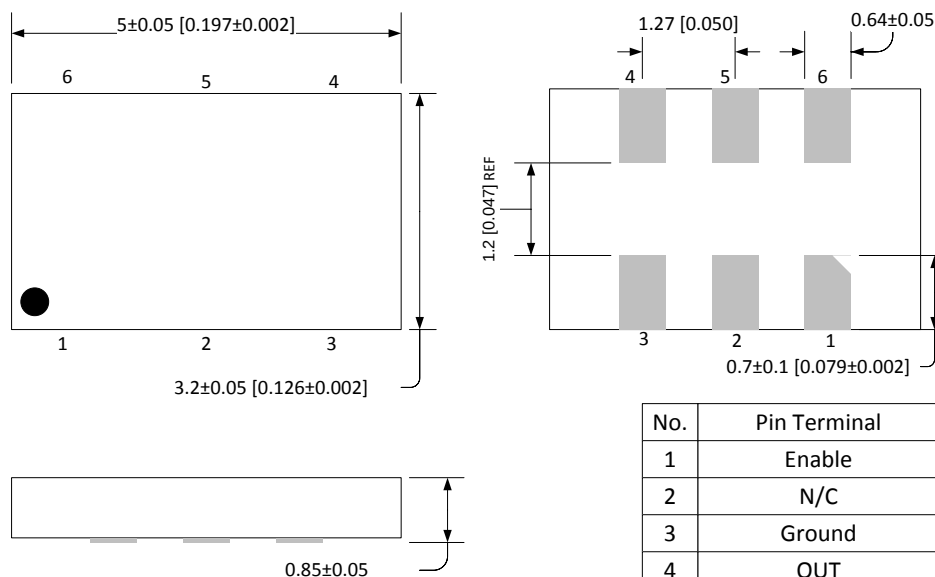


No.	Pin Terminal
1	Enable
2	N/C
3	Ground
4	OUT
5	N/C
6	VDD
PAD	TIE TO GND

5.0 x 3.2 mm Plastic Package

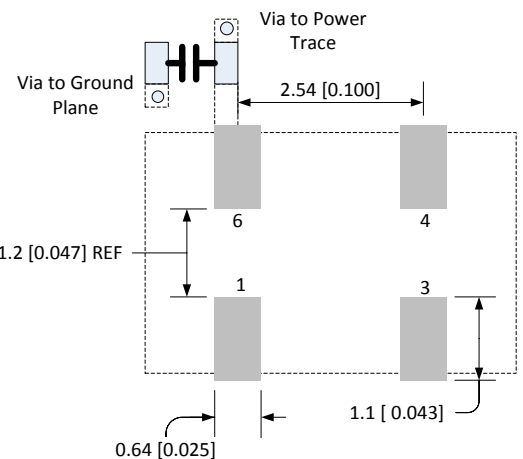
EXTERNAL DIMENSIONS

Units: mm [inches]



RECOMMENDED SOLDER PAD LAYOUT

Units: mm [inches]

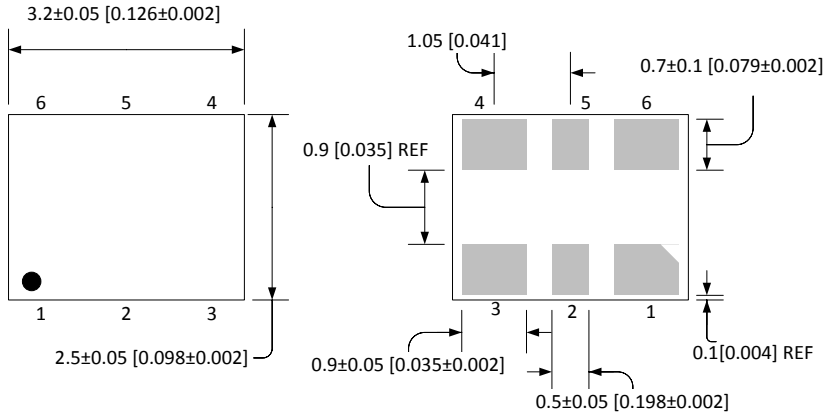


No.	Pin Terminal
1	Enable
2	N/C
3	Ground
4	OUT
5	N/C
6	VDD

3.2 x 2.5 mm Plastic Package

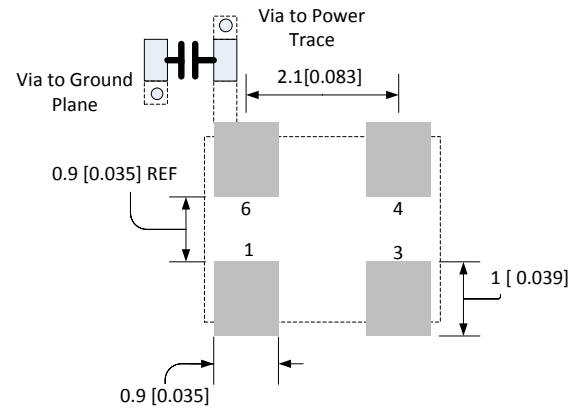
EXTERNAL DIMENSIONS

Units: mm [inches]



RECOMMENDED SOLDER PAD LAYOUT

Units: mm [inches]

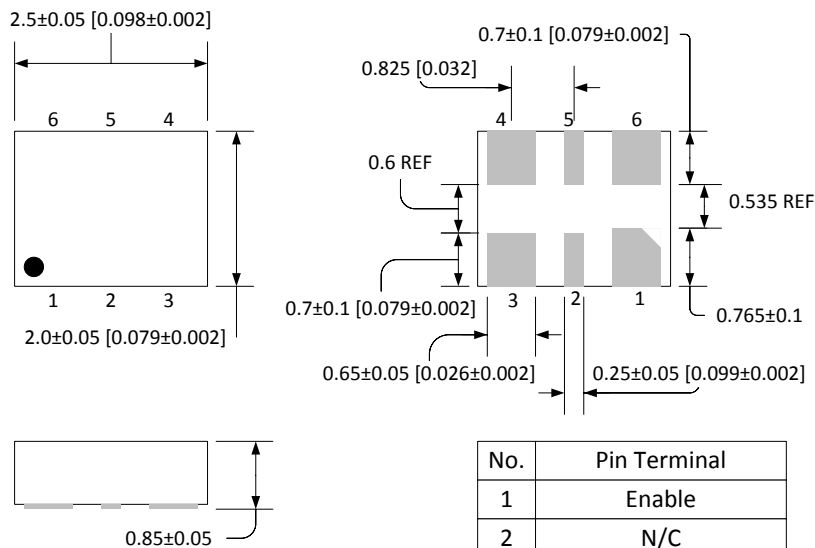


No.	Pin Terminal
1	Enable
2	N/C
3	Ground
4	OUT
5	N/C
6	VDD

2.5 x 2.0 mm Plastic Package

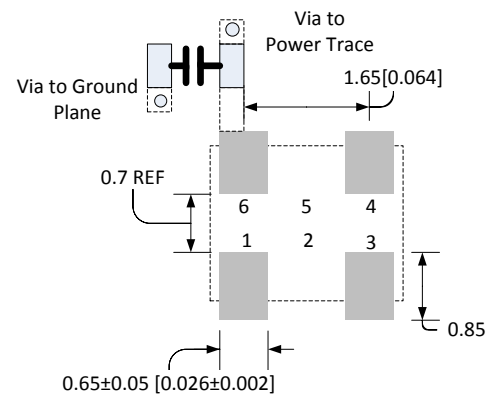
EXTERNAL DIMENSIONS

Units: mm [inches]



RECOMMENDED SOLDER PAD LAYOUT

Units: mm [inches]

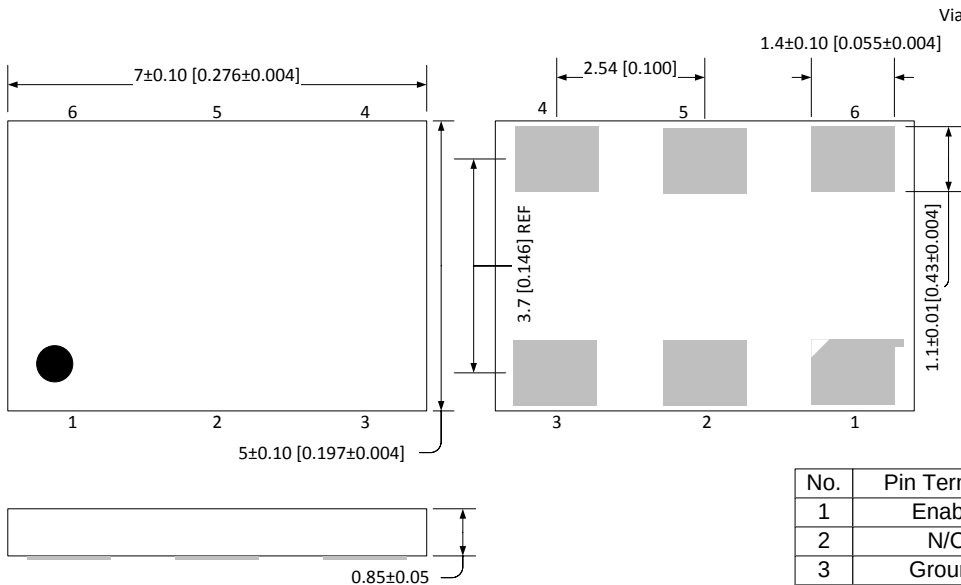


No.	Pin Terminal
1	Enable
2	N/C
3	Ground
4	OUT
5	N/C
6	VDD

7.0 x 5.0 mm Plastic Package (no center pad)

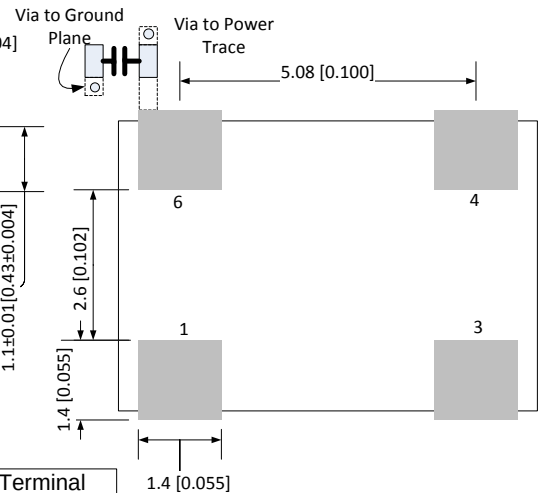
EXTERNAL DIMENSIONS

Units: mm [inches]



RECOMMENDED SOLDER PAD LAYOUT

Units: mm [inches]



No.	Pin Terminal
1	Enable
2	N/C
3	Ground
4	OUT
5	N/C
6	VDD

Disclaimer:

Micrel makes no representations or warranties with respect to the accuracy or completeness of the information furnished in this data sheet. This information is not intended as a warranty and Micrel does not assume responsibility for its use. Micrel reserves the right to change circuitry, specifications and descriptions at any time without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Micrel's terms and conditions of sale for such products, Micrel assumes no liability whatsoever, and Micrel disclaims any express or implied warranty relating to the sale and/or use of Micrel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right.

Micrel Products are not designed or authorized for use as components in life support appliances, devices or systems where malfunction of a product can reasonably be expected to result in personal injury. Life support devices or systems are devices or systems that (a) are intended for surgical implant into the body or (b) support or sustain life, and whose failure to perform can be reasonably expected to result in a significant injury to the user. A Purchaser's use or sale of Micrel Products for use in life support appliances, devices or systems is a Purchaser's own risk and Purchaser agrees to fully indemnify Micrel for any damages resulting from such use or sale.