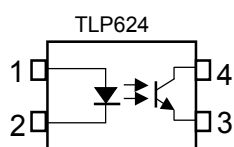
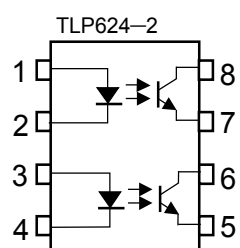


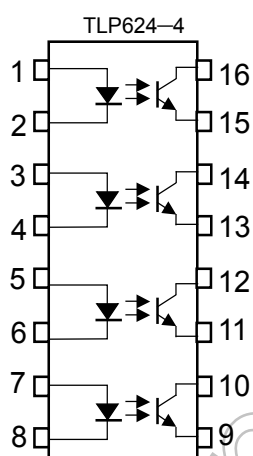
Pin Configurations (top view)



- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector



- 1,3 : Anode
- 2,4 : Cathode
- 5,7 : Emitter
- 6,8 : Collector



- 1,3,5,7: Anode
- 2,4,6,8: Cathode
- 9,11,13,15: Emitter
- 10,12,14,16: Collector

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating		Unit
			TLP624	TLP624-2 TLP624-4	
LED	Forward current	I _F	60	50	mA
	Forward current derating	ΔI _F / °C	-0.7(Ta ≥ 39°C)	-0.5(Ta ≥ 25°C)	mA / °C
	Pulse forward current	I _{FP}	1 (100μs, pulse, 100pps)		A
	Diode Power dissipation	P _D	100	70	mW
	Diode Power dissipation derating	ΔP _D / °C	-1.2(Ta ≥ 39°C)	-0.7(Ta ≥ 25°C)	mW / °C
	Reverse voltage	V _R	5		V
	Junction temperature	T _j	125		°C
Detector	Collector-emitter voltage	V _{CEO}	55		V
	Emitter-collector voltage	V _{ECO}	7		V
	Collector current	I _C	50		mA
	Collector power dissipation (1 circuit)	P _C	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C, 1 circuit)	ΔP _C / °C	-1.5	-1.0	mW / °C
	Junction temperature	T _j	125		°C
	Storage temperature range	T _{stg}	-55 to 125		°C
Operating temperature range		P _{opr}	-55 to 100		°C
Lead soldering temperature		T _{sol}	260 (10 s)		°C
Total package power dissipation (1 circuit)		P _T	250	150	mW
Total package power dissipation derating (Ta ≥ 25°C, 1 circuit)		ΔP _T / °C	-2.5	-1.5	mW / °C
Isolation voltage (Note 1)		BVs	5000 (AC, 60 s, R.H. ≤ 60 %)		V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	V _{CC}	—	5	24	V
Forward current	I _F	—	1.6	20	mA
Collector current	I _C	—	1	10	mA
Operating temperature	T _{opr}	-25	—	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V_F	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R = 5 \text{ V}$	—	—	10	μA
	Capacitance	C_T	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	30	—	pF
Detector	Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 0.5 \text{ mA}$	55	—	—	V
	Emitter-collector breakdown voltage	$V_{(BR)ECO}$	$I_E = 0.1 \text{ mA}$	7	—	—	V
	Collector dark current	I_{CEO}	$V_{CE} = 24 \text{ V}$	—	10	100	nA
			$V_{CE} = 24 \text{ V}, T_a = 85^\circ\text{C}$	—	2	50	μA
	Capacitance collector to emitter	C_{CE}	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	12	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I_C / I_F	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	100	—	1200	%
		Rank BV	200	—	1200	
Low input CTR	$I_C / I_F (\text{low})$	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	50	—	—	%
		Rank BV	100	—	—	
Collector-emitter saturation voltage	$V_{CE} (\text{sat})$	$I_C = 0.5 \text{ mA}, I_F = 1 \text{ mA}$	—	—	0.4	V
		$I_C = 1 \text{ mA}, I_F = 1 \text{ mA}$	—	0.2	—	
		Rank BV	—	—	0.4	

Coupled Electrical Characteristics (Ta = -25°C to 75°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I_C / I_F	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	50	—	—	%
		Rank BV	100	—	—	
Low input CTR	$I_C / I_F (\text{low})$	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	—	50	—	%
		Rank BV	—	100	—	

Isolation Characteristics (Ta = 25°C)

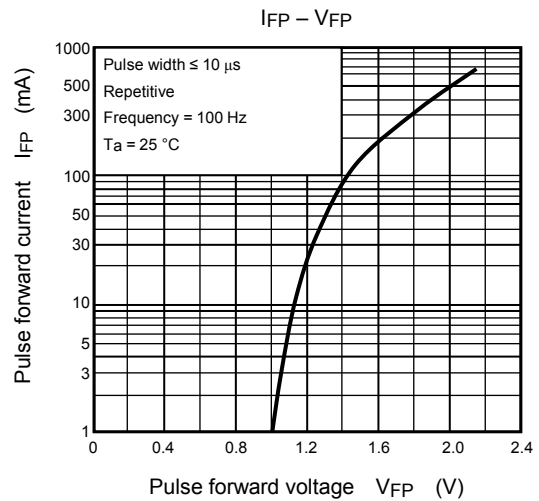
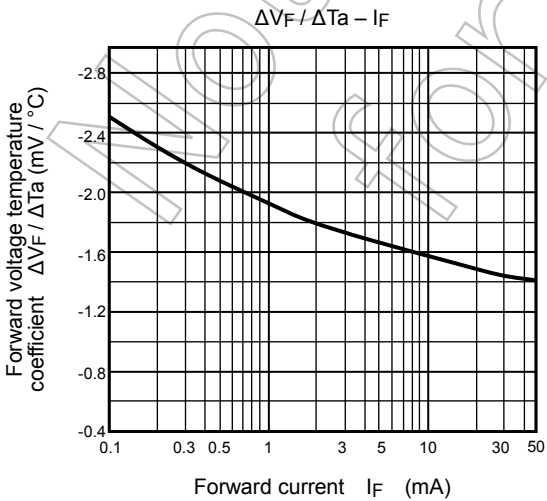
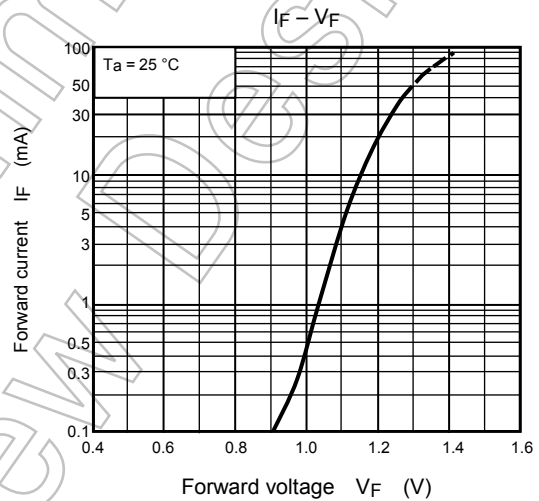
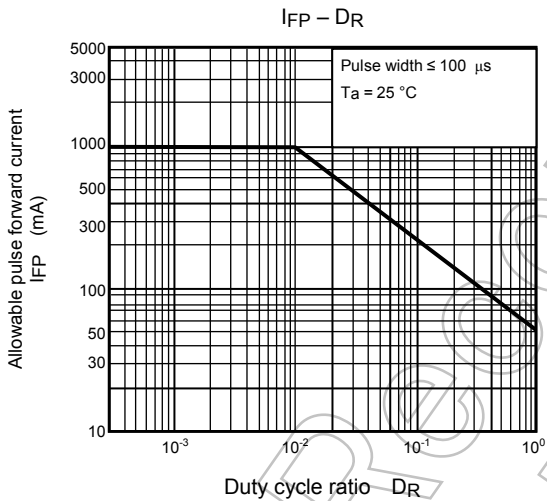
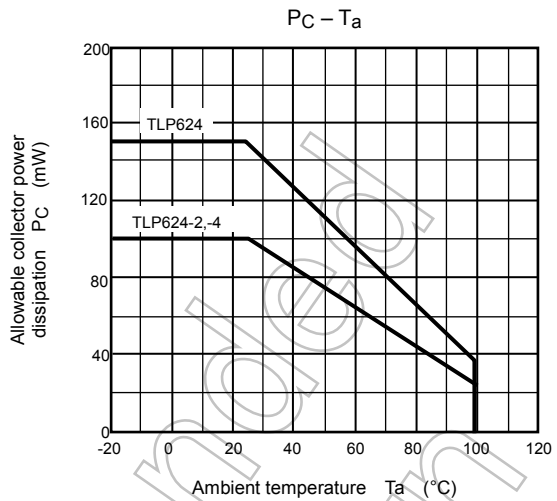
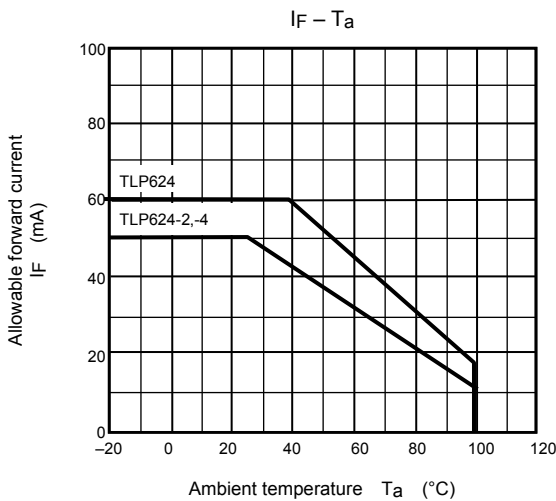
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	CS	VS = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	RS	VS = 500 V, R.H. ≤ 60 %	5×10 ¹⁰	10 ¹⁴	—	Ω
Isolation voltage	BVS	AC, 60 s	5000	—	—	Vrms

Switching Characteristics (Ta = 25°C)

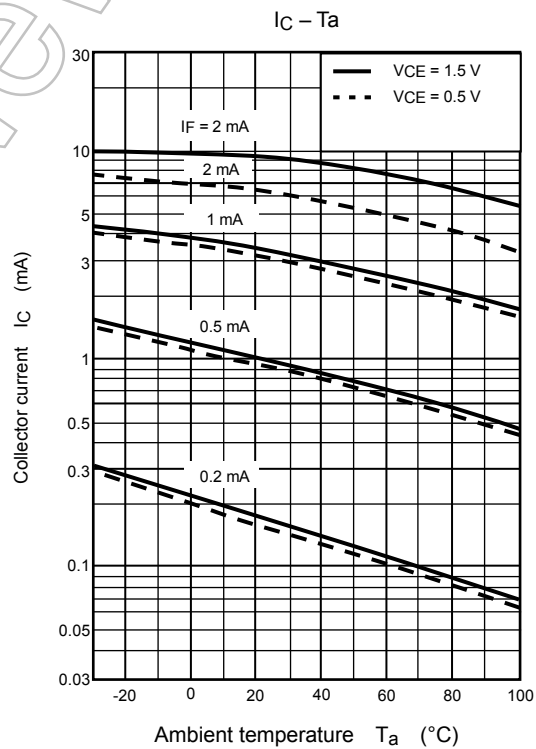
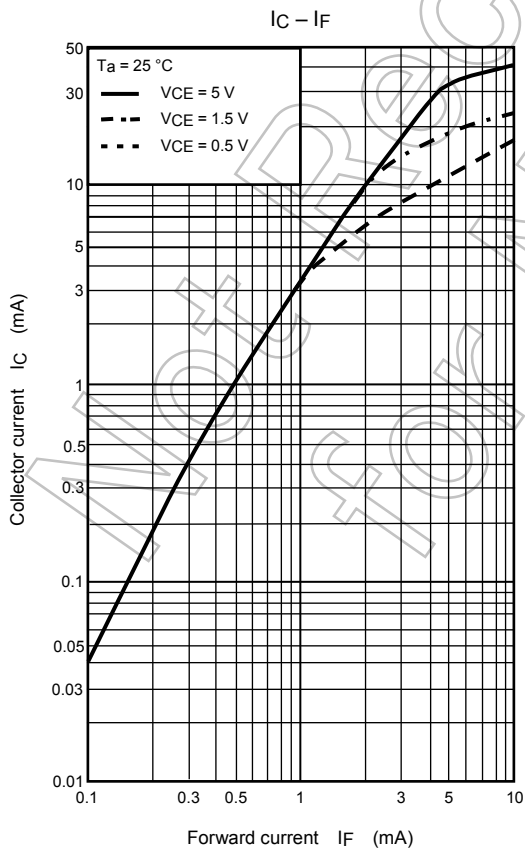
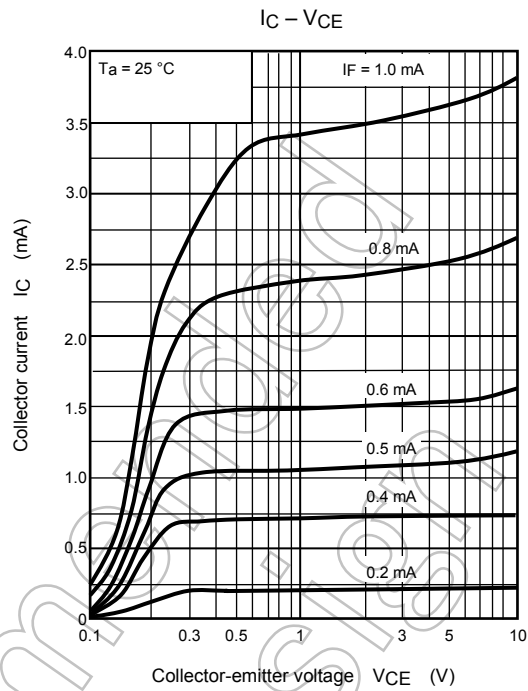
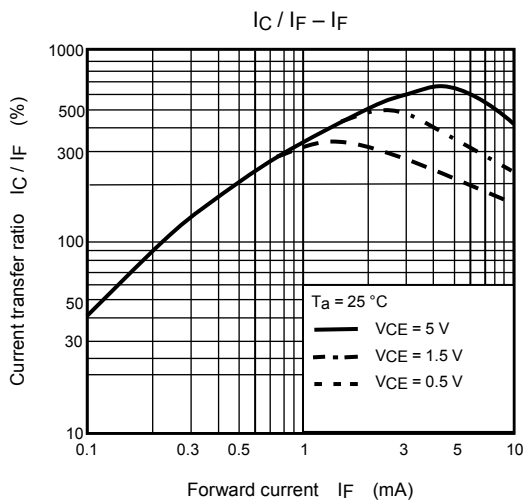
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	tr	VCC = 10 V, IC = 2 mA RL = 100 Ω	—	8	—	μs
Fall time	tf		—	8	—	
Turn-on time	ton		—	10	—	
Turn-off time	toff		—	8	—	
Turn-on time	tON	RL = 4.7 kΩ (Fig.1) VCC = 5 V, IF = 1.6 mA	—	10	—	μs
Storage time	ts		—	50	—	
Turn-off time	tOFF		—	300	—	

Fig. 1 Switching time test circuit

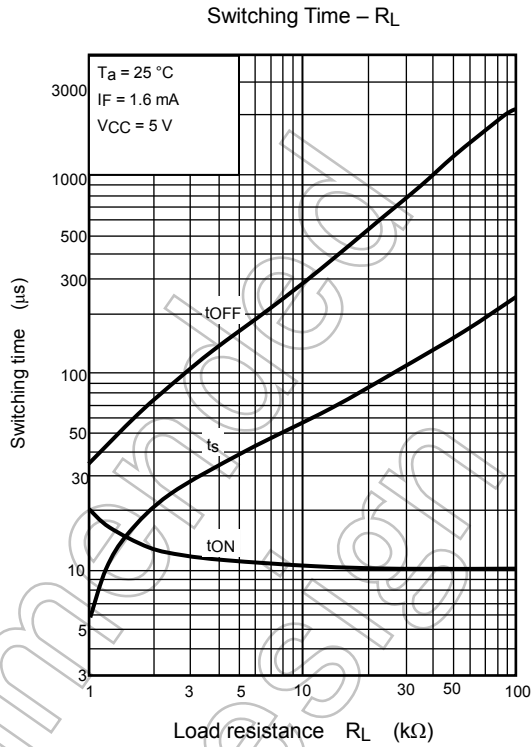
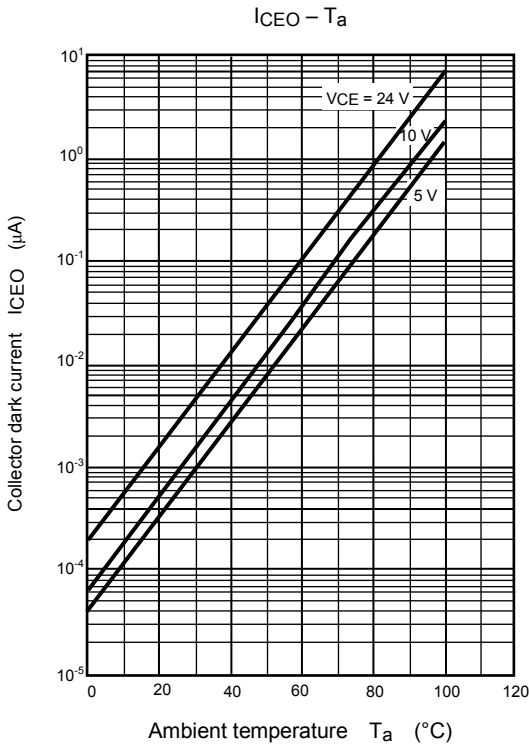




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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