TISP1120F3D Overvoltage Protector

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Absolute Maximum Ratings, TA = 25 °C (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage	V_{DRM}	-97	V
Non-repetitive peak impulse current (see Note 1)			
2/10 μs (GR-1089-CORE, 2/10 μs voltage wave shape)		2 x ±120	
8/20 μs (IEC 61000-4-5, combination wave generator, 1.2/50 μs voltage waveshape) 10/160 μs (TIA-968-A, 10/160 μs voltage wave shape)		2 x ±70	
		2 x ±60	
5/310 µs (ITU-T K.44, 10/700 µs voltage wave shape used in K.20/21/45)	I_{PPSM}	2 x ±50	Α
5/320 µs (TIA-968-A, 9/720 µs voltage waveshape)		2 x ±50	
10/560 μs (TIA-968-A, 10/560 μs voltage wave shape)		2 x ±45	İ
10/1000 μs (GR-1089-CORE, 10/1000 μs voltage wave shape)		2 x ±35	
Non-repetitive peak on-state current, 0 °C < T _A < 70 °C			
1 s, 50 Hz	I _{TSM}	2 x 4.3	Α
Initial rate of rise of on-state current, linear current ramp, maximum ramp value < 38 A		250	A/μs
Junction temperature	T _J	-65 to +150	°C
Storage temperature range	T _{stg}	-65 to +150	°C

NOTE: 1. Initially the device must be in thermal equilibrium with 0 $^{\circ}$ C < T_J < 70 $^{\circ}$ C. The surge may be repeated after the device returns to its initial conditions.

Electrical Characteristics for Terminals T and R, TA = 25 °C (Unless Otherwise Noted)

	Parameter	Test Conditions			Тур	Max	Unit
I _{DRM}	Repetitive peak off-state current	$V_D = \pm V_{DRM}$	T _A = 25 °C			±5	иΑ
			T _A = 70 °C			±10	μΛ
V _(BO)	Breakover voltage	$dv/dt = -250 \text{ V/ms}, R_{SOURCE} = 300 \Omega$				±123	V
Ι _Η	Holding current	$I_T = \pm 5A$, di/dt = ± 30 mA/ms		±150			mA

Electrical Characteristics for Terminals T and G or R and G, TA = 25 °C (Unless Otherwise Noted)

	Parameter	Test Conditions		Min	Тур	Max	Unit
I _{DRM}	Repetitive peak off-state current	$V_D = V_{DRM}$	$T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$			-5 -10	μΑ
V _(BO)	Breakover voltage	$dv/dt = -250 V/ms$, $R_{SOURCE} = 300 Ω$	$dv/dt = -250 \text{ V/ms}, R_{SOURCE} = 300 \Omega$				V
V _(BO)	Impulse breakover voltage	dv/dt ≤ -1000 V/μs, Linear voltage ramp, Maximum ramp value = -500 V di/dt ≤ -20 A/μs, Linear current ramp, Maximum ramp value = -10 A				-130	V
I _(BO)	Breakover current	dv/dt = -250 V/ms, R_{SOURCE} = 300 Ω		-100		-600	mA
I _H	Holding current	$I_T = -5 \text{ A, di/dt} = +30 \text{ mA/ms}$		-150			mA
V _T	On-state voltage	$I_T = -5 \text{ A}, t_w = 100 \mu\text{s}$				-3	V
V _F	Forward voltage	$I_F = +5 \text{ A}, t_W = 100 \mu \text{s}$				+3	V
V _{FRM}	Peak forward recovery voltage	dv/dt ≤ +1000 V/μs, Linear voltage ramp, Maximum ramp value = +500 V di/dt ≤ +20 A/μs, Linear current ramp, Maximum ramp value = +10 A			+3.3		V
dv/dt	Critical rate of rise of off-state voltage	Linear voltage ramp, maximum ramp value < 0.85V _{DRM}		-5			kV/μs
Co	Off-state capacitance	f = 1 MHz, V _d = 1 V rms	$V_D = -2 V$ $V_D = -50 V$		60 20	65 25	pF

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Thermal Characteristics, T_A = 25 °C (Unless Otherwise Noted)

Parameter		Test Conditions	Min	Тур	Max	Unit
$R_{\theta JA}$ Junction to ambient thermal re	P _{tot} = 0.8 W $5 \text{ cm}^2 \text{ FR4 PCB}$				160	°C/W

Parameter Measurement Information

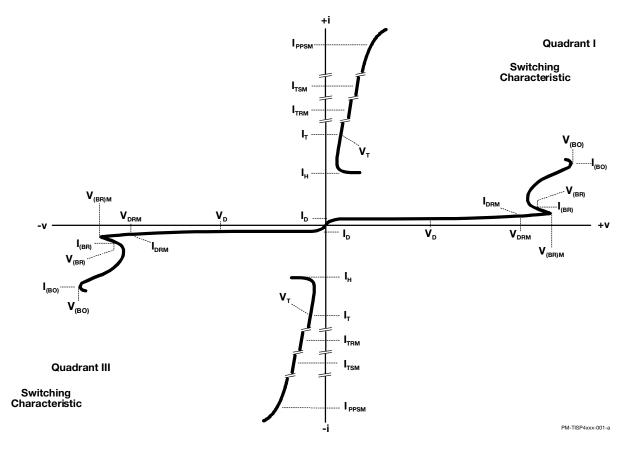


Figure 1. Voltage-Current Characteristic for the Terminals T and R All Measurements are Referenced to Terminal R

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Parameter Measurement Information

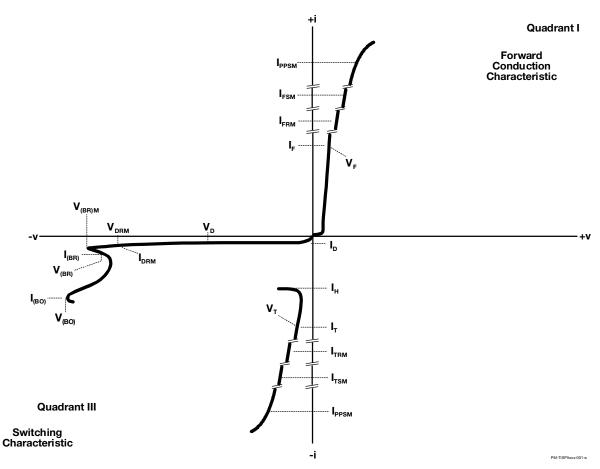


Figure 2. Voltage-Current Characteristic for Terminals T and G or R and G All Measurements are Referenced to Terminal G

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