Electrical Characteristics (Ta = 25°C)

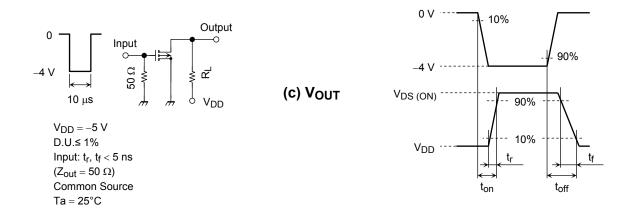
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0$	_		±1	μA
Drain-Source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30		_	V
Drain cut-off current		I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0$			-1	μA
Gate threshold voltage		V _{th}	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -0.1 \text{ mA}$	-1.1		-1.8	V
Forward transfer admittance		Y _{fs}	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -100 \text{ mA}$ (Note2)	115		_	mS
Drain-Source ON resistance		R _{DS} (ON)	$I_D = -100 \text{ mA}, V_{GS} = -10 \text{ V}$ (Note2)		2.1	2.7	Ω
			$I_D = -100 \text{ mA}, V_{GS} = -4 \text{ V}$ (Note2)		3.3	4.2	
			$I_D = -100 \text{ mA}, V_{GS} = -3.3 \text{ V}$ (Note2)		4.0	6.0	
Input capacitance		C _{iss}	$V_{DS}=-5~V,~V_{GS}=0,~f=1~MHz$		22	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$		5	_	pF
Output capacitance		C _{oss}	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$	_	14	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = -5 V, I _D = -100 mA, V _{GS} = 0 to -4 V	_	85	_	ns
	Turn-off time	t _{off}		_	85	_	ns

Note 2: Pulse test

Switching Time Test Circuit

(a) Test circuit

(b) V_{IN}



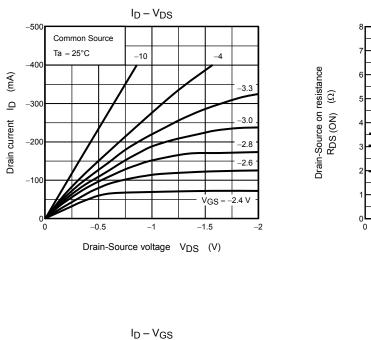
Precaution

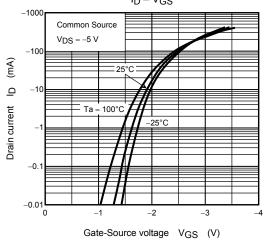
 V_{th} can be expressed as voltage between gate and source when low operating current value is I_D = $-100~\mu A$ for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

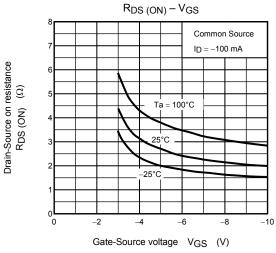
(relationship can be established as follows: $V_{\rm GS}$ (off) $< V_{th} < V_{\rm GS}$ (on))

Please take this into consideration for using the device.

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R_{DS} (ON) - I_D

VGS = -3.3 V

-100

10 V

-200

Drain current ID (mA)

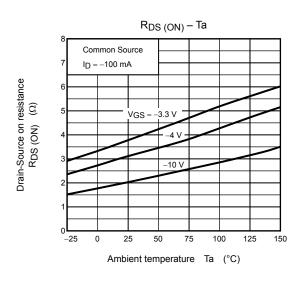
-300

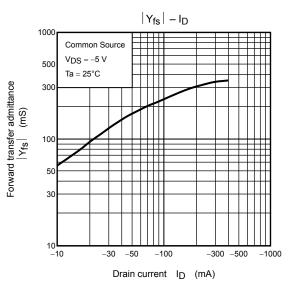
Common Source

-400

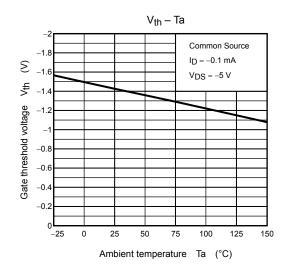
-500

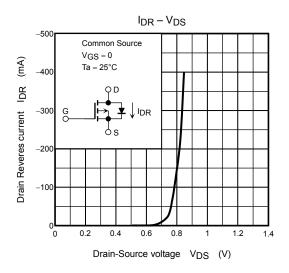
Ta = 25°C

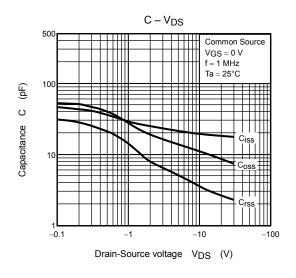


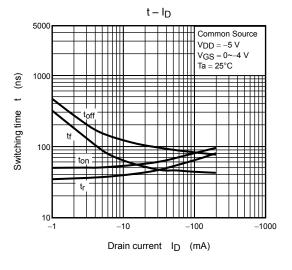


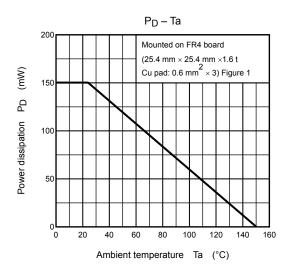
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