

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		19		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 4.4		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.1		2.4	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = + 20, - 16 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A		0.0053	0.0067	Ω
		V _{GS} = 4.5 V, I _D = 8 A		0.0075	0.0098	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 10 A		52		S
Dynamic ^{b, d}						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		1100		pF
Output Capacitance	C _{oss}			355		
Reverse Transfer Capacitance	C _{rss}			37		
C _{rss} /C _{iss} Ratio				0.034	0.068	
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 10 A		16.7	25	nC
		V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 10 A		8	12	
Gate-Source Charge	Q _{gs}			2.8		
Gate-Drain Charge	Q _{gd}			1.8		
Output Charge	Q _{oss}	V _{DS} = 15 V, V _{GS} = 0 V		7.8		
Gate Resistance	R _g	f = 1 MHz	0.4	1.25	2.5	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω		11	22	ns
Rise Time	t _r			11	22	
Turn-Off Delay Time	t _{d(off)}			19	38	
Fall Time	t _f			6	12	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 4.5 V, R _g = 1 Ω		19	38	
Rise Time	t _r			48	90	
Turn-Off Delay Time	t _{d(off)}			19	38	
Fall Time	t _f			9	18	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			28.4	A
Pulse Diode Forward Current ^a	I _{SM}				80	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.77	1.1	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 5 A, dI/dt = 100 A/μs, T _J = 25 °C		22	44	ns
Body Diode Reverse Recovery Charge	Q _{rr}			11	22	nC
Reverse Recovery Fall Time	t _a			12		ns
Reverse Recovery Rise Time	t _b			10		

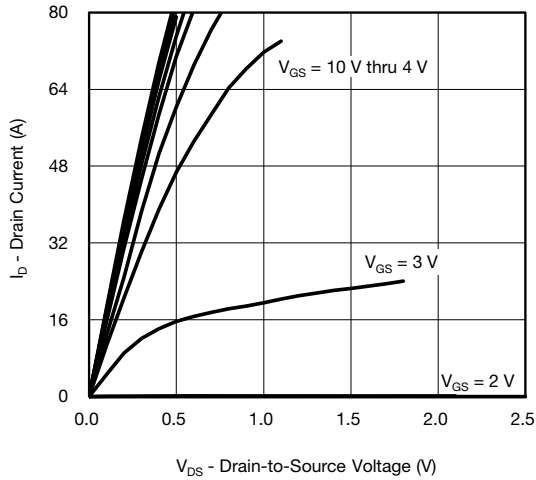
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

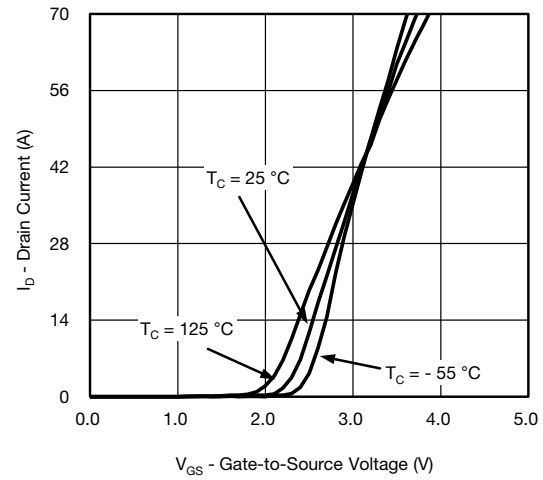
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

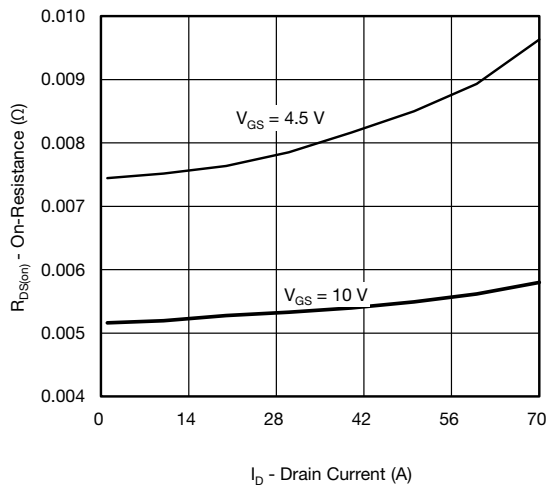
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



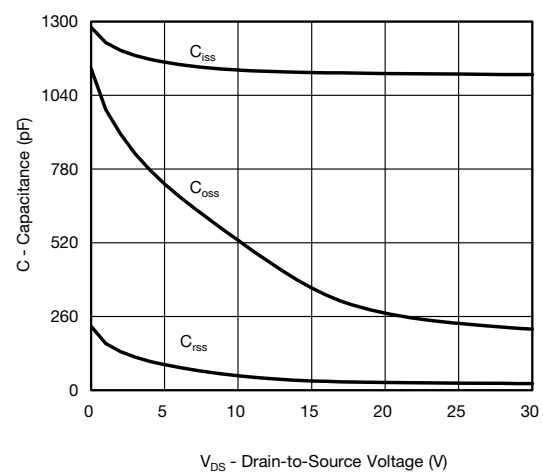
Output Characteristics



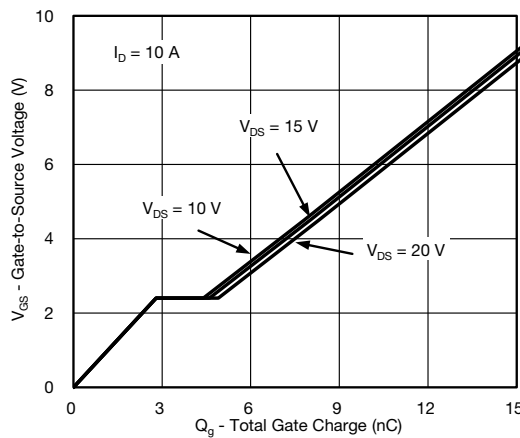
Transfer Characteristics



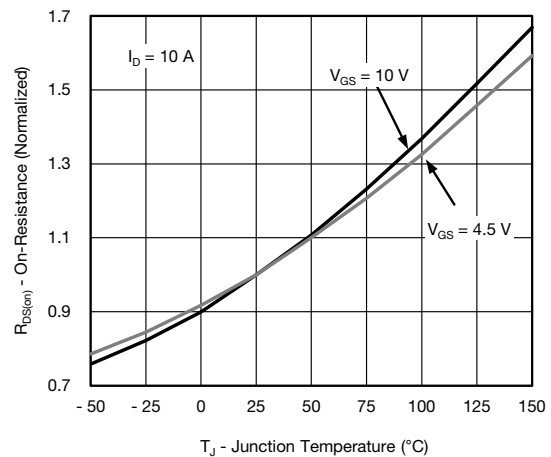
On-Resistance vs. Drain Current



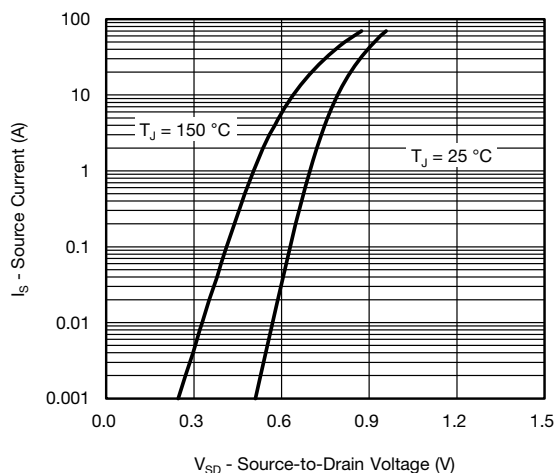
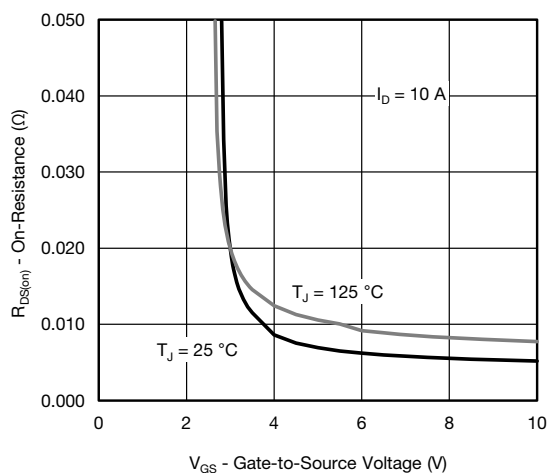
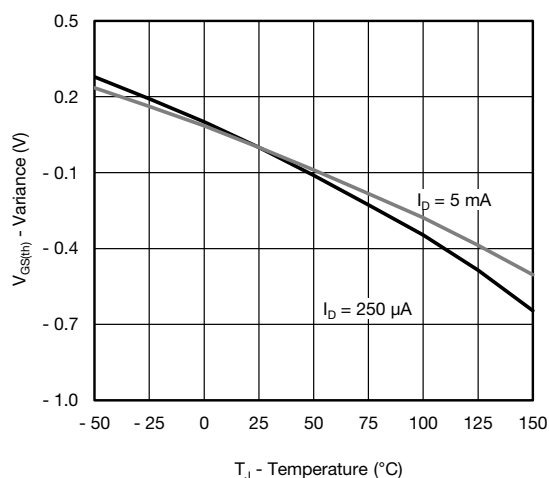
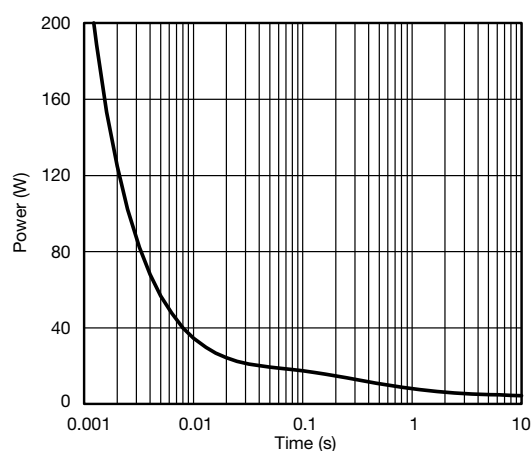
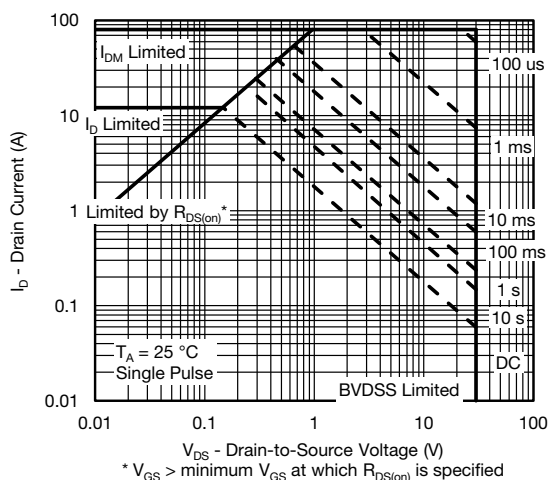
Capacitance



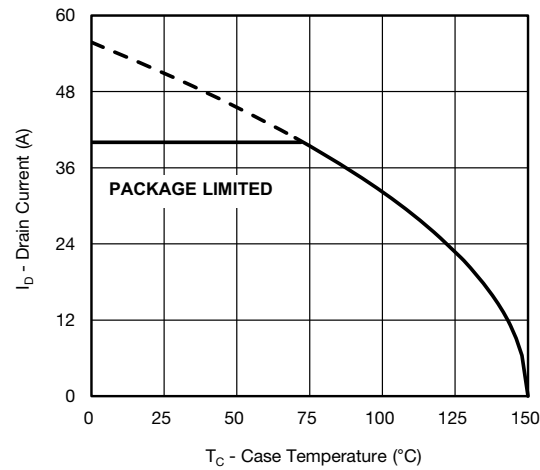
Gate Charge



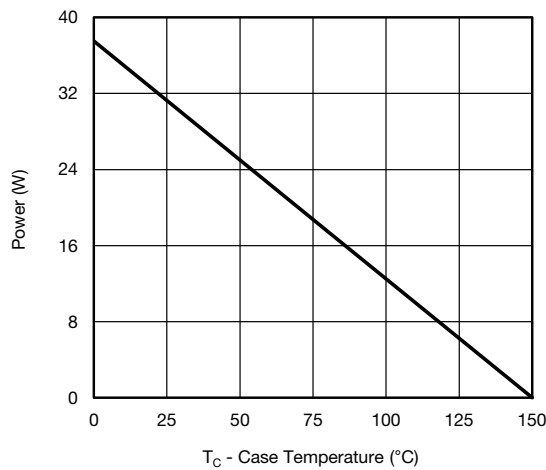
On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power, Junction-to-Ambient****Safe Operating Area**

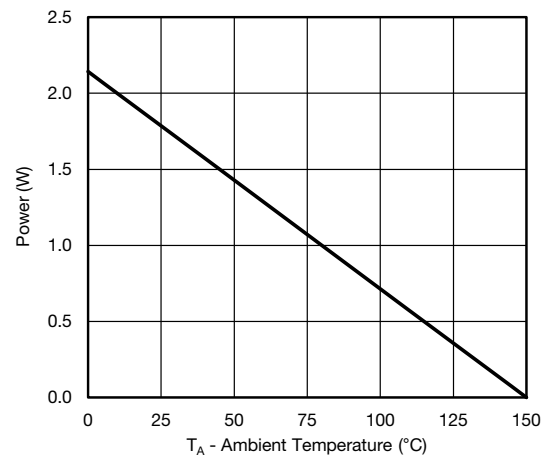
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*

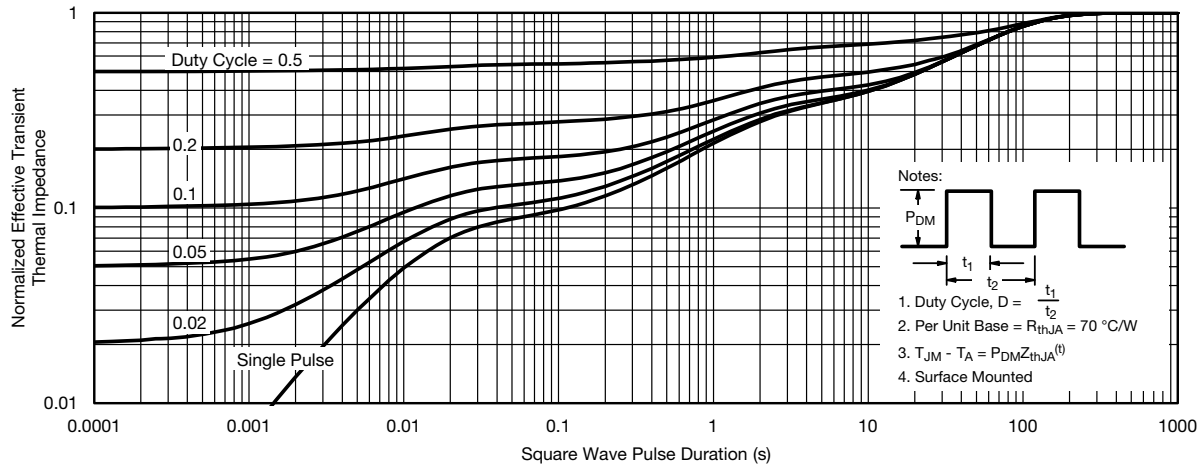
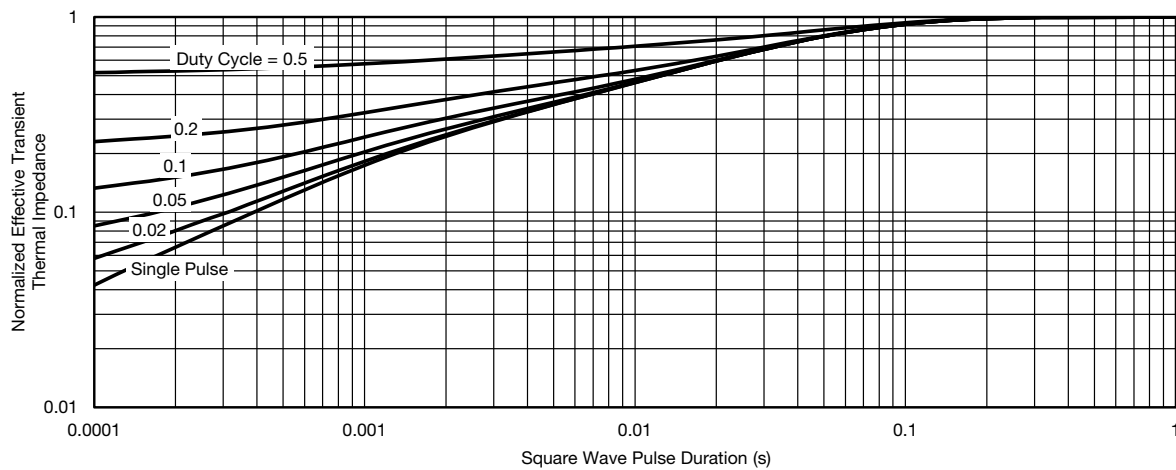


Power, Junction-to-Case



Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Case**

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