PNP General Purpose Amplifier

(continued)

Electrical Characteristics TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHAP	RACTERISTICS				
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 10 \mu\text{A}, I_B = 0$	60		V
BV _{CEO}	Collector-Emitter Breakdown Voltage*	$I_C = 1.0 \text{ mA}, I_E = 0$	45		V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 10 \mu\text{A}, I_C = 0$	6.0		V
I _{CBO}	Collector Cutoff Current	$V_{CB} = 50 \text{ V}, I_{E} = 0$		50	nA
I _{CES}	Collector Cutoff Current	V _{CE} = 40 V, I _E = 10		50	nA
I _{EBO}	Emitter Cutoff Current	$V_{EB} = 4.0 \text{ V}, I_{C} = 0$		50	nA

ON CHARACTERISTICS

h _{FE}	DC Current Gain	$I_C = 100 \mu A, V_{CE} = 1.0 \text{ V}$ $I_C = 10 \text{ mA}, V_{CE} = 1.0 \text{ V}$ $I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}^*$ $I_C = 150 \text{ mA}, V_{CE} = 5.0 \text{ V}^*$	200 200A 200 200A 200A 200A	80 240 100 300 100 100	450 600 350	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1.0 mA I _C = 200 mA, I _B = 20 mA*	200A	100	0.2 0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$ $I_C = 200 \text{ mA}, I_B = 20 \text{ mA}^*$			0.85 1.0	V V

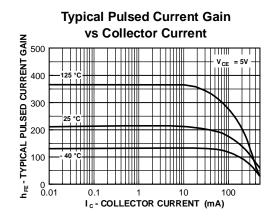
SMALL SIGNAL CHARACTERISTICS

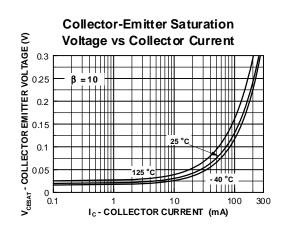
f _T	Current Gain - Bandwidth Product	$V_{CE} = 20 \text{ V}, I_{C} = 20 \text{ mA}$	250		MHz
C _{obo}	Output Capacitance	V _{CB} = 10 V, f = 1.0 MHz		6.0	pF
NF	Noise Figure	$I_C = 100 \mu\text{A}, V_{CE} = 5.0 \text{V},$			dB
		$R_G = 2.0 \text{ k}\Omega, f = 1.0 \text{ kHz}$		4.0	dB

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

Typical Characteristics

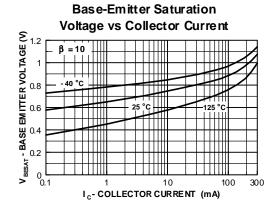


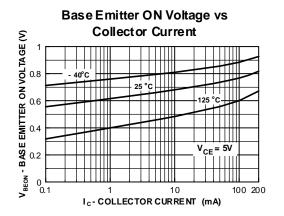


PNP General Purpose Amplifier

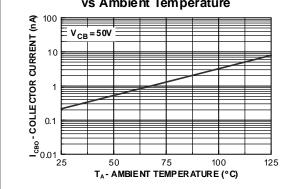
(continued)

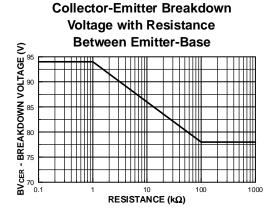
Typical Characteristics (continued)



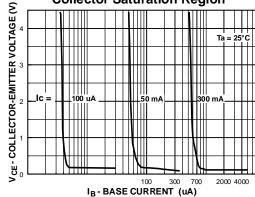


Collector-Cutoff Current vs Ambient Temperature

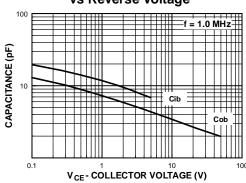




Collector Saturation Region



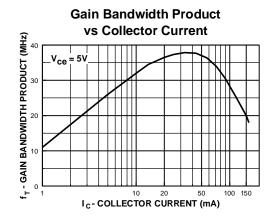
Input and Output Capacitance vs Reverse Voltage

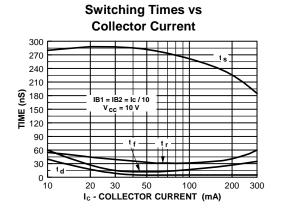


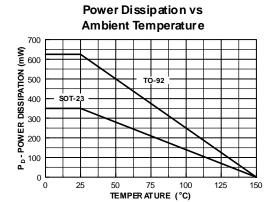
PNP General Purpose Amplifier

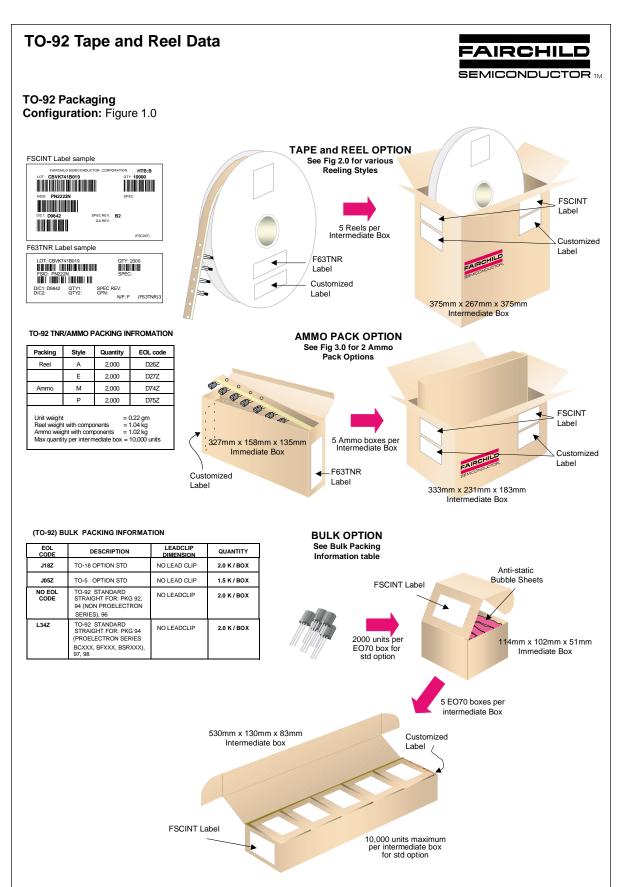
(continued)

Typical Characteristics (continued)







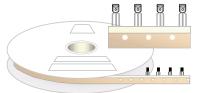


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TO-92 Tape and Reel Data, continued

TO-92 Reeling Style Configuration: Figure 2.0

Machine Option "A" (H)



Style "A", D26Z, D70Z (s/h)

Machine Option "E" (J)

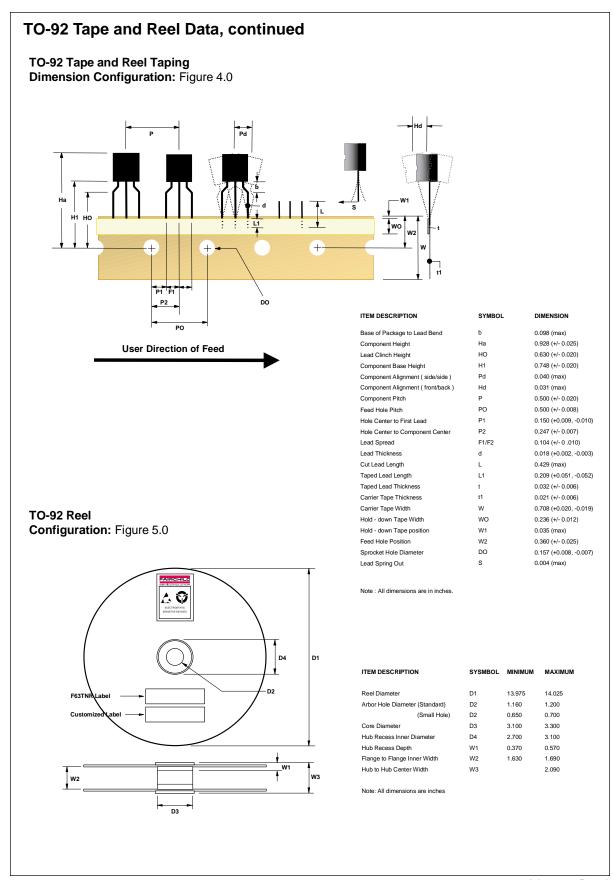
Style "E", D27Z, D71Z (s/h)

TO-92 Radial Ammo Packaging Configuration: Figure 3.0





FIRST WIRE OFF IS COLLECTOR (ON PKG. 92) ADHESIVE TAPE IS ON BOTTOM SIDE FLAT OF TRANSISTOR IS ON TOP

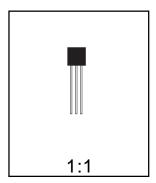


TO-92 Package Dimensions



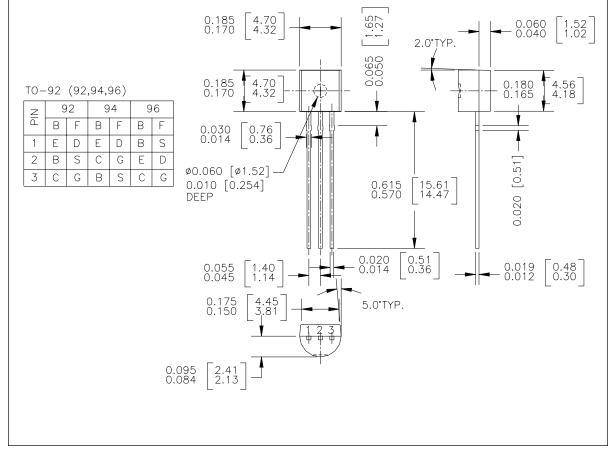
TO-92 (FS PKG Code 92, 94, 96)





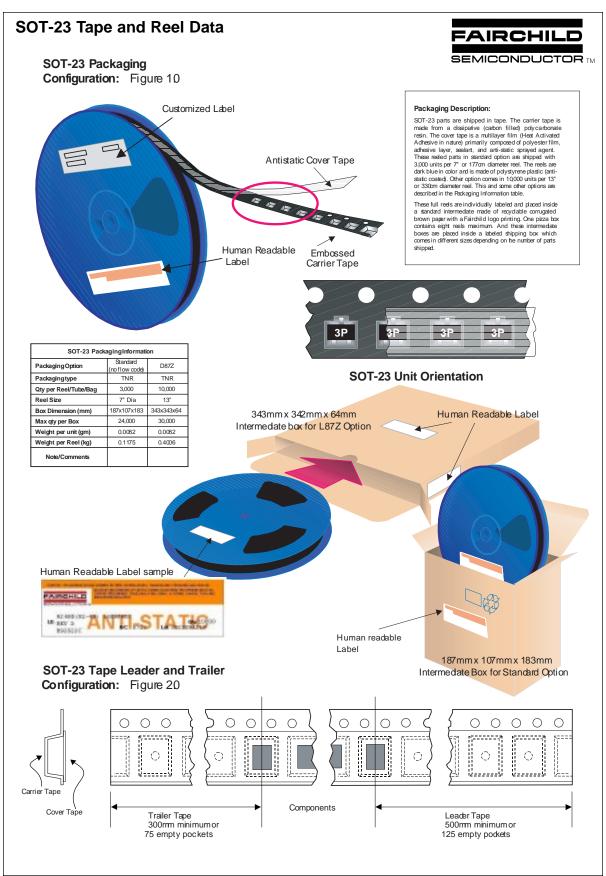
Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977



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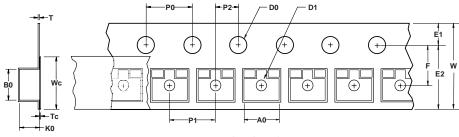
January 2000, Rev. B



SOT-23 Tape and Reel Data, continued

SOT-23 Embossed Carrier Tape

Configuration: Figure 3.0



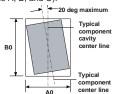
User Direction of Feed

Dimensions are in millimeter														
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SOT-23 (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



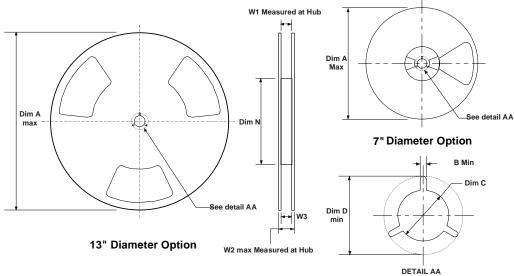
Sketch B (Top View)
Component Rotation



Sketch C (Top View)

Component lateral movement

SOT-23 Reel Configuration: Figure 4.0

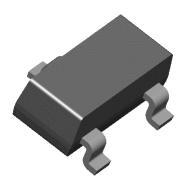


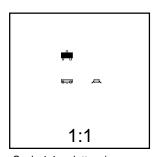
	Dimensions are in inches and millimeters								
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

SOT-23 Package Dimensions



SOT-23 (FS PKG Code 49)

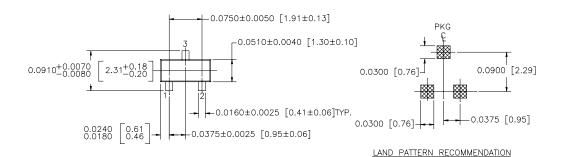


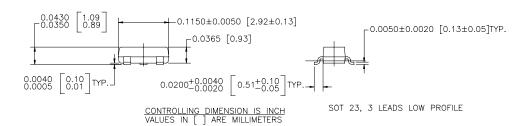


Scale 1:1 on letter size paper

Dimensions shown below are in: inches [millimeters]

Part Weight per unit (gram): 0.0082





NOTE: UNLESS OTHERWISE SPECIFIED

- 1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
- 2. REFERENCE JEDEC REGISTRATION TO -236, VARIATION AB, ISSUE G, DATED JUL 1993

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