

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5V to +7.0V
DC Input Voltage	-0.5V to +7.0V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = -40°C to +85°C, V_{CC} = 5.0V ±5%)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
V _{OH}	Output HIGH Voltage	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OH} = -15.0mA	2.4	3.0		V
V _{OL}	Output LOW Current	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OL} = 64mA		0.3	0.55	V
V _{OL}	Output LOW Current	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OL} = 12mA (25Ω Series)		0.3	0.50	V
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level				0.8	V
I _{IH}	Input HIGH Current	(Except I/O pins) V _{CC} = Max.	V _{IN} = V _{CC}			1	μA
I _{IL}	Input LOW Current	(Except I/O pins) V _{CC} = Max.	V _{IN} = GND			-1	μA
I _{IH}	Input HIGH Current	(I/O pins Only) V _{CC} = Max.	V _{IN} = V _{CC}			1	μA
I _{IL}	Input LOW Current	(I/O pins Only) V _{CC} = Max.	V _{IN} = GND			-1	μA
I _{OZH}	High Impedance	V _{CC} = MAX.	V _{OUT} = 2.7V			1	μA
I _{OZL}	Output Current		V _{OUT} = 0.5V			-1	μA
V _{IK}	Clamp Diode Voltage	V _{CC} = Min., I _{IN} = -18 mA			-0.7	-1.2	V
I _{OFF}	Power Down Disable	V _{CC} = GND, V _{OUT} = 4.5V				100	μA
I _{OS}	Short Circuit Current	V _{CC} = Max. ⁽³⁾ , V _{OUT} = GND		-60	-120		mA
V _H	Input Hysteresis				200		mV

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{CC} = 5.0V, +25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ.	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0V	6	10	pF
C _{OUT}	Output Capacitance	V _{OUT} = 0V	8	12	pF

Notes:

- These Parameters are determined by device characterization but are not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} =Max.	V _{IN} = GND or V _{CC}		0.1	500	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} =Max.,	V _{IN} =3.4V ⁽³⁾		0.5	2.0	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} =Max., Outputs Open OE=GND T/R=GND or V _{CC} One Input Toggling 50% Duty Cycle	V _{IN} =V _{CC} V _{IN} =GND		0.15	0.25	mA/ MHz
I _C	Total Power Supply Current ⁽⁶⁾	V _{CC} =Max., Outputs Open f _I = 10 MHz 50% Duty Cycle T/R=OE=GND One Bit Toggling	V _{IN} =V _{CC} V _{IN} =GND		1.5	3.5 ⁽⁵⁾	mA
			V _{IN} =3.4V V _{IN} =GND		1.8	4.5 ⁽⁵⁾	
		V _{CC} =Max., Outputs Open f _I = 2.5 MHz 50% Duty Cycle T/R=OE=GND Eight Bits Toggling	V _{IN} =V _{CC} V _{IN} =GND		3.0	6.0 ⁽⁵⁾	
			V _{IN} =3.4V V _{IN} =GND		5.0	14.0 ⁽⁵⁾	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V_{CC} = 5.0V, +25°C ambient.
- Per TTL driven input (V_{IN} = 3.4V); all other inputs at V_{CC} or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed but not tested.
- I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}
I_C = I_{CC} + ΔI_{CC} D_{HNT} + I_{CCD} (f_{CP}/2 + f_IN_I)
I_{CC} = Quiescent Current
ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = 3.4V)
D_H = Duty Cycle for TTL Inputs High
N_T = Number of TTL Inputs at D_H
I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
f_{CP} = Clock Frequency for Register Devices (Zero for Non-Register Devices)
f_I = Input Frequency
N_I = Number of Inputs at f_I
All currents are in milliamps and all frequencies are in megahertz.

Parameters	Description	Conditions ⁽¹⁾	245T/2245T		245AT/2245AT		245CT/2245CT		245DT		Units	
			Com.		Com.		Com.		Com.			
			Min.	Max.	Min.	Max.	Min.	Max.	Min	Max.		
tPLH tPHL	Propagation Delay A to B, B to A	C _L = 50 pF R _L = 500-ohm	1.5 ⁽¹⁾	7.0	1.5 ⁽¹⁾	4.6	1.5 ⁽¹⁾	4.1	1.5 ⁽¹⁾	3.8	ns	
tPZH tPZL	Output Enable Time ⁽²⁾ OE to A or B		1.5	9.0	1.5	6.2	1.5	5.8	1.5	5.0	ns	
tPHZ tPLZ	Output Disable Time ⁽²⁾ OE to A or B		1.5	7.5	1.5	5.0	1.5	4.8	1.5	4.3	ns	
tPZH tPZL	Output Enable Time ⁽²⁾ T/R to A or B		1.5	9.0	1.5	6.2	1.5	5.8	1.5	5.0	ns	
tPHZ tPLZ	Output Disable Time ⁽²⁾ T/R to A or B		1.5	7.5	1.5	5.0	1.5	4.8	1.5	4.3	ns	

1. Minimum limits are guaranteed but not tested on Propagation Delays.
2. This parameter is guaranteed but not production tested.

Technical drawing of a 20-pin connector showing three views: top, side, and end view. Dimensions are provided in inches and millimeters. A legend indicates that dimensions in boxes (X.XX) are in millimeters.

Top View Dimensions:

- Pin 1 to Pin 20 distance: $\frac{.496}{.511}$ inches / $\frac{12.60}{12.99}$ mm
- Pin 1 to Pin 20 distance (center-to-center): $\frac{.2914}{.2992}$ inches / $\frac{7.40}{7.60}$ mm

Side View Dimensions:

- Pin 1 to Pin 20 distance: $\frac{.020}{.030}$ inches / $\frac{0.508}{0.762}$ mm (REF)
- Pin 1 to Pin 20 distance (center-to-center): $\frac{.0926}{.1043}$ inches / $\frac{2.35}{2.65}$ mm
- Pin 1 to Pin 20 distance (bottom): $\frac{.0040}{.0118}$ inches / $\frac{0.10}{0.30}$ mm
- Pin 1 to Pin 20 distance (bottom): $\frac{.050}{1.27}$ mm (BSC)
- Pin 1 to Pin 20 distance (bottom): $\frac{.013}{.020}$ inches / $\frac{0.33}{0.51}$ mm

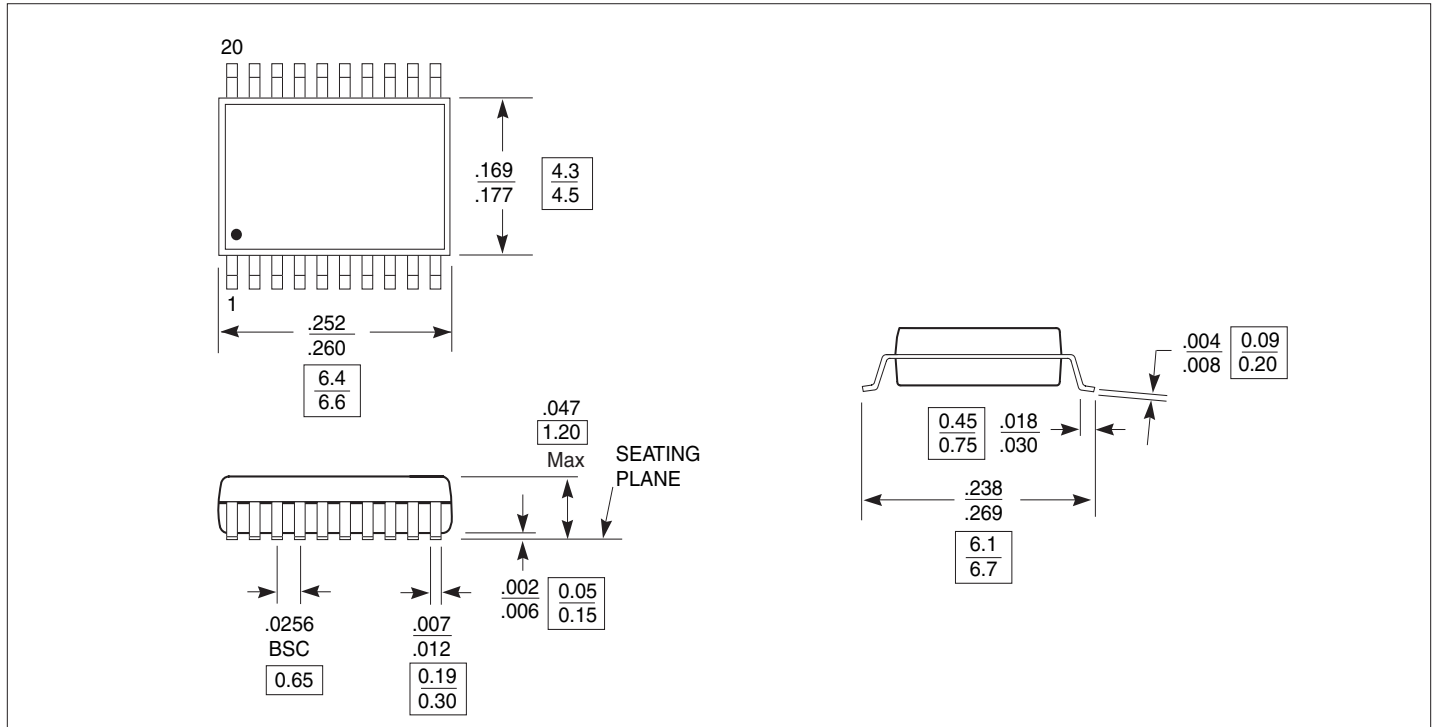
End View Dimensions:

- Pin 1 to Pin 20 distance: $\frac{.010}{.029}$ inches / $\frac{0.254}{0.737}$ mm x 45°
- Pin 1 to Pin 20 distance: $\frac{.0091}{.0125}$ inches / $\frac{0.23}{0.32}$ mm
- Pin 1 to Pin 20 distance: $\frac{.041}{1.27}$ inches / $\frac{.016}{.050}$ mm
- Pin 1 to Pin 20 distance: $\frac{.394}{.419}$ inches / $\frac{10.00}{10.65}$ mm

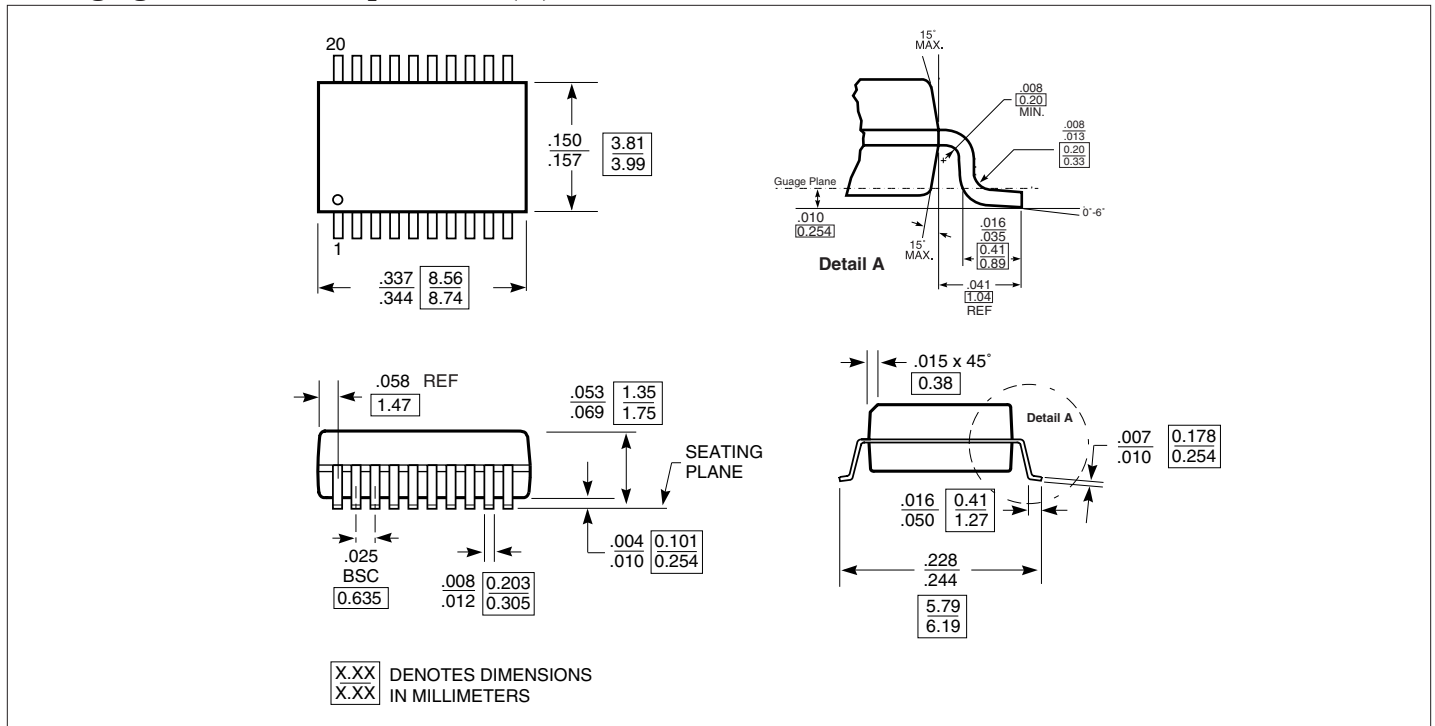
Legend:

- X.XX DENOTES CONTROLLING DIMENSIONS IN MILLIMETERS

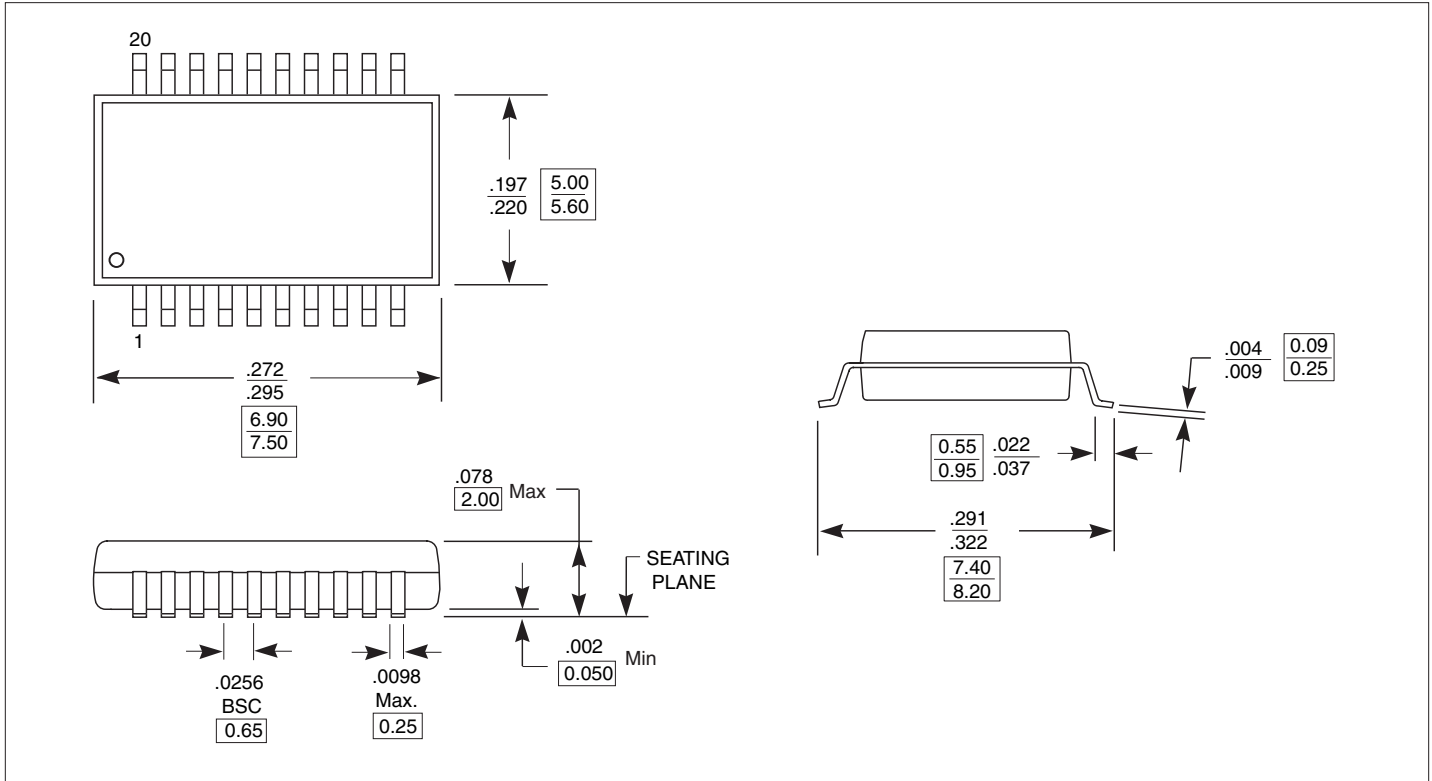
Packaging Mechanical: 20-Pin TSSOP (L)



Packaging Mechanical: 20-pin QSOP (Q)



Packaging Mechanical: 20-Pin SSOP (H)



PI74FCT245T Ordering Information

Ordering Code	Package Code	Speed Grade	Package Type
PI74FCT245TQ	Q	Blank	20-pin QSOP
PI74FCT245TQE	Q	Blank	Pb-free & Green, 20-pin QSOP
PI74FCT245TS	S	Blank	20-pin SOIC
PI74FCT245TSE	S	Blank	Pb-free & Green, 20-pin SOIC
PI74FCT245ATLE	L	A	Pb-free & Green, 20-pin TSSOP
PI74FCT245ATS	S	A	20-pin SOIC
PI74FCT245ATSE	S	A	Pb-free & Green, 20-pin SOIC
PI74FCT245ATQ	Q	A	20-pin QSOP
PI74FCT245ATQE	Q	A	Pb-free & Green, 20-pin QSOP
PI74FCT245CTH	H	C	20-pin SSOP
PI74FCT245CTHE	H	C	Pb-free & Green, 20-pin SSOP
PI74FCT245CTS	S	C	20-pin SOIC
PI74FCT245CTSE	S	C	Pb-free & Green, 20-pin SOIC
PI74FCT245CTQ	Q	C	20-pin QSOP
PI74FCT245CTQE	Q	C	Pb-free & Green, 20-pin QSOP
PI74FCT245DTQ	Q	D	20-pin QSOP
PI74FCT245DTQE	Q	D	Pb-free & Green, 20-pin QSOP

PI74FCT2245T Ordering Information

Ordering Code	Package Code	Speed Grade	Package Type
PI74FCT2245TQ	Q	Blank	20-pin QSOP
PI74FCT2245TQE	Q	Blank	Pb-free & Green, 20-pin QSOP
PI74FCT2245TS	S	Blank	20-pin SOIC
PI74FCT2245TSE	S	Blank	Pb-free & Green, 20-pin SOIC
PI74FCT2245ATS	S	A	20-pin SOIC
PI74FCT2245ATSE	S	A	Pb-free & Green, 20-pin SOIC
PI74FCT2245ATQ	Q	A	20-pin QSOP
PI74FCT2245ATQE	Q	A	Pb-free & Green, 20-pin QSOP
PI74FCT2245CTL	L	C	20-pin TSSOP
PI74FCT2245CTLE	L	C	Pb-free & Green, 20-pin TSSOP
PI74FCT2245CTQ	Q	C	20-pin QSOP
PI74FCT2245CTQE	Q	C	Pb-free & Green, 20-pin QSOP
PI74FCT2245DTSE	S	D	Pb-free & Green, 20-pin SOIC

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

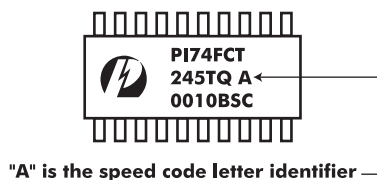
Part Marking Information

Pericom's standard product mark follows our standard part number ordering information, except for those products with a speed letter code. For marking purposes, the speed letter code mark is placed after the package code letter, rather than after the device number as it is ordered.

Although all products are marked immediately after assembly to assure material traceability, Pericom does not usually mark the speed code at that time. After electrical test screening and speed binning have been completed, we then perform an "add mark" operation which places the speed code letter at the end of the complete part number.

Please refer to the example shown below:

- Part Number as ordered: PI74FCT245ATQ
- Example of Part Number as marked:



Notes:

- 8-pin DIP, 8-pin SOIC, 8-pin TSSOP, 14-pin SOIC, 16-pin QSOP, SC70, MSOP, and SOT23 packages are not marked with the Pericom logo due to space limitations on the package.