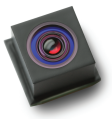
OVM9724



Applications

- Cellular and Picture Phones

Product Features

- MIPI and D-PHY specification (contains one clock lane and one data lane) with a maximum of 400 Mbps data transfer rate
- low operating voltage and low power consumption for embedded portable applications
- high sensitivity and low dark current for low-light conditions
- supports global analog gain
- supports free-running clock and gated clock
- supports down sample mode and VarioPixel®
- auto black level calibration
- defect correction capability

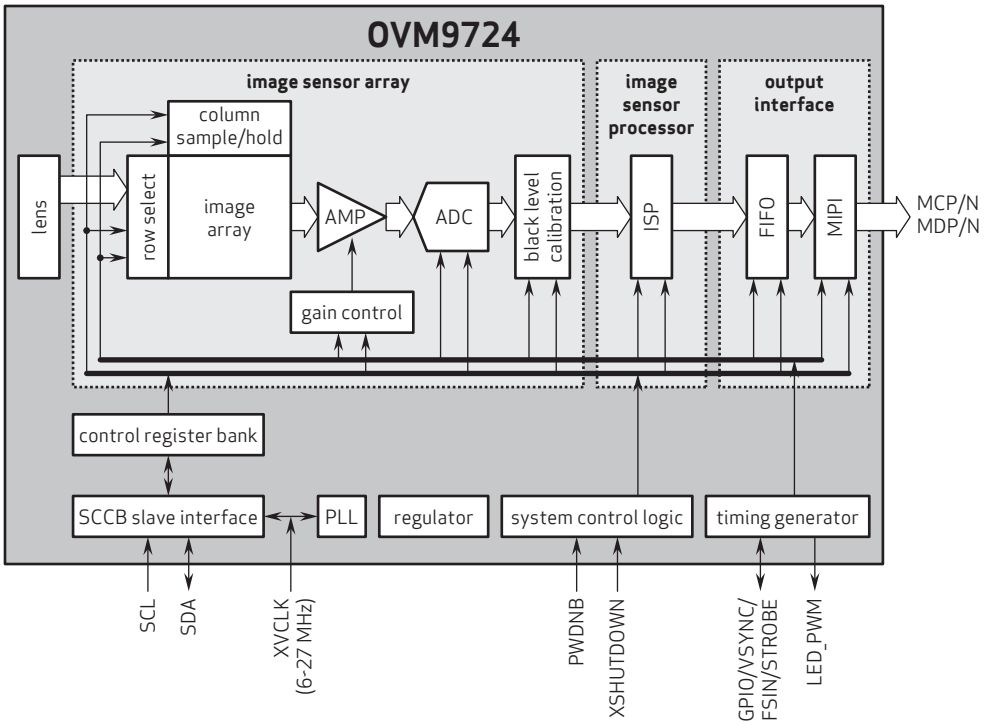
Ordering Information

- **OVM9724-RYDA**
  (color, lead-free, CameraCubeChip™ with metal can)
- **OVM9724-RADA**
  (color, lead-free, CameraCubeChip™ with black coating)

Product Specifications

- **active array size:** 1280 x 720
- **power supply:**
  - **core:** 1.5V
  - **analog:** 2.8V
  - **I/O:** 1.8V
- **power requirements:**
  - **active:** 55 mA
  - **hardware standby:** 60 µA
  - **XSHUTDOWN:** 20 µA
- **temperature range:**
  - **operating:** -30°C to +70°C junction temperature
  - **stable image:** 0°C to +50°C junction temperature
- **output formats:** 10-bit RAW RGB data
- **lens size:** 1/9"
- **diagonal field of view (FOV):** 65°
- **f no.:** 2.8
- **focal length:** 1.66 mm
- **input clock frequency:** 6 - 27 MHz
- **max S/N ratio:** 36.2 dB
- **dynamic range:** 70.4 dB @ 8x gain
- **maximum image transfer rate:** 30 fps
- **sensitivity:** 740 mV/lux-sec
- **scan mode:** progressive
- **maximum exposure interval:** 760 x t_{ROW}
- **pixel size:** 1.4 µm x 1.4 µm
- **dark current:** 80 mV/s @ 50°C junction temperature
- **image area:** 1840 µm x 1040 µm
- **package dimensions:**
  - **RYDA:** 4180 x 3280 x 2610 µm
  - **RADA:** 3900 x 2890 x 2320 µm

Functional Block Diagram



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