



OUTPUT CHARACTERISTICS				
Parameter	Conditions 1	Тур.	Max.	Units
Voltago oot point aggurage	All NDL05/12 input types with external input/output capacitors	±1	±3	%
Voltage set point accuracy	All NDL24/48 input types with external input/output capacitors	±2	±5	70
Line regulation	All NDL05/12 input types, low line to high line with external input/output capacitors	0.05	0.5	%
Line regulation	All NDL24/48 input types, low line to high line with external input/output capacitors	0.04	0.4	
Load regulation	All NDL05/12 input types, minimum load to rated load with external input/output capacitors	0.2	0.75	%
Load regulation	All NDL24/48 input types, minimum load to rated load with external input/output capacitors	0.2	0.75	
Ripple	B/W = 20MHz to 300kHz with external input/output capacitors	5	10	mV rms
	All NDL05 input types, B/W =DC to 20MHz with external input/output capacitors	50	100	
Noise	All NDL12 input types, B/W =DC to 20MHz with external input/output capacitors	110	170	mV p-p
	All NDL24/48 input types, B/W =DC to 20MHz with external input/output capacitors	50	100	100
Shutdown power	+V <sub>IN</sub> nominal	2.8		mW

ISOLATION CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Isolation test voltage	Flash tested for 1 second	1000			VDC
Resistance	Viso = 1000VDC	1			GΩ

GENERAL CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Control pin (CTRL) input current	Please refer to control pin application note	6	10	15	mA
Switching frequency	Max. rated load to Min. rated load, Vin Min. to Vin. Max.	100		600	kHz

TEMPERATURE CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Operation		-40		85	90
Storage		-50		130	- 0
Cooling	Free air convection				

ABSOLUTE MAXIMUM RATINGS	
Short-circuit protection	Continuous
Lead temperature 1.5mm from case for 10 seconds	260°C
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to application notes for further information.
Control pin input current	15mA
Input voltage 05 types	10V
Input voltage 12 types	20V
Input voltage 24 types	40V
Input voltage 48 types	80V

<sup>1.</sup> Refer to recommended test circuit for external input/output capacitors.





## **TECHNICAL NOTES**

#### **ISOLATION VOLTAGE**

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions NDL series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 1kVDC for 1 second.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals, such as the NDL series, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

#### REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The NDL series has an El ferrite core, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

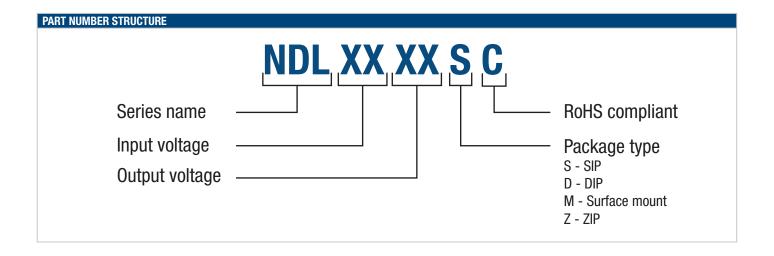
This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

#### **Rohs Compliant Information**



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to application notes for further information. The pin termination finish on this product series is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The series is backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata-ps.com/rohs



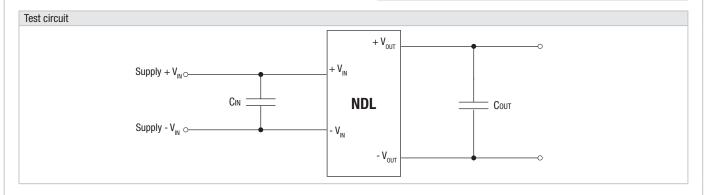


## **APPLICATION NOTES**

#### External capacitance

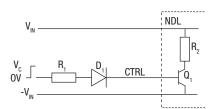
Although these converters will work without external capacitors, they are necessary in order to guarantee the full parametric performance over the full line and load range. All parts have been tested and characterized using the following values and test circuit.

Value				
Input Voltage (V)	Cin	Соит		
5 & 12	100μF, 25V	100μF, 25V		
24 & 48	10μF, 200V	100μF, 25V		



#### Control Pin

The NDL converters have a shutdown feature which enables the user to put the converter into a low power state. The control pin connects directly to the base of an internal transistor, and the switch off mechanism for the NDL works by forward biasing this NPN transistor. If the pin is left open (high impedance), the converter will be ON (there is no allowed low state for this pin), but once a control voltage is applied with sufficient drive current, the converter will be switched OFF. A suitable application circuit is shown below.



 $D_{_1}$  (e.g. 1N4001) is required to provide high impedence when the signal is low. From the NDL specification, the drive current to operate this function is recommended to be 10mA, and hence the value of  $R_{_1}$  can be derived as follows:

$$R_{\scriptscriptstyle 1} = \frac{V_{\scriptscriptstyle C} - V_{\scriptscriptstyle D} - V_{\scriptscriptstyle Q}}{I_{\scriptscriptstyle C}}$$

Assuming  $V_c=5V$ ,  $V_D=0.7V$  and  $V_Q=1V$ :

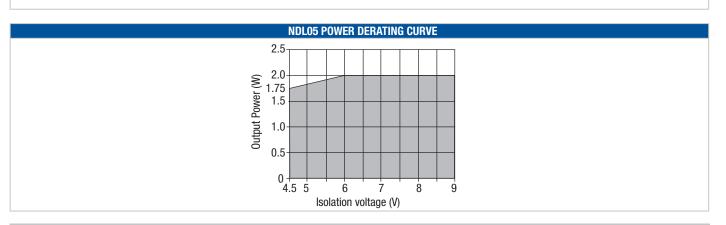
$$R_1 = \frac{5 - 0.7 - 1.0}{10 \times 10^{-3}} = 330\Omega$$

#### Pin 8 (Cs)

This pin provides a connection point to the main reservoir capacitor. Additional capacitance can be added from this pin to pin 7. Any lower ESR capacitor will remove ripple and noise to some degree. The benefit of this access point over simple additional output capacitance is that it precedes the output filter inductor. Maximum values of external capacitance will be dependent on the output voltage, the loading of the converter and the desired ripple figure. Values can be up to 100µF.

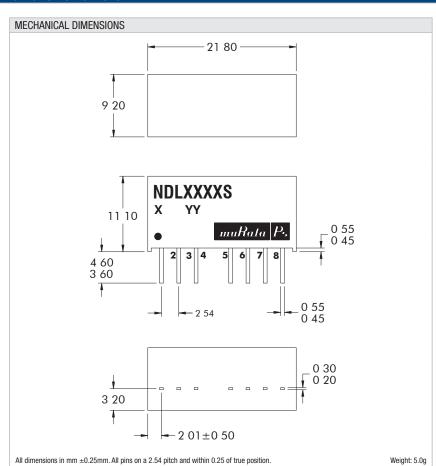
#### Minimum load

The minimum load for correct operation is 25% of the full rated load across the specified input voltage range. Lower loads may cause a significant increase in output ripple and may cause the output voltage to exceed its specification transiently during power-down when the input voltage also falls below its rated minimum.





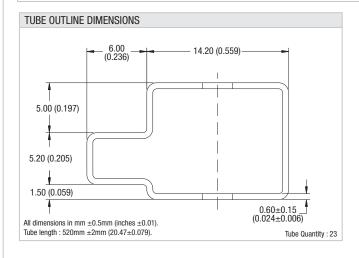
## PACKAGE SPECIFICATIONS

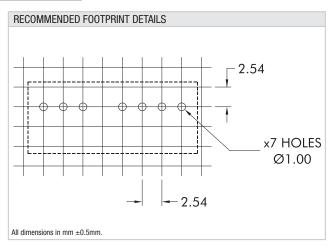


#### PIN CONNECTIONS

Pin	Function
1	-V <sub>IN</sub>
2	+VIN
3	CTRL
5*	IC
6	+Vout
7	-Vout
8	Cs

\* This pin is internally connected, and must have no external connection and is used for mechanical reasons. External connection to anything will result in converter failure.







# **NDL Series**

## Isolated 2W Wide Input Single Output DC-DC Converters

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