

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-160	V
Collector-Emitter Voltage	V _{CEO}	-150	V
Emitter-Base Voltage	V _{EBO}	-5	V
Continuous Collector Current	I _C	-200	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	200	mW
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 7)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)					
Collector-Base Breakdown Voltage	V _{CBO}	-160	—	V	I _C = -100μA, I _E = 0
Collector-Emitter Breakdown Voltage	V _{CEO}	-150	—	V	I _C = -1mA, I _B = 0
Emitter-Base Breakdown Voltage	V _{EBO}	-5	—	V	I _E = -100μA, I _C = 0
Collector Cutoff Current	I _{CBO}	—	-50	nA μA	V _{CB} = -120V, I _E = 0 V _{CB} = -120V, I _E = 0, T _A = +100°C
Emitter Cutoff Current	I _{EBO}	—	-50	nA	V _{EB} = -3V, I _C = 0
ON CHARACTERISTICS (Note 8)					
DC Current Gain	h _{FE}	50 60 50	— 240 —	—	I _C = -1mA, V _{CE} = -5V I _C = -10mA, V _{CE} = -5V I _C = -50mA, V _{CE} = -5V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	—	-0.2 -0.5	V	I _C = -10mA, I _B = -1mA I _C = -50mA, I _B = -5mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	—	-1	V	I _C = -10mA, I _B = -1mA I _C = -50mA, I _B = -5mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}	—	6.0	pF	V _{CB} = -10V, f = 1MHz, I _E = 0
Small Signal Current Gain	h _{fe}	40	260	—	V _{CE} = -10V, I _C = -1mA, f = 1kHz
Current Gain-Bandwidth Product	f _T	100	300	MHz	V _{CE} = -10V, I _C = -10mA, f = 100MHz
Noise Figure	NF	—	8	dB	V _{CE} = -5V, I _C = -200μA, R _S = 10Ω, f = 1kHz

- Notes:
6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.
 7. Refer to JEDEC specification JESD22-A114 and JESD22-A115.
 8. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

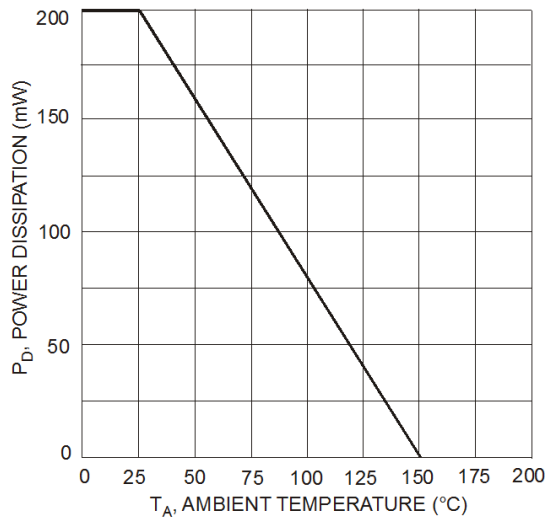


Fig. 1, Max Power Dissipation vs. Ambient Temperature

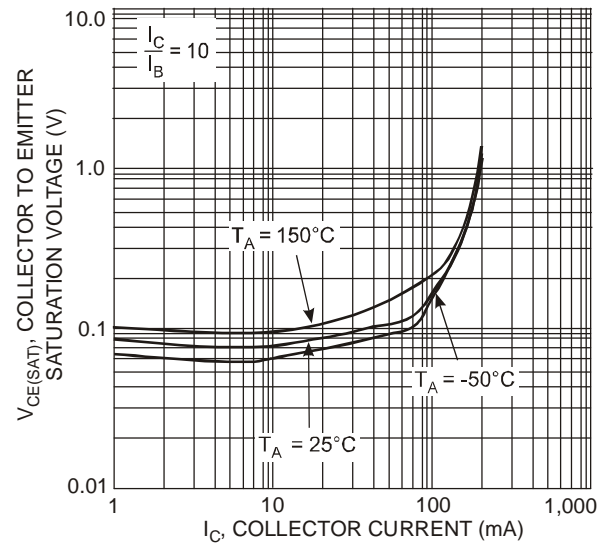


Fig. 2, Collector Emitter Saturation Voltage vs. Collector Current

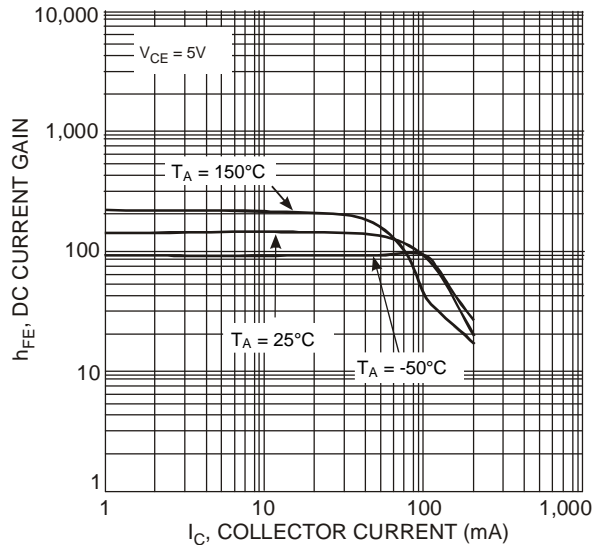


Fig. 3, DC Current Gain vs. Collector Current

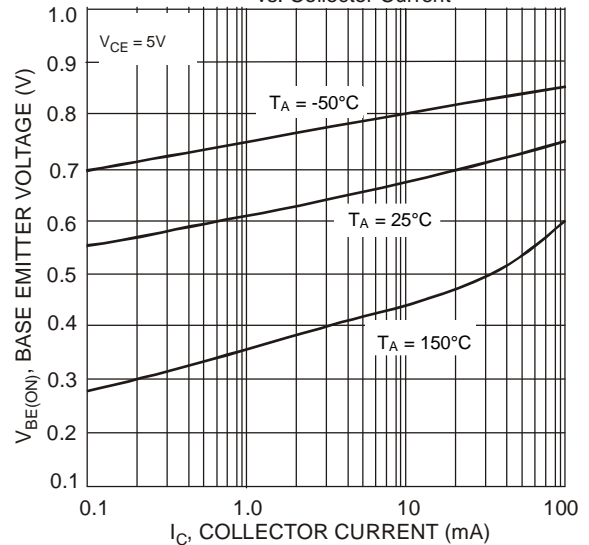


Fig. 4, Base Emitter Voltage vs. Collector Current

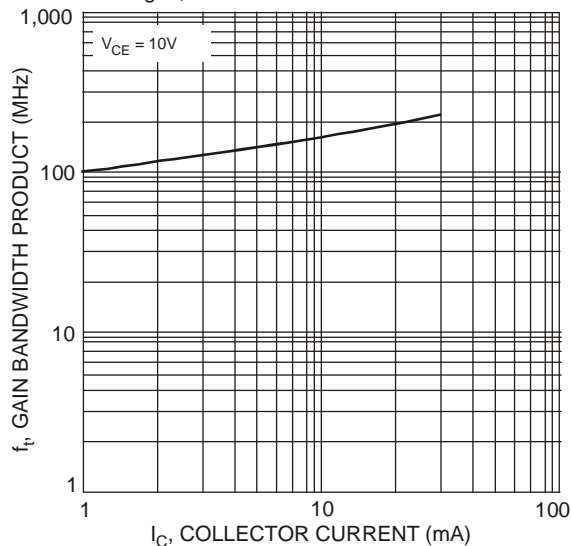
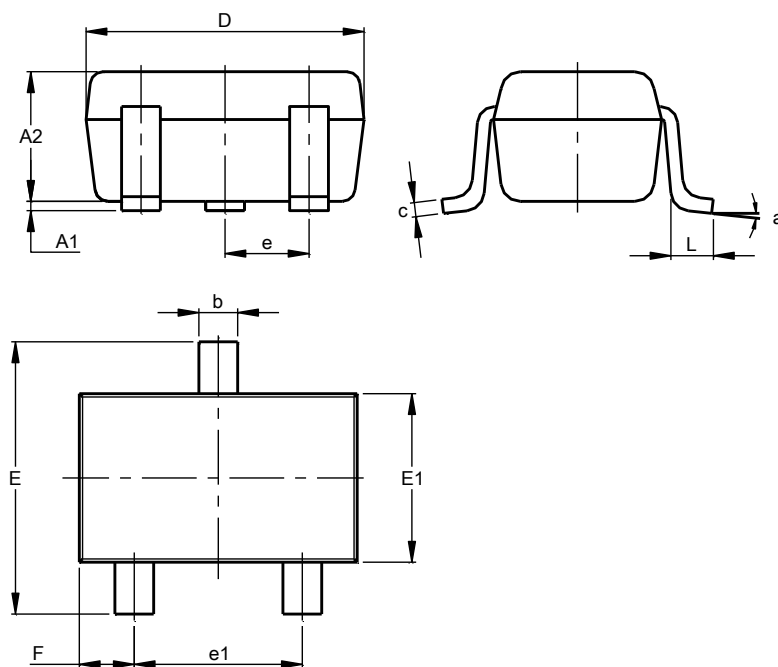


Fig. 5, Gain Bandwidth Product vs. Collector Current

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323

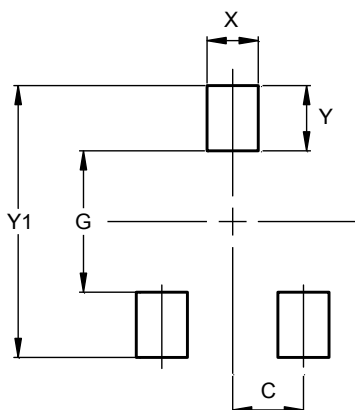


SOT323			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.

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