Absolute Maximum Ratings

V_{CC} to GND ....-0.3V to +6.0V
RESET, $\overline{RESET}$ to GND
(MAX6806/MAX6807) .... -0.3V to (V_{CC} + 0.3)
$\overline{RESET}$ to GND (MAX6808)................................-0.3V to + 6.0V
MR to GND (SOT143 package only) ....-0.3V to + 6.0V
Input Current, V_{CC} ....±20mA
Output Current, RESET, $\overline{RESET}$ ....±20mA
Rate of Rise, V_{CC} 100V/µs

Continuous Power Dissipation (T_A = +70°C)
    SOT23, SOT143 (derate 4mW/°C above +70°C) 320mW
    SC70 (derate 2.17mW/°C above +70vC) 174mW
    µMAX (derate 4.5mW/°C above +70°C) 362mW
    SO (derate 5.9mW/°C above +70°C) 471mW
Operating Temperature Range .... -40°C to +85°C
Storage Temperature Range........................... -65°C to +160°C
Lead Temperature (soldering, 10s) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics

(V_{CC} = +5V, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	VCC	TA = 0°C to +70°C		1.0		5.5	V
		TA = -40°C to +85°C		1.2		5.5	
Supply Current	ICC	VCC = +3.6V, no load			35	60	µA
		VCC = +5.5V, no load			50	80	
Reset Threshold	VTH-	TA = +25°C VCC falling	MAX680_ _ _46	4.508	4.60	4.692	V
			MAX680_ _ _32	3.136	3.20	3.264	
			MAX680_ _ _26	2.548	2.60	2.652	
			MAX680_ _ _23	2.254	2.30	2.346	
Reset Threshold Hysteresis	VHYST	TA = +25°C, Figure 2			0.02 VTH-	0.03 VTH-	V
RESET Output Voltage (MAX6806/MAX6808)	VOL	ISINK = 20µA, VCC = 1.0V				0.3	V
		ISINK = 1.0mA, VCC = 2.0V				0.4	
	VOH	ISOURCE = 2.0mA, VCC = 5.0V (MAX6806 only)		0.8 VCC			
RESET Output Leakage Current	IOH	VCC = 5.5V, RESET deasserted (MAX6808 only)				1	µA
RESET Output Voltage (MAX6807)	VOH	ISOURCE = 20µA, VCC = 1.2V		0.8 VCC			V
		ISOURCE = 400µA, VCC = 2.0V		0.8 VCC			
	VOL	ISINK = 3.2mA, VCC = 5.0V				0.4	
VCC to Reset Delay (Note 2)		VCC falling at 1mV/µs			30		µs
Reset Threshold Tempco					30		ppm/°C
MR Pulldown Resistance	RMR			40	80	120	kΩ
MR Input Threshold	VIL	VCC > VTH+				0.6	V
	VIH			0.7 VCC			

Note1: All devices are 100% production tested at T_A = +25°C, and are guaranteed by design for T_A = T_{MIN} to T_{MAX}, as specified.
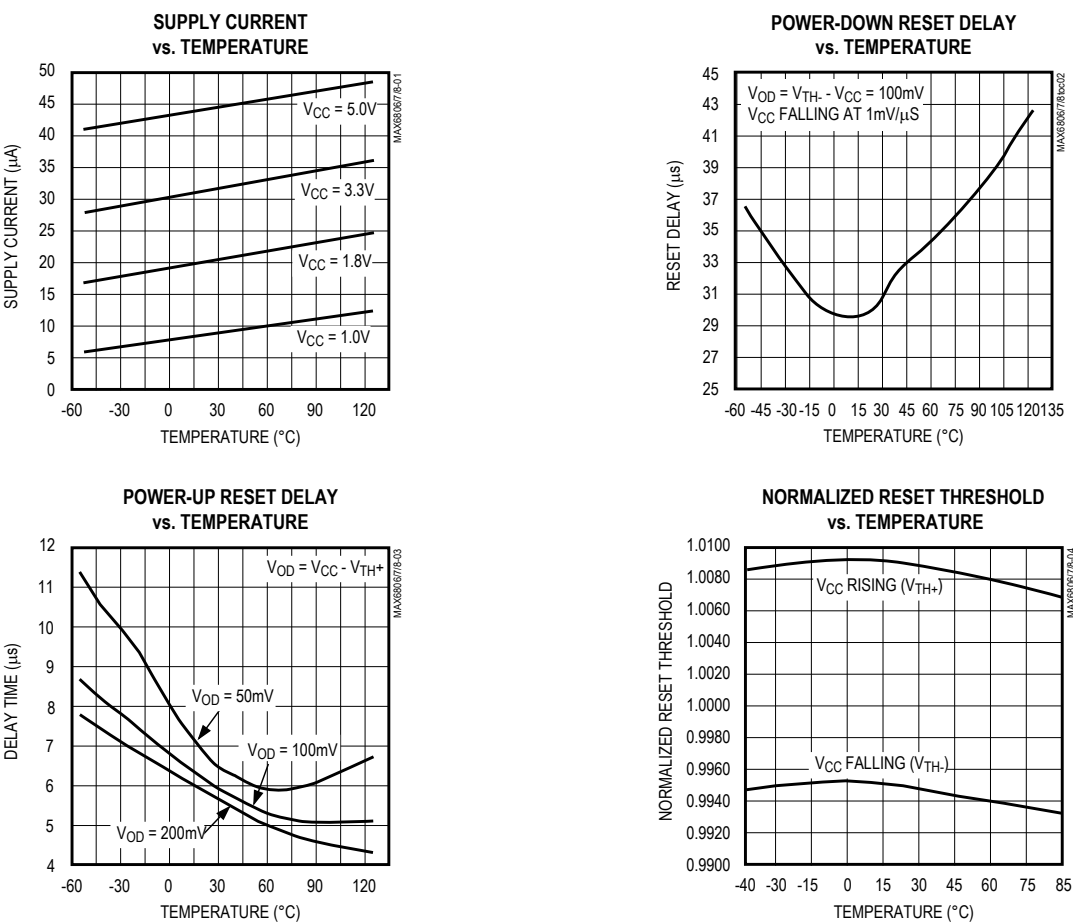Note 2: $\overline{RESET}$ output for MAX6806/MAX6808, RESET output for MAX6807.

Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

SUPPLY CURRENT vs. TEMPERATURE

POWER-DOWN RESET DELAY vs. TEMPERATURE

POWER-UP RESET DELAY vs. TEMPERATURE

NORMALIZED RESET THRESHOLD vs. TEMPERATURE

Pin Description

PIN			NAME	FUNCTION
SOT23/SC70	SOT143	µMAX/SO		
1	1	1	$\overline{\text{RESET}}$	Active-Low Reset Output (MAX6806/MAX6808). $\overline{\text{RESET}}$ remains low while V_{CC} is below the reset-threshold voltage or while MR is held high.
			RESET	Active-High Reset Output (MAX6807). RESET remains high while V_{CC} is below the reset-threshold voltage or while MR is held high.
2	4	4	GND	Ground
3	3	2	VCC	Supply Voltage Input
—	2	—	MR	Active-High Manual Reset Input. Internal 80kΩ resistor to GND.
—	—	3,5–8	N.C.	Not Internally Connected

Detailed Description

Reset Output

The MAX6806 voltage detector features an active-low, push-pull $\overline{RESET}$ output, while the MAX6807 features an active-high, push-pull RESET output. Unlike microprocessor (μP) supervisory circuits that offer a reset timeout period, the MAX6806 and MAX6807 $\overline{RESET}$ goes high and RESET goes low virtually immediately once V_{CC} exceeds the reset threshold. If a brownout occurs (V_{CC} falls below the reset threshold), $\overline{RESET}$ goes low and RESET goes high. $\overline{RESET}$ is guaranteed to be a logic low for $V_{CC} \geq 1V$ and RESET is guaranteed to be a logic high for $V_{CC} \geq 1.2V$.

The MAX6808 features an active-low, open-drain $\overline{RESET}$ output. The output sinks current when V_{CC} falls below the reset threshold. Connect a pullup resistor from $\overline{RESET}$ to any supply voltage up to 6V (Figure 1). Select a resistor value large enough to provide a logic low and small enough to provide a logic high while supplying all input and leakage currents connected to the $\overline{RESET}$ line. A 100kΩ resistor is sufficient in applications driving high-impedance loads.

The manual reset input (MR, 4-pin SOT143 package) can also initiate a reset (see Manual Reset Input section).
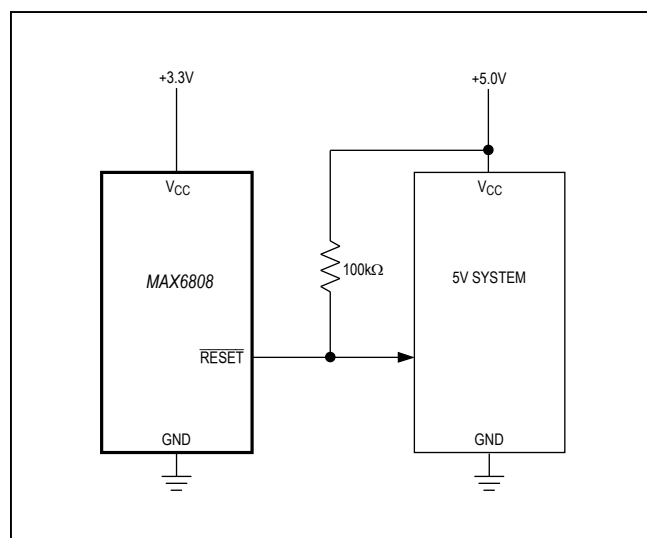
Manual-Reset Input

Many applications require manual-reset capabilities, allowing an operator, a test technician, or external logic circuitry to initiate a reset. A logic high on MR asserts a reset and remains asserted while MR is high. This input has an 80kΩ pulldown resistor, so the input can be left unconnected if not used. Connect a normally open momentary switch from MR to V_{CC} to create a manual-reset function. If MR is driven from long cables or if the device is used in a noisy environment, connecting a 0.1μF capacitor from MR to ground provides noise immunity.

Hysteresis

The MAX6806/MAX6807/MAX6808 feature internal hysteresis that creates two trip points: one for the rising supply voltage and one for the falling supply voltage (Figure 2). The hysteresis prevents the output from oscillating (chattering) when V_{CC} is near the reset threshold.

Reset Threshold Accuracy

The MAX6806/MAX6807/MAX6808 are ideal for sys-tems using a 5V ±5%, 3V ±5%, or 2.5V ±5% power supply with ICs specified for 5V ±10%, 3V ±10%, or 2.5V ±10%, respectively. The reset is guaranteed to assert after the power supply falls out of regulation, but before power drops below the minimum specified oper-ating voltage range for the system ICs.
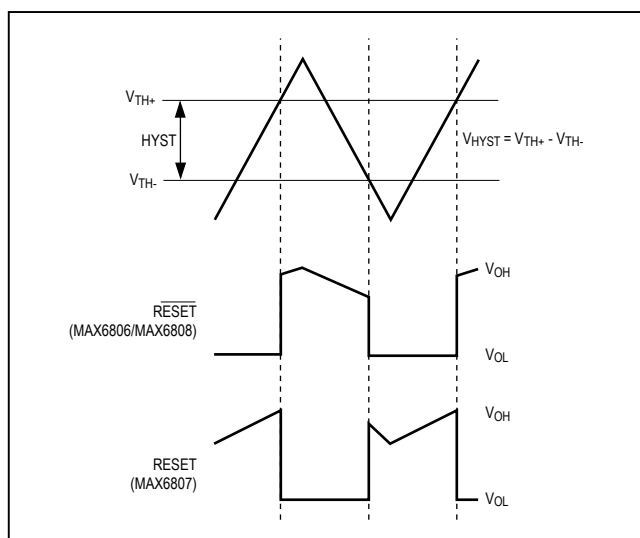

Figure 1. The MAX6808 Open-Drain $\overline{RESET}$ Output Allows Use with Multiple Supplies

Figure 2. Input and Output Waveforms with V_{CC} Varied

Applications Information

Negative-Going V_{CC} Transients

In addition to asserting a reset signal during power-up, power-down, and brown-out conditions, the MAX6806/MAX6807/MAX6808 are immune to short-duration, negative-going V_{CC} transients.

Figure 3 displays typical transient durations vs. reset-comparator overdrive for which the MAX6806/MAX6807/MAX6808 do **not** generate a reset pulse. The graph was generated using a negative-going pulse applied to V_{CC}, starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset-comparator overdrive). The graph indicates the maximum pulse width a negative-going V_{CC} transient can have without causing a reset pulse. As the magnitude of the transient increases (goes further below the reset threshold), the maximum allowable pulse width decreases. A 0.1µF capacitor mounted as close as possible to V_{CC} provides additional transient immunity.
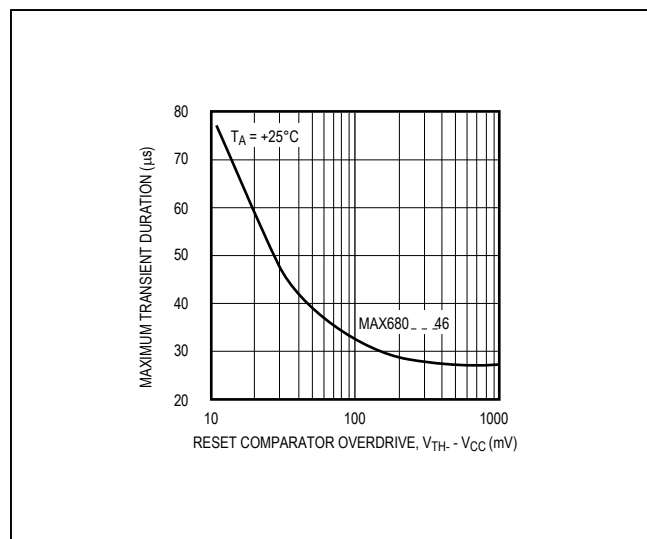
Ensuring a Valid Reset Output Down to V_{CC} = Ground

When V_{CC} falls below 1V, the MAX6806/MAX6808 $\overline{RESET}$ output no longer sinks current—it becomes an open circuit. Therefore, high-impedance CMOS logic inputs connected to $\overline{RESET}$ can drift to undetermined voltages. This presents no problem in most applications, since most circuitry is inoperative when V_{CC} is below 1V. In applications where $\overline{RESET}$ must be valid down to ground, add a pulldown resistor to $\overline{RESET}$ so any stray leakage currents flow to ground, holding $\overline{RESET}$ low (Figure 4). Select R1 to be large enough not to load $\overline{RESET}$ and small enough to pull $\overline{RESET}$ to ground. For most applications, 100kΩ will not load $\overline{RESET}$ and will pull $\overline{RESET}$ to ground. Similarly, if RESET (MAX6807) must be valid below 1.2V, add a pullup resistor to RESET.
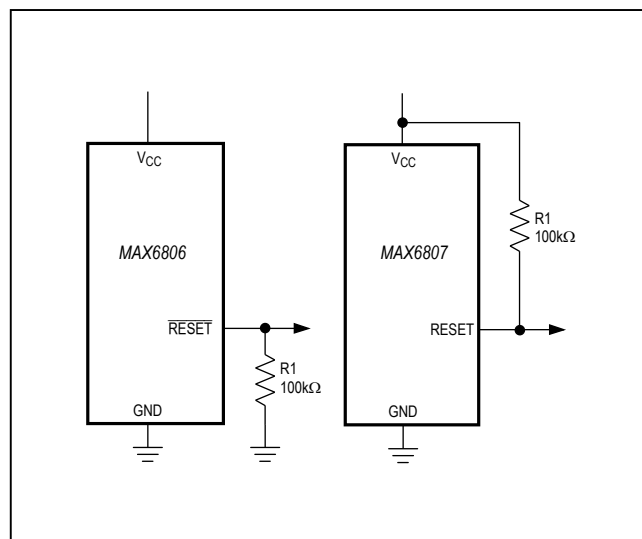

Figure 3. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

Figure 4. $\overline{RESET}$ / RESET Valid to V_{CC} = Ground Circuit

Interfacing to µPs with Bidirectional Reset Pins

µPs with bidirectional reset pins (such as the Motorola 68HC11 series) can contend with push-pull $\overline{RESET}$ outputs, resulting in indeterminate logic levels. Use the MAX6808 with the open-drain $\overline{RESET}$ when interfacing to this type of controller.
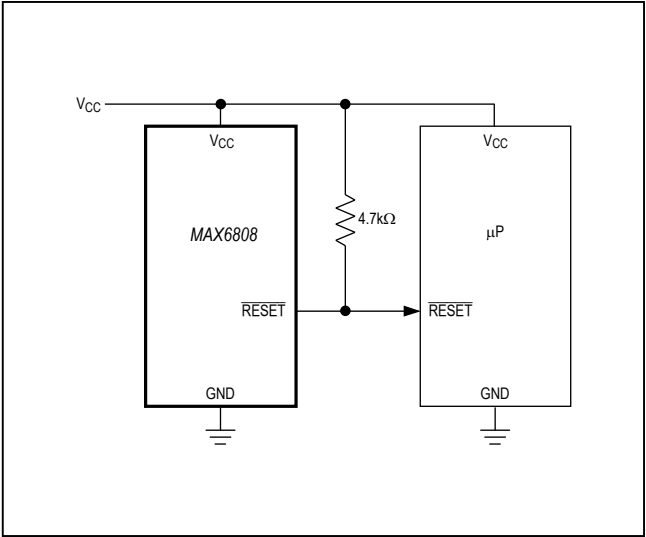

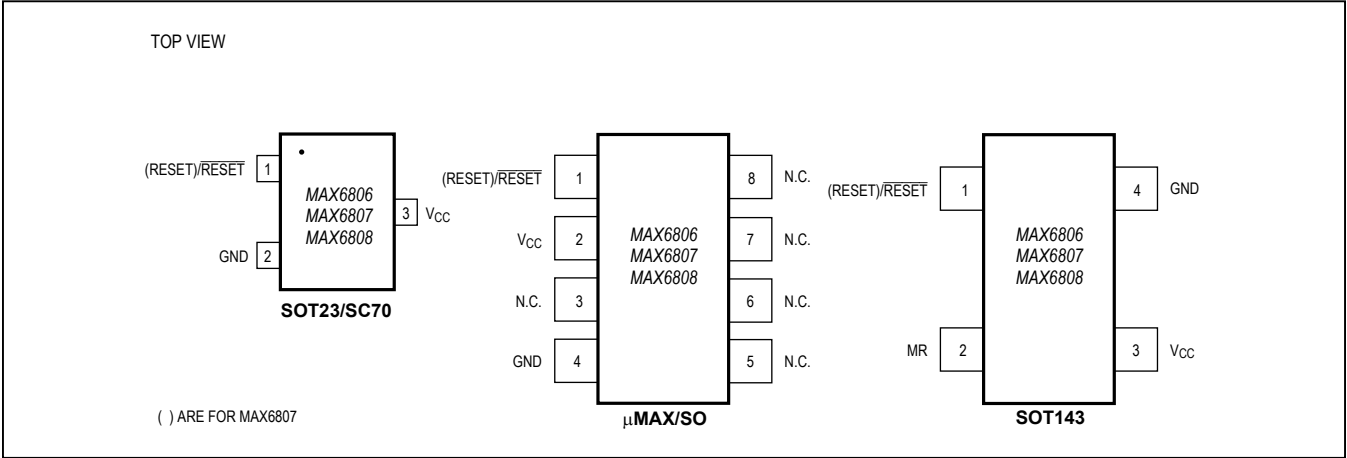Figure 5. Interfacing to µPs with Bidirectional Reset I/O

Selector Guide

PART	RESET OUTPUT	TOP MARK	NOMINAL V_{TH-} (V)†
MAX6806SA46	Active-Low, Push-Pull	—	4.6
MAX6806SA26		—	2.6
MAX6806SA23		—	2.3
MAX6806UA46		—	4.6
MAX6806UA26		—	2.6
MAX6806UA23		—	2.3
MAX6806UR46-T		FZDP	4.6
MAX6806UR26-T		FZDQ	2.6
MAX6806UR23-T		FZDR	2.3
MAX6806US46-T		KABT	4.6
MAX6806US26-T		KABU	2.6
MAX6806US23-T		KABV	2.3
MAX6806XR46-T		AAA	4.6
MAX6806XR26-T		AAB	2.6
MAX6806XR23-T		AAC	2.3

Selector Guide (continued)

PART	RESET OUTPUT	TOP MARK	NOMINAL V$_{TH-}$ (V) †
MAX6807SA46	Active-High, Push-Pull	—	4.6
MAX6807SA26		—	2.6
MAX6807UA46		—	4.6
MAX6807UA26		—	2.6
MAX6807UR46-T		FZDS	4.6
MAX6807UR26-T		FZDT	2.6
MAX6807US46-T		KABW	4.6
MAX6807US26-T		KABX	2.6
MAX6807XR46-T		AAD	4.6
MAX6807XR26-T		AAE	2.6
MAX6808SA46	Active-Low, Open-Drain	—	4.6
MAX6808SA32		—	3.2
MAX6808SA26		—	2.6
MAX6808UA46		—	4.6
MAX6808UA32		—	3.2
MAX6808UA26		—	2.6
MAX6808UR46-T		FZDU	4.6
MAX6808UR32-T		FZFL	3.2
MAX6808UR26-T		FZDV	2.6
MAX6808US46-T		KABY	4.6
MAX6808US32-T		KACC	3.2
MAX6808US26-T		KABZ	2.6
MAX6808XR46-T		AAF	4.6
MAX6808XR32-T		ACD	3.2
MAX6808XR26-T		AAG	2.6

Other voltage thresholds may be available. Contact factory for availability.

Pin Configurations

TOP VIEW

(RESET)/$\overline{RESET}$ | 1 — MAX6806 MAX6807 MAX6808 — 3 | V_{CC}

GND | 2

SOT23/SC70

( ) ARE FOR MAX6807

(RESET)/$\overline{RESET}$ | 1 — 8 | N.C.
V_{CC} | 2 — MAX6806 MAX6807 MAX6808 — 7 | N.C.
N.C. | 3 — 6 | N.C.
GND | 4 — 5 | N.C.

μMAX/SO

(RESET)/$\overline{RESET}$ | 1 — MAX6806 MAX6807 MAX6808 — 4 | GND
MR | 2 — 3 | V_{CC}

SOT143

Chip Information

TRANSISTOR COUNT: 72

Package Information

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
3 SC70	X3-2	21-0075	90-0208
3 SOT23	U3-1	21-0051	90-0179
4 SOT143	U4-1	21-0052	90-0183
8 SO	—	21-0041	—
8 μMAX	—	21-0036	—

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
3	7/14	No /V OPNs; removed Automotive reference from *Applications* section; updated *Package Information*	1, 9–13

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