

# LT1120A

## ABSOLUTE MAXIMUM RATINGS

(Note 1)

Input Voltage.....	36V	Operating Temperature Range	
NPN Collector Voltage.....	36V	LT1120AC.....	0°C to 100°C
Output Short-Circuit Duration .....	Indefinite	LT1120AI.....	–40°C to 100°C
Power Dissipation .....	Internally Limited	Storage Temperature Range .....	–65°C to 150°C

## PIN CONFIGURATION



## ORDER INFORMATION <http://www.linear.com/product/LT1120A#orderinfo>

LEAD FREE FINISH	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	TEMPERATURE RANGE
LT1120ACN8#PBF	LT1120ACN8#TRPBF	1120ACN8	8-Lead Plastic DIP	0°C to 100°C
LT1120ACS8#PBF	LT1120ACS8#TRPBF	1120A	8-Lead Plastic SO	0°C to 100°C
LT1120AIS8#PBF	LT1120AIS8#TRPBF	1120AI	8-Lead Plastic SO	–40°C to 100°C

Consult LTC Marketing for parts specified with wider operating temperature ranges.

For more information on lead free part marking, go to: <http://www.linear.com/leadfree/>

For more information on tape and reel specifications, go to: <http://www.linear.com/tapeandreeel/>. Some packages are available in 500 unit reels through designated sales channels with #TRMPBF suffix.

**ELECTRICAL CHARACTERISTICS**  $T_J = 25^\circ\text{C}$ .

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>Reference</b>					
Reference Voltage	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$	2.46	2.50	2.54	V
Line Regulation	$4.5\text{V} \leq V_{IN} \leq 36\text{V}$		0.01	0.015	%/V
Load Regulation	$-2.0\text{mA} \leq I_{REF} \leq 2\text{mA}$ , $V_{IN} = 12\text{V}$		0.3	0.6	%
Output Source Current	$V_{IN} = 5\text{V}$	2	4		mA
Output Sink Current	$V_{IN} = 5\text{V}$	2	4		mA
Temperature Stability			1		%
<b>Regulator</b>					
Supply Current	$V_{IN} = 6\text{V}$ , $I_{OUT} \leq 100\mu\text{A}$		20	25	$\mu\text{A}$
	$V_{IN} = 36\text{V}$ , $I_{OUT} \leq 100\mu\text{A}$		30	40	$\mu\text{A}$
	$V_{IN} = 12\text{V}$ , $I_{OUT} = 125\text{mA}$		11	20	mA
Output Current	$(V_{IN} - V_{OUT}) \geq 1\text{V}$ , $V_{IN} \geq 6\text{V}$	125			mA
Load Regulation	$(V_{IN} - V_{OUT}) \geq 1\text{V}$ , $V_{IN} \geq 6\text{V}$		0.2	0.5	%
Line Regulation	$6\text{V} \leq V_{IN} \leq 36\text{V}$		0.01	0.015	%/V
Dropout Voltage	$I_{OUT} = 100\mu\text{A}$		0.02	0.05	V
	$I_{OUT} = 125\text{mA}$		0.40	0.65	V
Feedback Sense Voltage	$V_{IN} = 12\text{V}$	2.44	2.5	2.56	V
Shutdown Pin Voltage	$V_{OUT} \leq 0.5\text{V}$	Normal		0.4	V
		Shutdown	2.2	1.4	V
Shutdown Pin Current	$V_{IN} = 1.4\text{V}$		10		$\mu\text{A}$
Feedback Bias Current			15	40	nA
Minimum Load Current	$V_{IN} = 36\text{V}$		1	5	$\mu\text{A}$
Short-Circuit Current	$V_{IN} = 36\text{V}$		250	400	mA
<b>Comparator</b>					
Offset Voltage	$V_{IN} = 36\text{V}$		3	7	mV
Bias Current	$V_{IN} = 36\text{V}$		15	40	nA
Gain	$\Delta V_{OUT} = 29\text{V}$ , $R_L = 20\text{k}$	2000	10000		V/V
Power Supply Rejection	$4.5\text{V} \leq V_S \leq 36\text{V}$	80	96		dB
Output Sink Current	$V_{IN} = 4.5\text{V}$	10	18		mA
Saturation Voltage	$I_{OUT} = 1\text{mA}$		0.4	0.6	V
Input Voltage Range		0		$V_{IN} - 1$	V
Response Time			5		$\mu\text{s}$
Leakage Current	$V_{IN} = 36\text{V}$ , $V(\text{Pin } 7) = 36\text{V}$			2	$\mu\text{A}$

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_J = 25^\circ\text{C}$ .

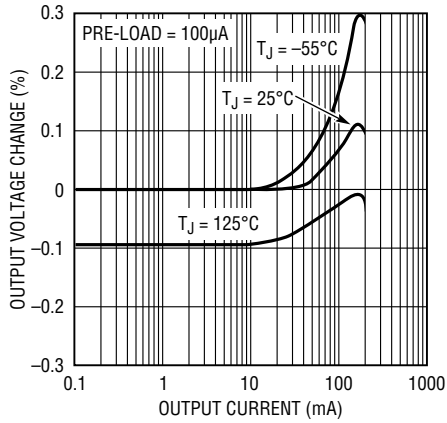
PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
<b>Reference</b>						
Reference Voltage	$4.5\text{V} \leq V_{\text{IN}} \leq 36\text{V}$	●	2.40	2.50	2.55	V
Line Regulation	$4.5\text{V} \leq V_{\text{IN}} \leq 36\text{V}$	●		0.01	0.02	%/V
Load Regulation	$-2.0\text{mA} \leq I_{\text{REF}} \leq 2\text{mA}$ , $V_{\text{IN}} = 12\text{V}$	●		0.4	0.8	%
Output Source Current	$V_{\text{IN}} = 5\text{V}$	●	2			mA
Output Sink Current	$V_{\text{IN}} = 5\text{V}$	●	2			mA
<b>Regulator</b>						
Supply Current	$V_{\text{IN}} = 6\text{V}$ , $I_{\text{OUT}} \leq 100\mu\text{A}$	●		30	40	$\mu\text{A}$
	$V_{\text{IN}} = 36\text{V}$ , $I_{\text{OUT}} \leq 100\mu\text{A}$	●		40	50	$\mu\text{A}$
	$V_{\text{IN}} = 12\text{V}$ , $I_{\text{OUT}} = 125\text{mA}$	●		11	20	mA
Output Current	$(V_{\text{IN}} - V_{\text{OUT}}) \geq 1\text{V}$ , $V_{\text{IN}} \geq 6\text{V}$	●	125			mA
Load Regulation	$(V_{\text{IN}} - V_{\text{OUT}}) \geq 1\text{V}$ , $V_{\text{IN}} \geq 6\text{V}$	●			1	%
Line Regulation	$6\text{V} \leq V_{\text{IN}} \leq 36\text{V}$	●			0.02	%/V
Dropout Voltage	$I_{\text{OUT}} = 100\mu\text{A}$	●			0.06	V
	$I_{\text{OUT}} = 125\text{mA}$	●			0.85	V
Feedback Sense Voltage	$V_{\text{IN}} = 12\text{V}$	●	2.38	2.5	2.57	V
Feedback Bias Current		●			50	nA
Minimum Load Current	$V_{\text{IN}} = 36\text{V}$	●			50	$\mu\text{A}$
Short-Circuit Current	$V_{\text{IN}} = 36\text{V}$	●		240	400	mA
<b>Comparator</b>						
Offset Voltage	$V_{\text{IN}} = 36\text{V}$	●			10	mV
Bias Current	$V_{\text{IN}} = 36\text{V}$	●		15	60	nA
Gain	$\Delta V_{\text{OUT}} = 29\text{V}$ , $R_L = 20\text{k}$	●	1000			V/V
Output Sink Current (Note 2)	$V_{\text{IN}} = 4.5\text{V}$	●	5	10		mA
Leakage Current	$V_{\text{IN}} = 36\text{V}$ , $V(\text{Pin } 7) = 36\text{V}$	●			8	$\mu\text{A}$

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** For  $T_A \leq -40^\circ\text{C}$  output sink current drops to 2.5mA.

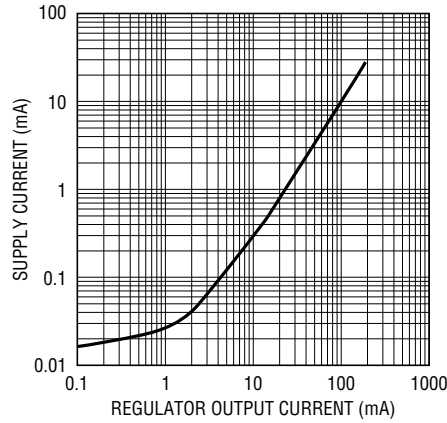
# TYPICAL PERFORMANCE CHARACTERISTICS

**Regulator Load Regulation**



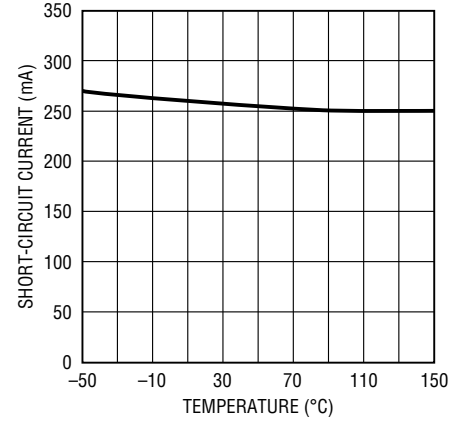
1120A G01

**Supply Current**



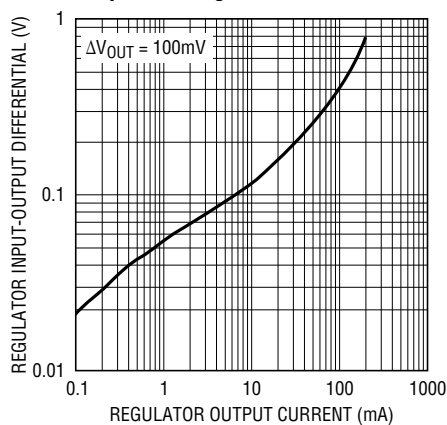
1120A G02

**Regulator Short-Circuit Current**



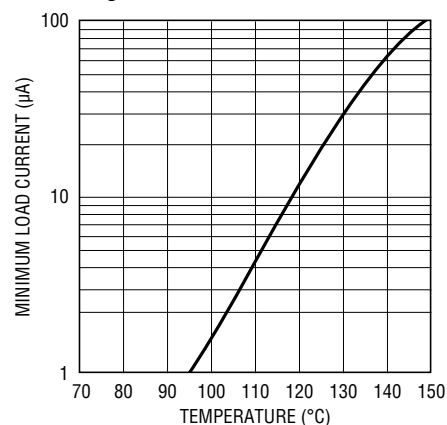
1120A G03

**Dropout Voltage**



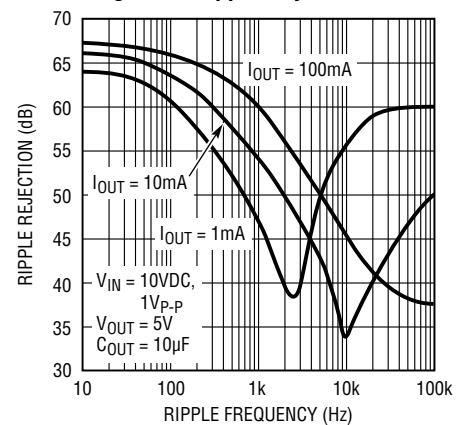
1120A G04

**Regulator Minimum Load Current**



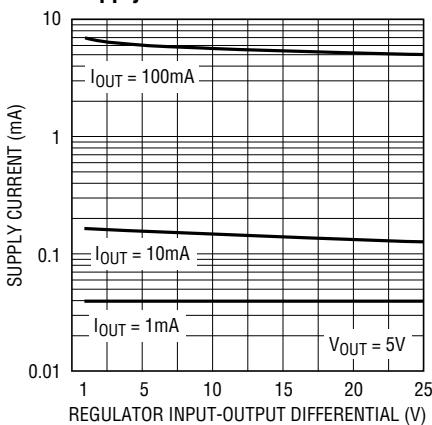
1120A G05

**Regulator Ripple Rejection**



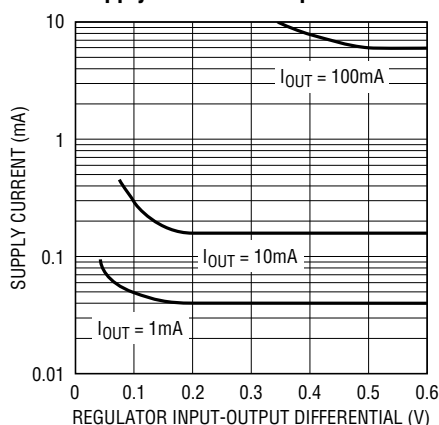
1120A G06

**Supply Current**



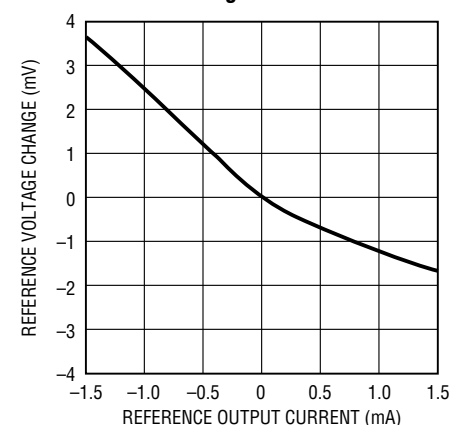
1120A G07

**Supply Current at Dropout**



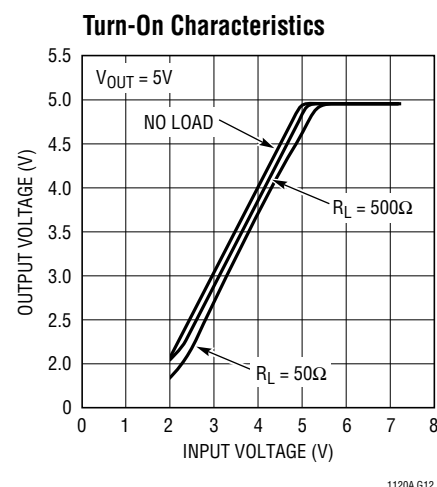
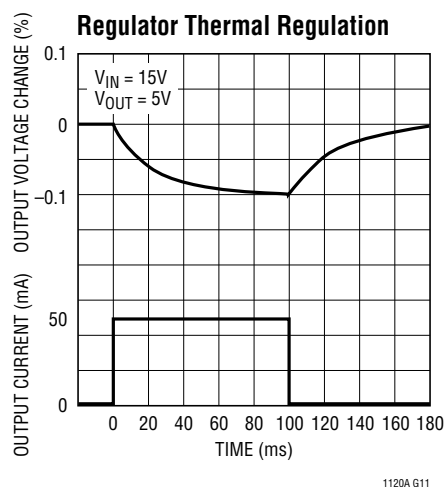
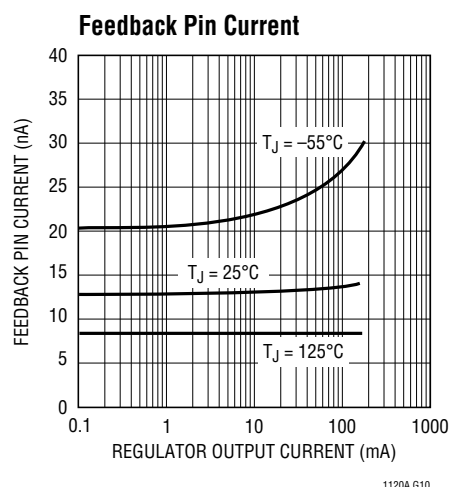
1120A G08

**Reference Regulation**



1120A G09

## TYPICAL PERFORMANCE CHARACTERISTICS



## PIN FUNCTIONS

**GND (Pin 1):** Ground

**FEEDBACK (Pin 2):** This is the feedback point of the regulator. When operating, it is nominally at 2.5V. Optimum source resistance is 200k to 500k. The feedback pin should not be driven below ground or more positive than 5V.

**SHUTDOWN (Pin 3):** A logic 1 shuts off main regulator. Caution: noise or leakage into the shutdown pin can affect output voltage.

**$V_{OUT}$  (Pin 4):** Main output, requires 10 $\mu\text{F}$  output capacitor. Can be shorted to  $V_{IN}$  or ground without damaging device.

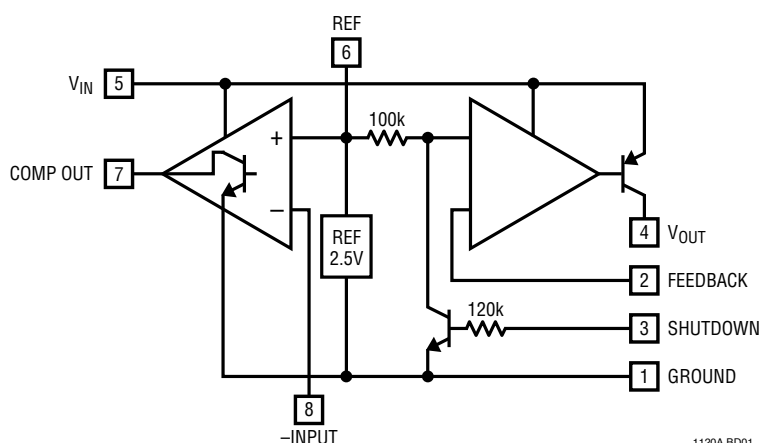
**$V_{IN}$  (Pin 5):** Input Supply. Bypass with 10 $\mu\text{F}$  capacitor. Must always be more positive than ground.

**REF (Pin 6):** Reference. 2.5V can source or sink current. May be shorted to ground or up to 5V. Voltages in excess of 5V can damage the device.

**COMP OUT (Pin 7):** Open Collector NPN Output. May be connected to any voltage from ground to 36V more positive than ground (operates above  $V_{IN}$ ). Short-circuit protected.

**-INPUT (Pin 8):** Inverting Comparator Input.

## BLOCK DIAGRAM



## APPLICATIONS INFORMATION

The LT1120A is especially suited for micropower system applications. For example, the comparator section of the LT1120A may be used as a battery checker to provide an indication of low battery. Another type of system application for the LT1120A would be to generate the equivalent of split supplies off of a single power input. The regulator section provides regulated output voltage and the reference, which can both source and sink current is then an artificial system ground providing a split supply for the system.

For many applications the comparator can be frequency compensated to operate as an amplifier. Compensation values for various gains are given in the data sheet. The comparator gain is purposely low to make it easier to frequency compensate as an amplifier. The NPN output is capable of sinking 10mA and can drive loads connected to voltages in excess of the positive power supply. This is useful for driving switches or linear regulators off of a higher input voltage.

### Reference

Internal to the LT1120A is a 2.5V trimmed class B output reference. The reference was designed to be able to source or sink current so it could be used in supply splitting applications as well as a general purpose reference for external circuitry. The design of the reference allows it to source and sink typically 4mA. The available source and sink current decreases as temperature increases. It is sometimes desirable to decrease the AC output impedance by placing an output capacitor on Pin 6. The reference in the LT1120A becomes unstable with large capacitive loads placed directly on it. When using an output capacitor, about 20 $\Omega$  should be used to isolate the capacitor from the reference pin. This 20 $\Omega$  resistor can be placed directly in series with the capacitor or alternatively the reference line can have 20 $\Omega$  placed in series with it and then a capacitor to ground. This is shown in Figure 1.

## APPLICATIONS INFORMATION

Other than placing large capacitive loads on the reference, no other precautions are necessary and the reference is stable with nominal stray capacitances.

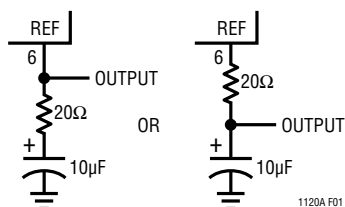


Figure 1. Bypassing Reference

### Overload Protection

The main regulator in the LT1120A is current limited at approximately 250mA. The current limit is stable with both input voltage and temperature.

Like most other IC regulators, a minimum load is required on the output of the LT1120A to maintain regulation. For most standard regulators this is normally specified at 5mA. Of course, for a micropower regulator this would be a tremendously large current. The output current must be large enough to absorb all the leakage current of the pass transistor at the maximum operating temperature. It also

affects the transient response; low output currents have long recovery times from load transients. At high operating temperatures the minimum load current increases and having too low of a load current may cause the output to go unregulated. Devices are tested for minimum load current at high temperature. The output voltage setting resistors to the feedback terminal can usually be used to provide the minimum load current.

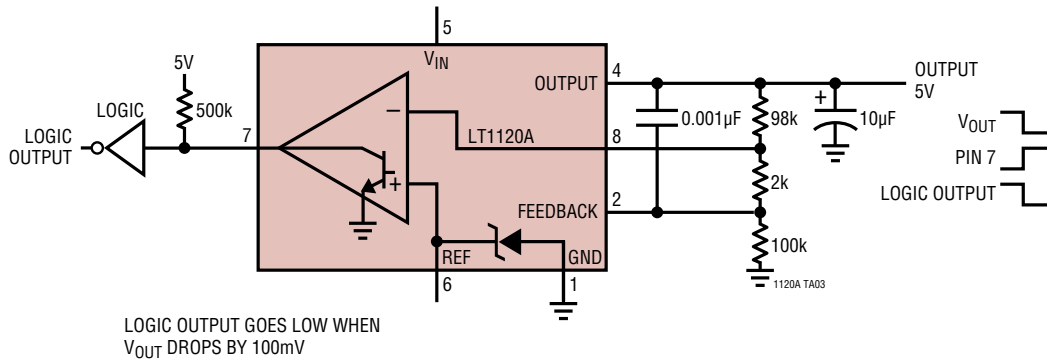
### Frequency Compensation

The LT1120A is frequency compensated by a dominant pole on the output. An output capacitor of 10μF is usually large enough to provide good stability. **Increasing the output capacitor above 10μF further improves stability.** In order to ensure stability, a feedback capacitor is needed between the output pin and the feedback pin. This is because stray capacitance can form another pole with the large value of feedback resistors used with the LT1120A. Also, a feedback capacitor minimizes noise pickup and improves ripple rejection.

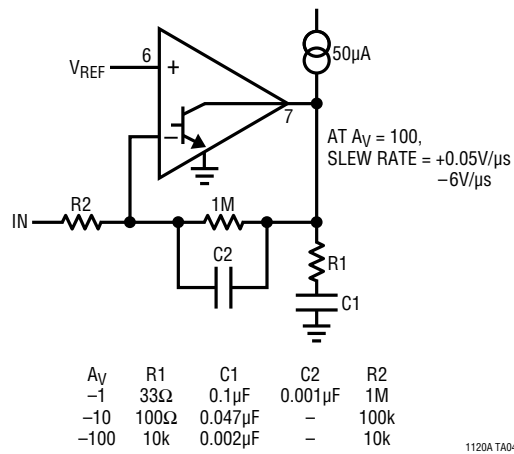
With the large dynamic operating range of the output current, 10000:1, frequency response changes widely. Low AC impedance capacitors are needed to insure stability. While solid tantalum are best, aluminum electrolytics can be used but larger capacitor values may be needed.

## TYPICAL APPLICATIONS

### Regulator with Output Voltage Monitor



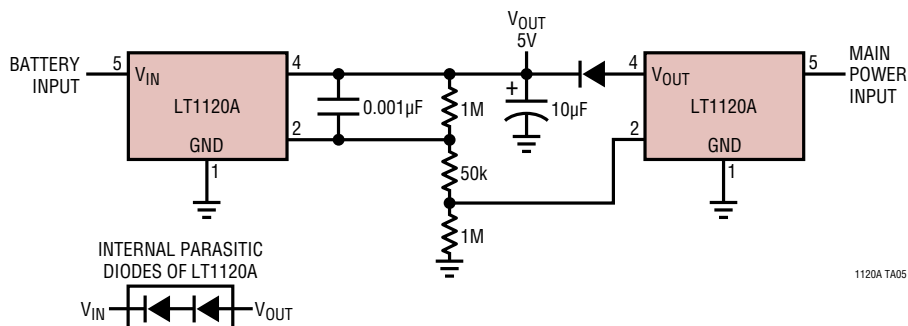
### Compensating the Comparator as an Op Amp



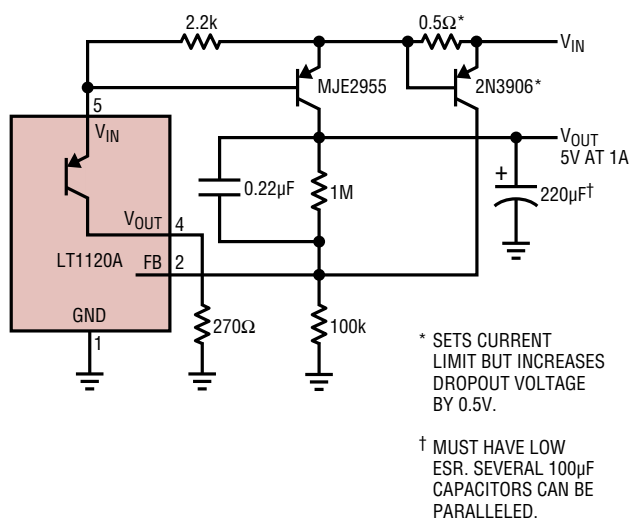


## TYPICAL APPLICATIONS

### Battery Backup Regulator



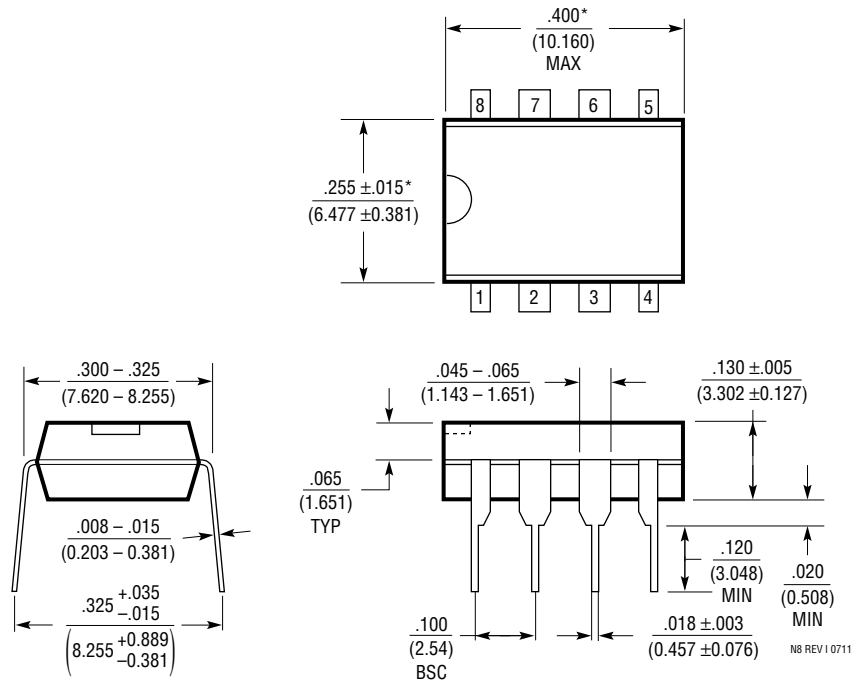
### Current Limited 1 Amp Regulator



## PACKAGE DESCRIPTION

Please refer to <http://www.linear.com/product/LT1120A#packaging> for the most recent package drawings.

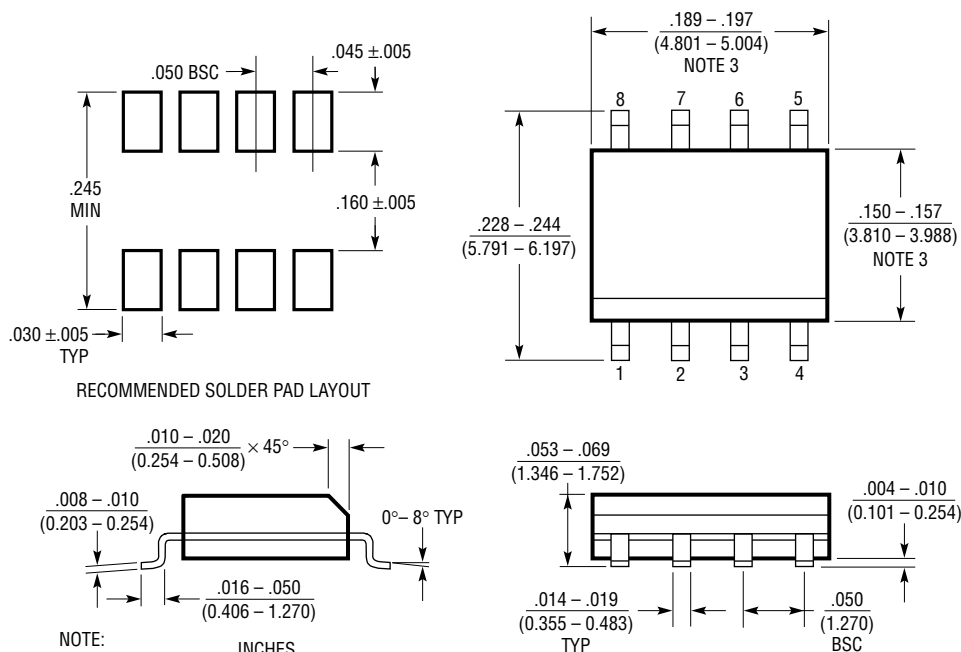
### N Package 8-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510 Rev I)



## PACKAGE DESCRIPTION

Please refer to <http://www.linear.com/product/LT1120A#packaging> for the most recent package drawings.

### S8 Package 8-Lead Plastic Small Outline (Narrow .150 Inch) (Reference LTC DWG # 05-08-1610 Rev G)



- NOTE:
1. DIMENSIONS IN  $\frac{\text{INCHES}}{\text{MILLIMETERS}}$
  2. DRAWING NOT TO SCALE
  3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED  $.006''$  (0.15mm)
  4. PIN 1 CAN BE BEVEL EDGE OR A DIMPLE

S08 REV G 0212

## REVISION HISTORY (Revision history begins at Rev B)

REV	DATE	DESCRIPTION	PAGE NUMBER
B	05/17	Changed units in Electrical Characteristics Regulator section	3

## RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
<a href="#">LT1761/LT1762</a>	100mA/150mA, Low Noise Micropower, LDO	$V_{IN}$ : 1.8V to 20V; $V_{OUT(MIN)}$ = 1.22V; Dropout Voltage = 0.30V; $I_Q$ = 20 $\mu$ A; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = Adj, 1.5V, 1.8V, 2V, 2.5V, 2.8V, 3V, 3.3V, 5V; ThinSOT™/MS8 Packages. Low Noise < 20 $\mu$ V <sub>RMS</sub>
<a href="#">LT1763</a>	500mA, Low Noise Micropower, LDO	$V_{IN}$ : 1.8V to 20V; $V_{OUT(MIN)}$ = 1.22V; Dropout Voltage = 0.30V; $I_Q$ = 30 $\mu$ A; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = 1.5V, 1.8V, 2.5V, 3V, 3.3V, 5V; SO-8 Package. Low Noise < 20 $\mu$ V <sub>RMS</sub>
<a href="#">LT1764/LT1764A</a>	3A, Low Noise, Fast Transient Response, LDO	$V_{IN}$ : 2.7V to 20V; $V_{OUT(MIN)}$ = 1.21V; Dropout Voltage = 0.34V; $I_Q$ = 1mA; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = 1.8V, 2.5V, 3.3; DD, TO220, Packages. Low Noise < 40 $\mu$ V <sub>RMS</sub> , "A" Version Stable with Ceramic Capacitors
<a href="#">LT1962</a>	300mA, Low Noise Micropower, LDO	$V_{IN}$ : 1.8V to 20V; $V_{OUT(MIN)}$ = 1.22V; Dropout Voltage = 0.27V; $I_Q$ = 30 $\mu$ A; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = 1.5V, 1.8V, 2.5V, 3V, 3.3V, 5V; MS8 Package. Low Noise < 20 $\mu$ V <sub>RMS</sub>
<a href="#">LT1963/LT1963A</a>	1.5A, Low Noise, Fast Transient Response, LDO	$V_{IN}$ : 2.1V to 20V; $V_{OUT(MIN)}$ = 1.21V; Dropout Voltage = 0.34V; $I_Q$ = 1mA; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = 1.5V, 1.8V, 2.5V, 3.3; DD, TO220, SOT-223, SO-8 Packages. Low Noise < 40 $\mu$ V <sub>RMS</sub> , "A" Version Stable with Ceramic Capacitors
<a href="#">LT1964</a>	200mA, Low Noise Micropower Negative, LDO	$V_{IN}$ : -1.9V to -20V; $V_{OUT(MIN)}$ = -1.21V; Dropout Voltage = 0.34V; $I_Q$ = 30 $\mu$ A; $I_{SD}$ < 3 $\mu$ A; $V_{OUT}$ = Adj, -5V; ThinSOT Package. Low Noise < 30 $\mu$ V <sub>RMS</sub> , Stable with Ceramic Capacitors
<a href="#">LT3010</a>	80V, 50mA, Low Noise Linear Regulator	$V_{IN}$ : 1.5V to 80V; $V_{OUT(MIN)}$ = 1.28V; Dropout Voltage = 0.30V; $I_Q$ = 30 $\mu$ A; $I_{SD}$ < 1 $\mu$ A; $V_{OUT}$ = Adj, -5V; MS8E Package. Low Noise < 100 $\mu$ V <sub>RMS</sub> , Stable with Ceramic Capacitors