

RATING AND CHARACTERISTIC CURVES L35L5V0CB2

LITEON

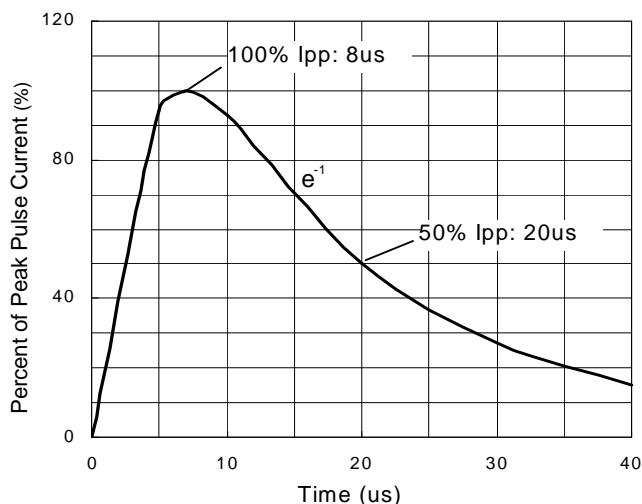


Figure 1. 8/20 us pulse waveform according to IEC 61000-4-5

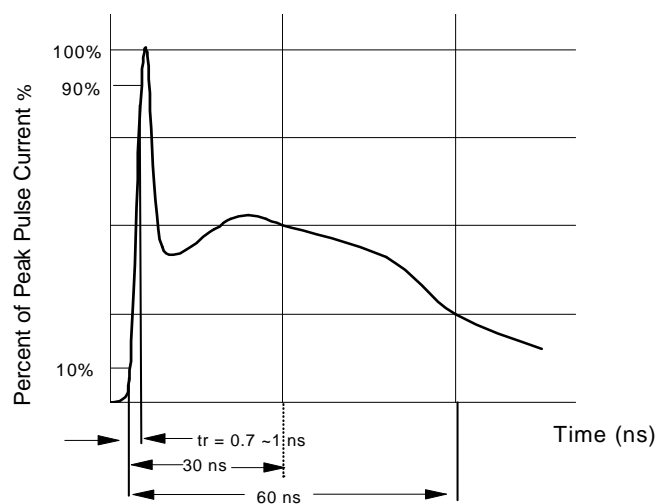


Figure 2. ESD pulse waveform according to IEC 61000-4-2

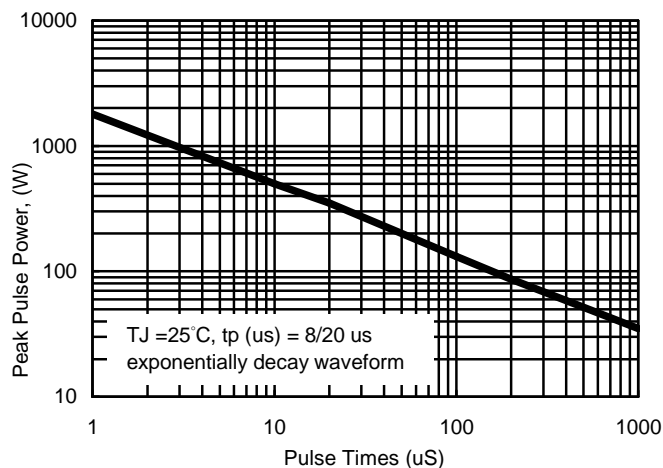


Figure 3. Power Dissipation versus Pulse Time

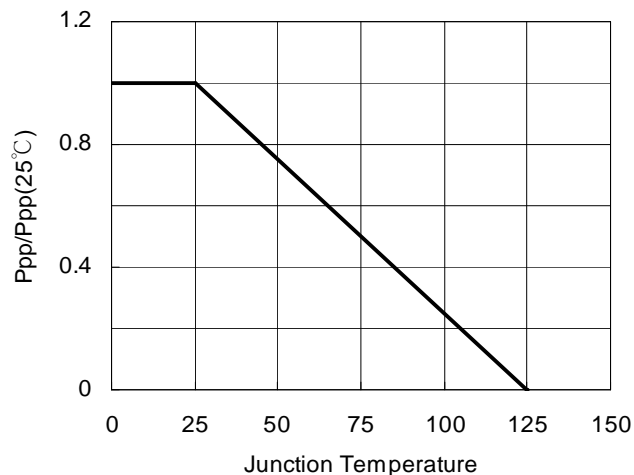


Figure 4. Peak pulse power versus TJ

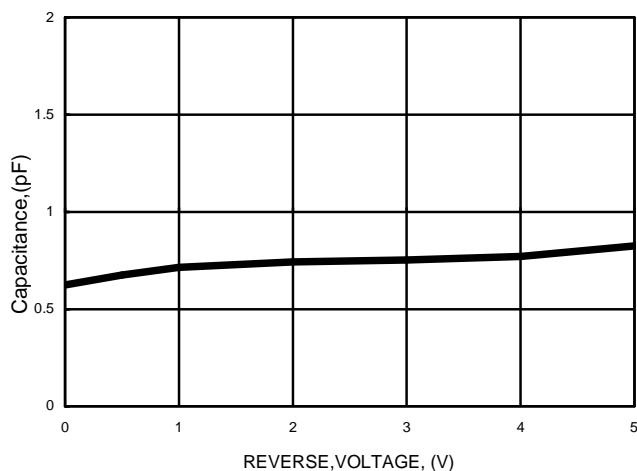


Figure 5. Typical Junction Capacitance

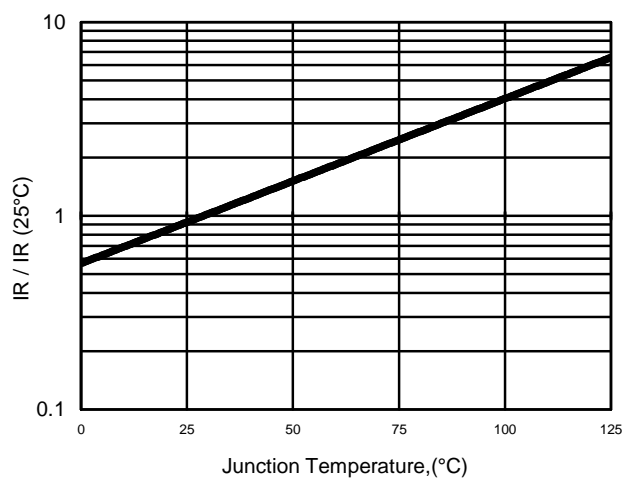


Figure 6. Reverse Leakage Current versus TJ

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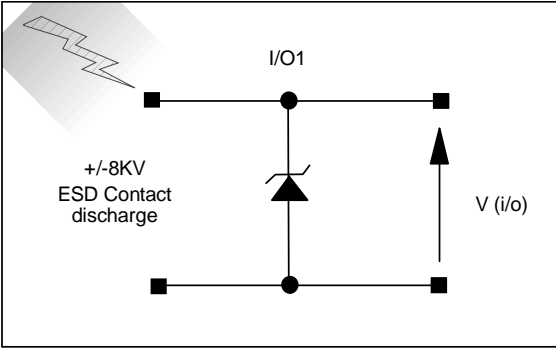


Figure 7. ESD Test Configuration

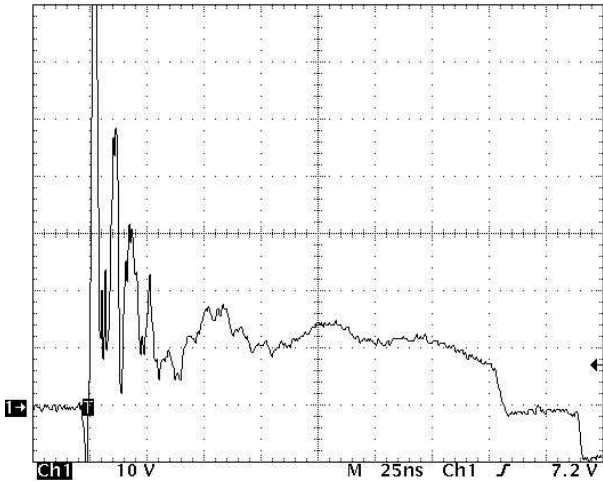


Figure 8. Clamped +8 kV ESD voltage waveform

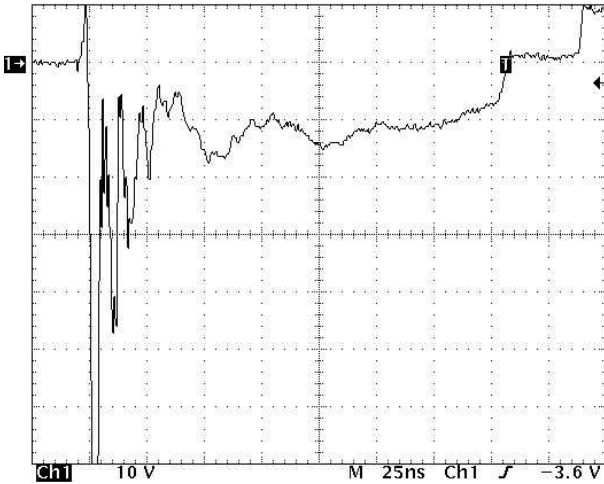
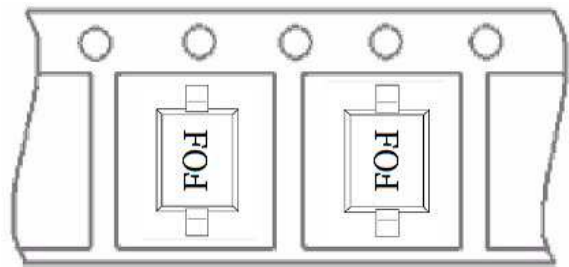


Figure 9. Clamped -8 kV ESD voltage waveform

MARKING AND PACKAGING INFORMATION
L35L5V0CB2

Marking and Orientation :

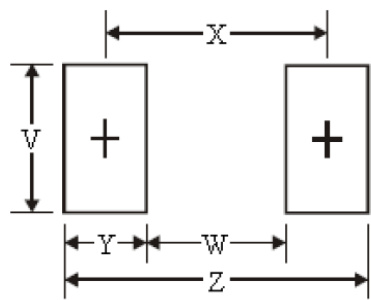


Note: Marking is none direction

Packaging Information :

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L35L5V0CB2	3000	7	45K	90K

SOD-323 Soldering Pad Layout :



Dim.	Millimeters	Inches
Z	3.05	0.120
X	2.15	0.084
W	1.25	0.049
Y	0.90	0.035
V	0.70	0.027

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