# STEALTH™ Diode 30 A, 600 V

# ISL9R3060G2, ISL9R3060P2

### Description

The ISL9R3060G2, ISL9R3060P2 is a STEALTH  $^{\text{TM}}$  diode optimized for low loss performance in high frequency hard switched applications. The STEALTH family exhibits low reverse recovery current ( $I_{\text{TT}}$ ) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low  $I_{\text{TT}}$  and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

### **Features**

- Stealth Recovery,  $t_{rr} = 36 \text{ ns}$  (@  $I_F = 30 \text{ A}$ )
- Max Forward Voltage,  $V_F = 2.4 \text{ V}$  (@  $T_C = 25^{\circ}\text{C}$ )
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- This Device is Pb-Free and is RoHS Compliant

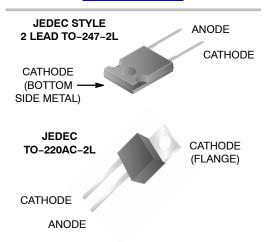
### **Applications**

- SMPS
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

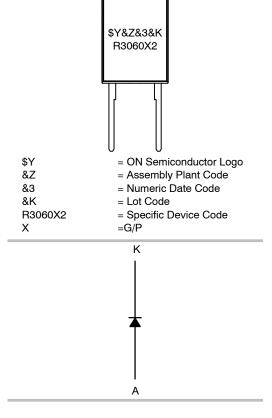


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#### **MARKING DIAGRAM**



### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **DEVICE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$	600	V
Working Peak Reverse Voltage	$V_{RWM}$	600	V
DC Blocking Voltage	$V_{R}$	600	V
Average Rectified Forward Current	I <sub>F(AV)</sub>	30	Α
Repetitive Peak Surge Current (20 kHz Square Wave )	I <sub>FRM</sub>	70	Α
Nonrepetitive Peak Surge Current (Halfwave, 1 Phase, 60 Hz)	I <sub>FSM</sub>	325	Α
Power Dissipation	$P_{D}$	200	W
Avalanche Energy (1 A, 40 mH)	E <sub>AVL</sub>	20	mJ
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to 175	°C
Maximum Temperature for Soldering Leads at 0.063 in (1.6 mm) from Case for 10 s	TL	300	°C
Maximum Temperature for Soldering Package Body for 10 s	T <sub>PKG</sub>	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Methode	Reel Size	Tape Width	Quantity
R3060G2	ISL9R3060G2	TO-247-2L	Tube	N/A	N/A	30
R3060G2	ISL9R3060P2	TO-220AC-2L	Tube	N/A	N/A	50

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25$ °C unless otherwise noted)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
OFF STATE C	HARACTERISTICS					•	
I <sub>R</sub>	Instantaneous Reverse Current	V <sub>R</sub> = 600 V	T <sub>C</sub> = 25°C	-	-	100	μΑ
			T <sub>C</sub> = 125°C	-	-	1	mA
ON CHARACT	TERISTICS						
V <sub>F</sub>	Instantaneous Forward Voltage	I <sub>F</sub> = 30 A T <sub>C</sub> = 25°C		-	2.1	2.4	V
			T <sub>C</sub> = 125°C	-	1.7	2.1	V
DYNAMIC CH	ARACTERISTICS	•	•		•		
CJ	Junction Capacitance	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A		-	120	_	pF
SWITCHING C	CHARACTERISTICS	•			•	1	
T <sub>rr</sub>	Reverse Recovery Time	$I_F = 1 \text{ A, di}_F/dt = 100 \text{ A/}\mu\text{s, V}_R = 30 \text{ V}$ $I_F = 30 \text{ A, di}_F/dt = 100 \text{ A/}\mu\text{s, V}_R = 30 \text{ V}$		-	27	35	ns
				-	36	45	ns
T <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30 A		_	36	-	ns
I <sub>RR</sub>	Reverse Recovery Current	di <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 390 V		-	2.9	-	Α
Q <sub>RR</sub>	Reverse Recovery Charge	T <sub>C</sub> = 25°C		_	55	_	nC
T <sub>rr</sub>	Reverse Recovery Time	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/μs}$ $V_R = 390 \text{ V}$ , $T_C = 125^{\circ}\text{C}$		_	110	_	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub> )			-	1.9	-	-
I <sub>RR</sub>	Reverse Recovery Current			-	6	-	Α
Q <sub>RR</sub>	Reverse Recovery Charge			_	450	-	nC

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted) (continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VITCHING C	CHARACTERISTICS					
T <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30 A	-	60	-	ns
S	Softness Factor (t <sub>b</sub> /t <sub>a</sub> )	dl <sub>F</sub> /dt = 1000 A/μs V <sub>R</sub> = 390 V	-	1.25	-	-
I <sub>RR</sub>	Reverse Recovery Current	T <sub>C</sub> = 125°C	-	21	-	Α
Q <sub>RR</sub>	Reverse Recovery Charge		-	730	-	nC
dI <sub>M</sub> /dt	Maximum di/dt during t <sub>b</sub>		-	800	-	A/μs
IERMAL CH	ARACTERISTICS					
$R_{ heta JC}$	Thermal Resistance Junction to Case		-	-	0.75	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-247	-	_	30	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220	_	_	62	°C/W

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### **TYPICAL PERFORMANCE CURVES**

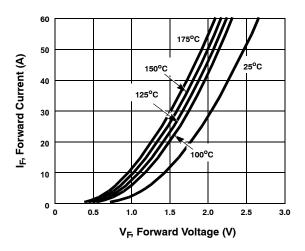


Figure 1. Forward Current vs. Forward Voltage

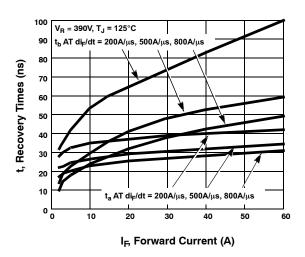


Figure 3. t<sub>a</sub> and t<sub>b</sub> Curves vs. Forward Current

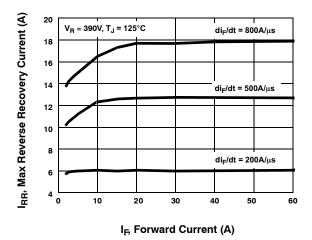


Figure 5. Maximum Reverse Recovery Current vs. Forward Current

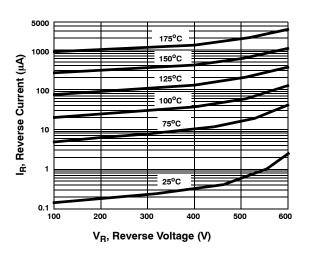


Figure 2. Reverse Current vs. Reverse Voltage

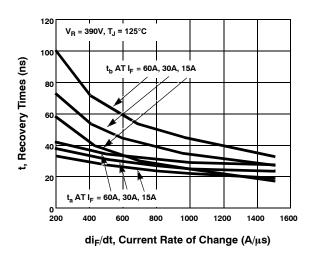
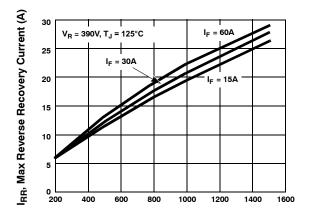


Figure 4.  $t_a$  and  $t_b$  Curves vs.  $di_F/dt$ 



 $di_F/dt$ , Current Rate of Change (A/ $\mu$ s)

Figure 6. Maximum Reverse Recovery Current vs. di<sub>F</sub>/dt

### TYPICAL PERFORMANCE CURVES

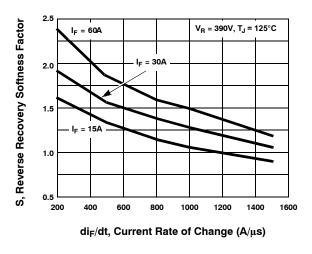


Figure 7. Reverse Recovery Softness Factor vs. di<sub>F</sub>/dt

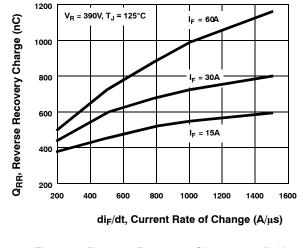


Figure 8. Reverse Recovery Charge vs. di<sub>F</sub>/dt

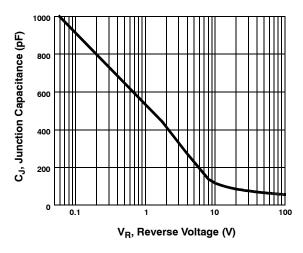


Figure 9. Junction Capacitance vs. Reverse Voltage

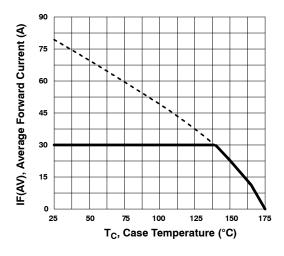


Figure 10. Forward Current Derating Curve

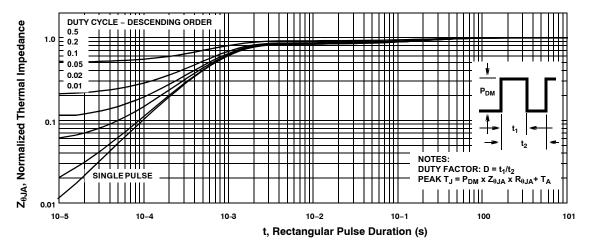


Figure 11. Normalized Maximum Transient Thermal Impedance

### **TEST CIRCUITS AND WAVEFORMS**

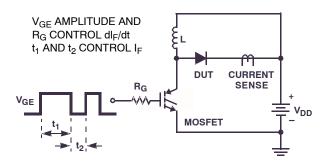


Figure 12. T<sub>rr</sub> Test Circuit

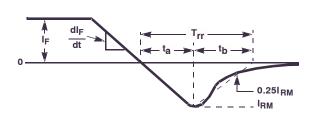


Figure 13.  $T_{rr}$  Waveforms and Definitions

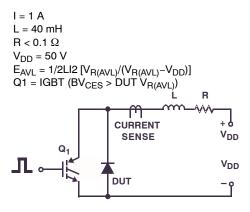


Figure 14. Avalanche Energy Test Circuit

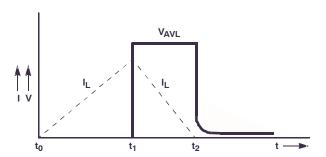


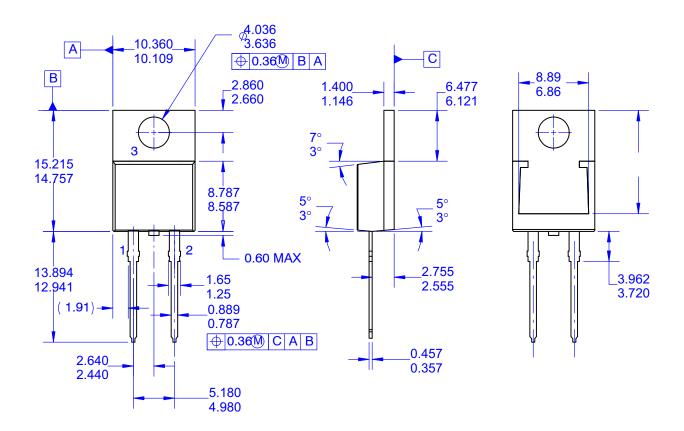
Figure 15. Avalanche Current and Voltage Waveforms

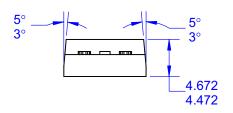
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**DATE 31 AUG 2016** 





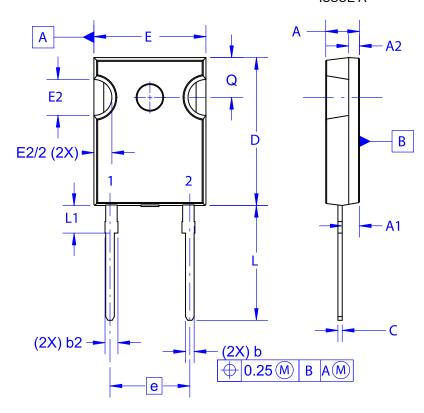
NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220 VARIATION AC.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
  C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

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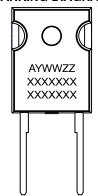
### TO-247-2LD CASE 340CL **ISSUE A**





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- B. ALL DIMENSIONS ARE IN MILLIMETERS.
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  D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

### **GENERIC MARKING DIAGRAM\***



XXXX = Specific Device Code

= Assembly Location

= Year

WW = Work Week

= Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

	DATE 03 DEC 2019			
Ø P —			—	P1 D2
E1 —		1		D1
•				

DIM	MIL	MILLIMETERS				
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A1	2.29	2.40	2.66			
A2	1.30	1.50	1.70			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
С	0.51	0.61	0.71			
D	20.32	20.57	20.82			
D1	16.37	16.57	16.77			
D2	0.51	0.93	1.35			
Е	15.37	15.62	15.87			
E1	12.81	l	~			
E2	4.96	5.08	5.20			
е	~	11.12	~			
L	15.75	16.00	16.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
ØP1	6.61	6.73	6.85			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			

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