



**GaAs MMIC I/Q DOWNCONVERTER
12 - 16 GHz**

Data Taken As IRM With External 90° IF Hybrid

Conversion Gain vs. Temperature

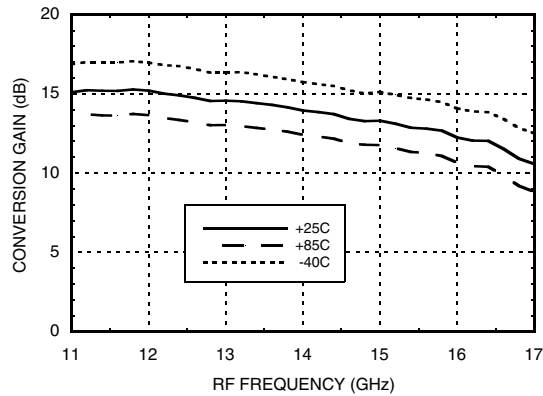
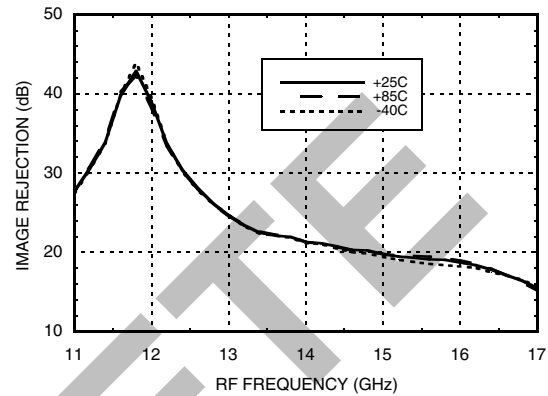
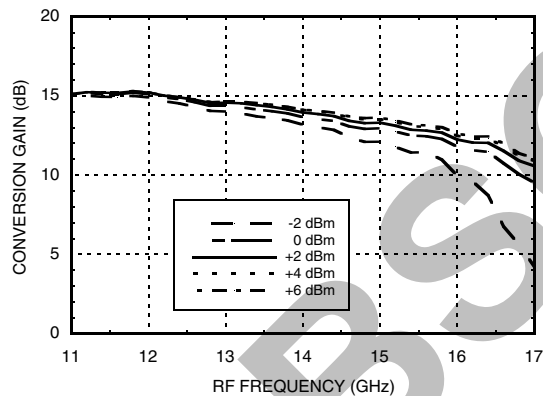


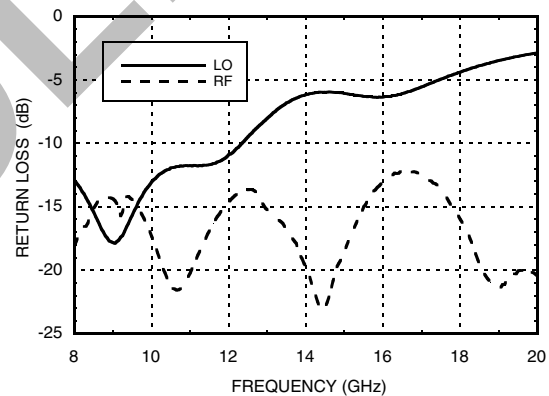
Image Rejection vs. Temperature



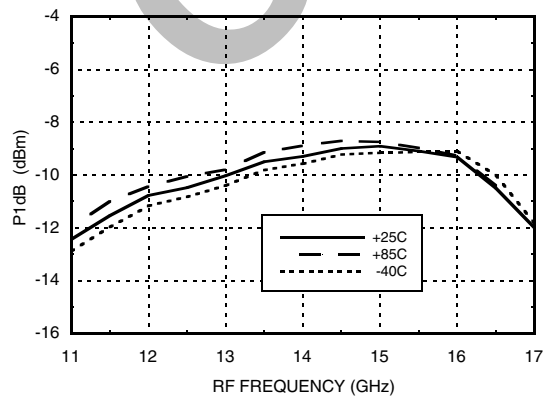
Conversion Gain vs. LO Drive



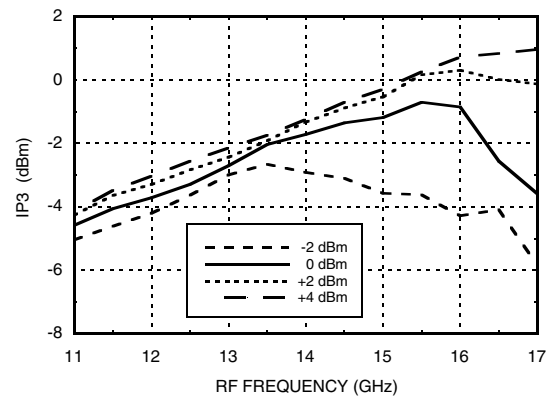
Return Loss



Input P1dB vs. Temperature



Input IP3 vs. LO Drive

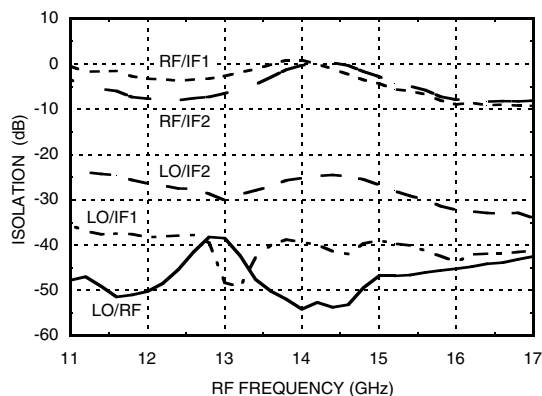




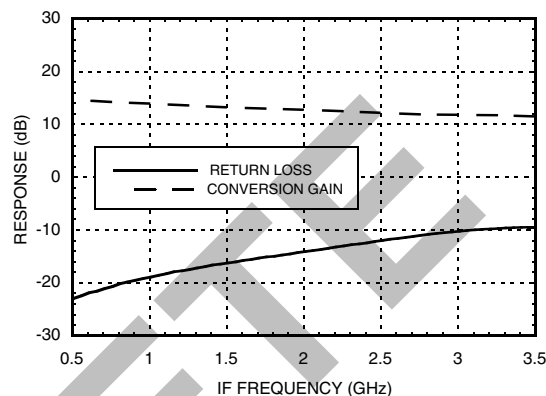
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Quadrature Channel Data Taken Without IF Hybrid

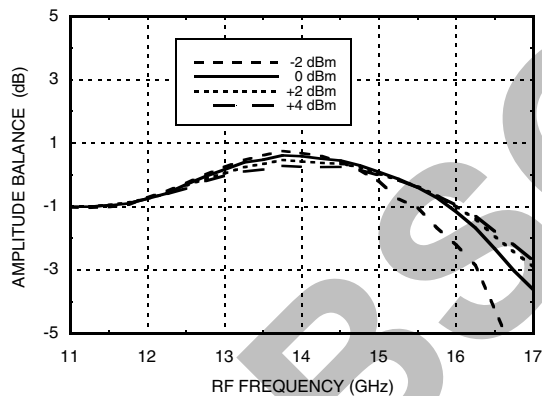
Isolations



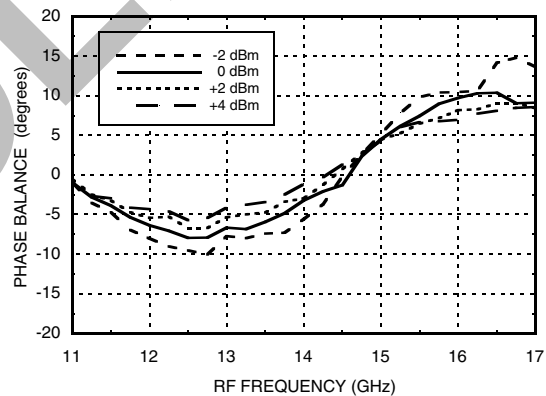
IF Bandwidth*



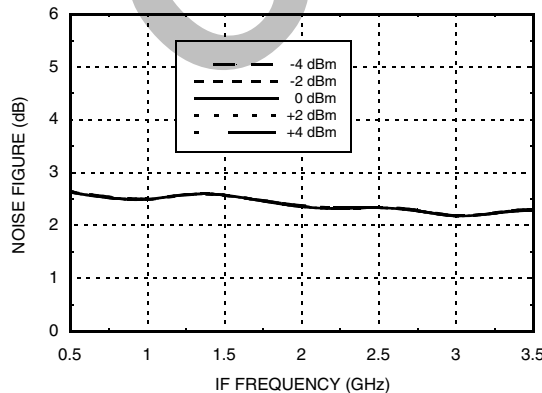
Amplitude Balance vs. LO Drive



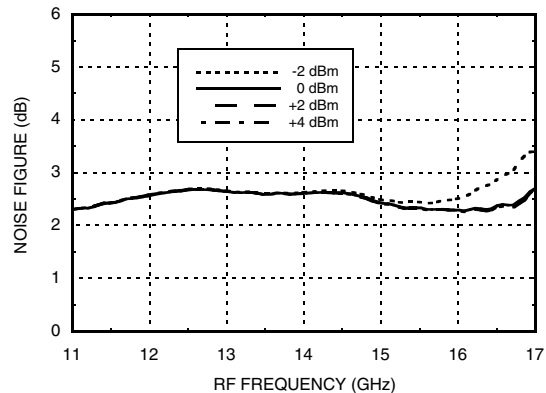
Phase Balance vs. LO Drive



**Noise Figure vs. LO Drive,
LO Frequency = 12 GHz**



**Noise Figure vs. LO Drive,
IF Frequency = 100 MHz**



* Conversion gain data taken with external IF hybrid, LO frequency fixed at 12 GHz and RF varied

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**GaAs MMIC I/Q DOWNCONVERTER
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MxN Spurious Outputs

mRF	nLO				
	0	1	2	3	4
0	xx	43	40	54	xx
1	22	xx	42	56	77
2	74	67	xx	74	98
3	99	97	73	xx	90
4	xx	104	120	102	xx

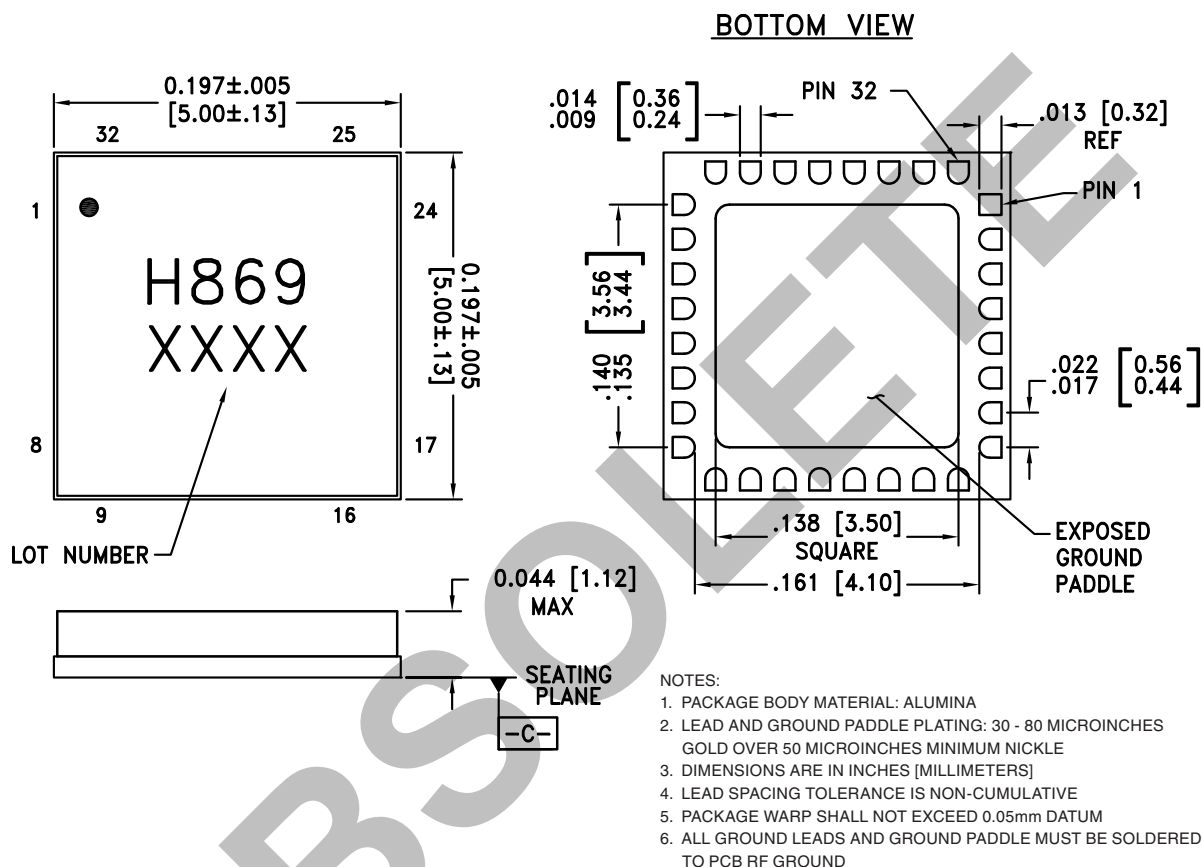
RF = 13.6 GHz @ -20 dBm
 LO = 13.5 GHz @ +2 dBm
 Data taken without IF hybrid
 All values in dBc below IF power level.

Absolute Maximum Ratings

RF	+5 dBm
LO Drive	+20 dBm
VD1, VD2	4.0V
VD3	5.5V
Channel Temperature	150 °C
Continuous P _{diss} (T=85°C) (derate 9.56 mW/°C above 85°C)	0.65 W
Thermal Resistance (R _{TH}) (channel to package bottom)	71 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C



**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**

GaAs MMIC I/Q DOWNCONVERTER
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Outline Drawing**Package Information**

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC869LC5	Alumina, White	Gold over Nickel	MSL3 ^[1]	H869 XXXX


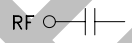
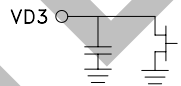
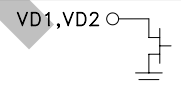
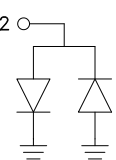
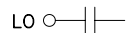
[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX

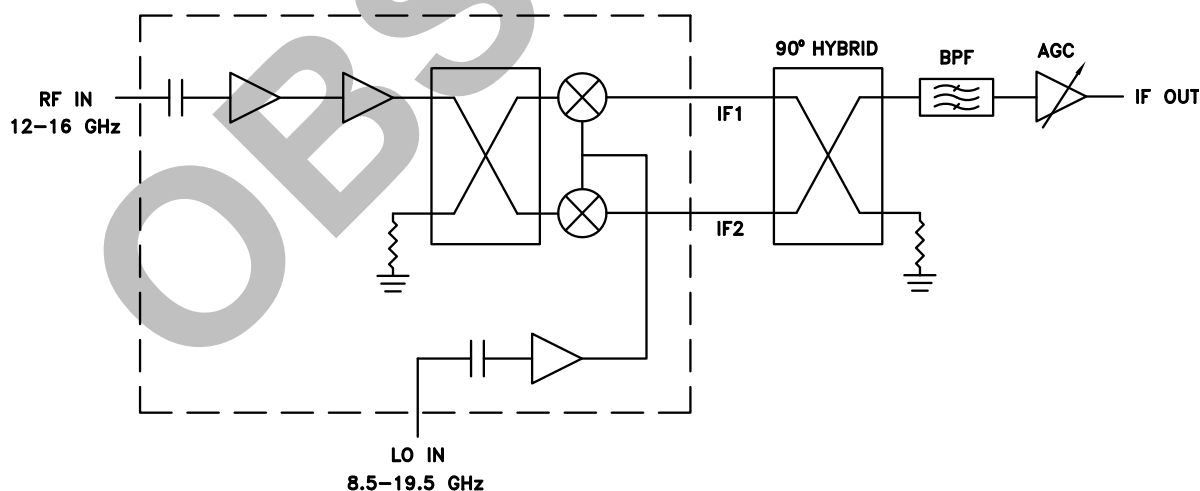


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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 5, 7 - 9, 13 - 16, 22 - 27, 30 - 32	N/C	The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	
2, 4, 10, 12, 17, 19, 21	GND	These pins and ground paddle must be connected to RF/DC ground.	
3	RF	This pin is AC coupled and matched to 50 Ohms.	
6	VD3	Power supply for LO amplifier.	
28, 29	VD2, VD1	Power supply for RF LNA.	
18	IF2	This pin is DC coupled for applications not requiring operation to DC. This port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary frequency range. For operation to DC, this pin must not sink / source more than 3 mA of current or part non-function and possible failure will result.	
20	IF1		
11	LO	This pin is AC coupled and matched to 50 Ohms.	

Typical Application



Note: LSB and USB is determined by GND on Hybrid

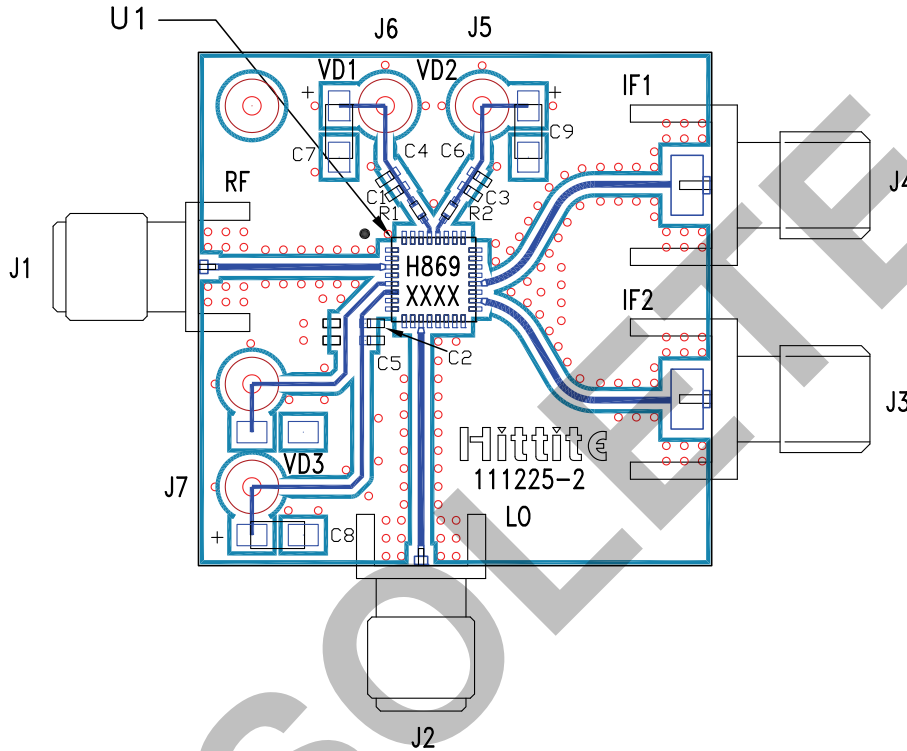
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Evaluation PCB



List of Materials for Evaluation PCB 111227 ^[1]

Item	Description
J1, J2	PCB Mount SMA RF Connector, SRI
J3, J4	PCB Mount SMA Connector, Johnson
J5, J6, J7	DC Pin
C1, C2, C3	Capacitor 0402, Pkg. 100pF
C4, C5, C6	Capacitor 0402, Pkg. 1000pF
C7, C8, C9	Capacitor, Case A, 2.2uF
R1, R2	Resistor, 0402 Pkg. 0 Ohm
U1	HMC869LC5
PCB ^[2]	111225 Evaluation Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350 or Arlon 25FR

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

OBSOLETE