



10 dB GaAs MMIC 1-BIT DIGITAL POSITIVE CONTROL ATTENUATOR, DC - 5 GHz

Insertion Loss



Normalized Attenuation











Relative Phase vs. Frequency



Input IP3 vs. Frequency



For price, delivery, and to place orders, please contact Hittite Microwave Corporation: 20 Alpha Road, Chelmsford, MA 01824 Phone: 978-250-3343 Fax: 978-250-3373 Order On-line at www.hittite.com



MICROWAVE CORPORATION V01.1005



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Bias Voltage & Current

Vdd = +5.0 Vdc ± 10%		
Vdd (VDC)	ldd (Typ.) (mA)	
+4.5	1.3	
+5.0	1.5	
+5.5	1.7	

Control Voltage

State	Bias Condition
Low	0 to +0.8V @ -5 uA Typ.
High	+2.0 to + 5.0 Vdc @ 40 uA Typ.
Note: Vdd = +5V	

Truth Table

V1 10 dB	Attenuation State RF1 - RF2
High	Reference I.L.
Low	10 dB

Downloaded from Arrow.com.

5



RoHS√

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Absolute Maximum Ratings

RF Input Power (DC - 5 GHz)	+27 dBm (T = +85 °C)
Control Voltage Range (V1)	-1V to Vdd +1V
Bias Voltage (Vdd)	+7.0 Vdc
Channel Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 14.7 mW/°C above 85 °C)	0.96 W
Thermal Resistance	68 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1B



ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS**

Outline Drawing



PLANE

-C-

- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.

6. ALL GROUND LEADS AND GROUND PADDLE MUST BE

SOLDERED TO PCB RF GROUND.

7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

.003[0.08] C

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC541LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	541 XXXX
HMC541LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>541</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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5





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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	Vdd	Supply Voltage.	
2, 11	RF1, RF2	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required. Select value based on lowest frequency of operation.	RF1 RF2
3, 4, 6, 7, 9, 10, 12, 13, 15, 16	N/C	These pins should be connected to PCB RF ground to maximize performance.	
5, 8	ACG1, ACG2	External capacitor to ground is required. Select value for lowest frequency of operation. Place capacitor as close to pins as possible.	
14	V1	See truth table and control voltage table.	500 142K (V1)
	GND	Package bottom has an exposed metal paddle that must be connected to RF Ground.	



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5 - 180



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Evaluation PCB



List of Material for Evaluation PCB 111317 [1]

Item	Description	
J1 - J2	PCB Mount SMA Connector	
J3 - J5	DC Connector	
C1 - C3	1000 pF Capacitor, 0402 Pkg.	
C4 - C5	330 pF Capacitor, 0402 Pkg.	
U1	HMC541LP3 / HMC541LP3E Digital Attenuator	
PCB [2]	111290 Evaluation PCB	

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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