Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FQB25N33	FQB25N33	D2-PAK	330mm	24mm	800

Electrical Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
B_{VDSS}	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	330	--	--	V
$\Delta B_{VDSS}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	I_D = 250μA, Referenced to 25°C	--	0.34	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 330V,V_{GS} = 0V	--	--	1	μA
		V_{DS} = 264V,T_C =125°C	--	--	10	
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30V, V_{DS} = 0V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Forward	V_{GS} = -30V, V_{DS} = 0V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	3.0	--	5.0	V
$R_{DS(on)}$	Drain to Source On Resistance	V_{GS} = 10V, I_D = 12.5A,	--	0.18	0.23	Ω
g_{FS}	Forward Transonductance	V_{DS} = 50V, I_D = 12.5A, (Note 4)	--	1	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	--	1510	2010	pF
C_{oss}	Output Capacitance		--	290	385	pF
C_{rss}	Reverse Transfer Capacitance		--	40	60	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 165V, I_D = 25A, R_{GS} = 25Ω (Note 4, 5)	--	20	35	ns
t_r	Turn-On Rise Time		--	100	160	ns
$t_{d(off)}$	Turn-Off Delay Time		--	90	145	ns
t_f	Turn-Off Fall Time		--	70	110	ns
$Q_{g(TOT)}$	Total Gate Charge	V_{DS} = 297V, I_D = 25A, V_{GS} = 15V, (Note 4, 5)	--	58	75	nC
Q_{gs}	Gate to Source Gate Charge		--	11.2	--	nC
Q_{gd}	Gate to Drain Charge		--	21	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	25	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	100	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0, I_S = 25A	--	--	1.5	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0, I_S = 25A, dI_F/dt = 100A/μs (Note 4)	--	275	--	ns
Q_{rr}	Reverse Recovery Charge		--	3.6	--	μC

Notes:
1: Repetitive Rating : Pluse width Limited by maximum junction temperature
2: L = 1.79mH, I_{AS} = 25A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3: I_{SD} ≤ 25A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4: Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%
5: Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

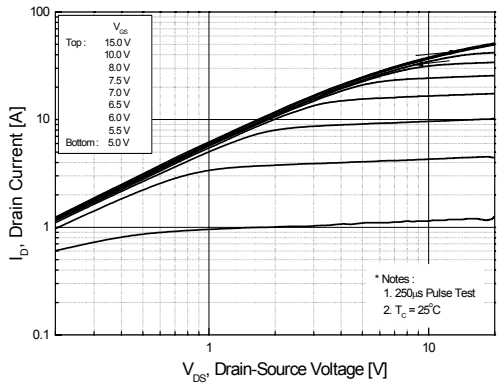


Figure 2. Transfer Characteristics

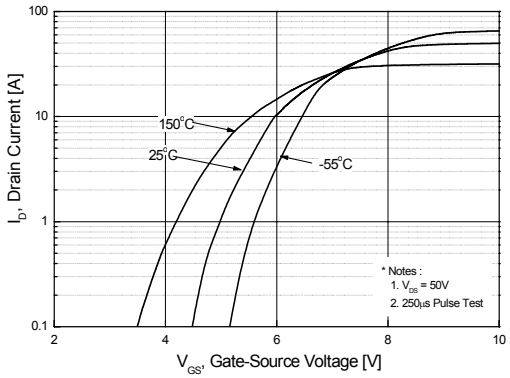


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

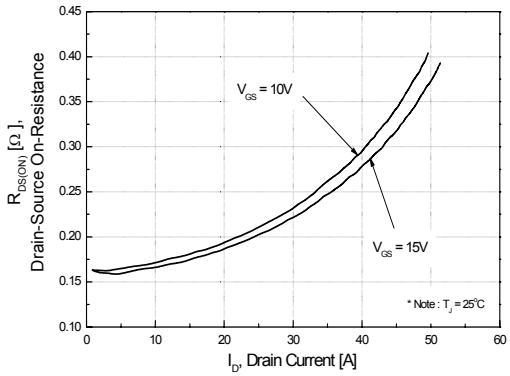


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

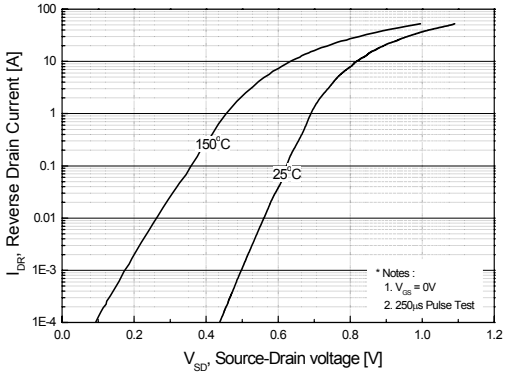


Figure 5. Capacitance Characteristics

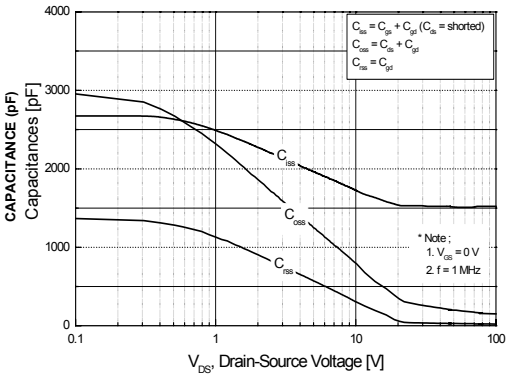
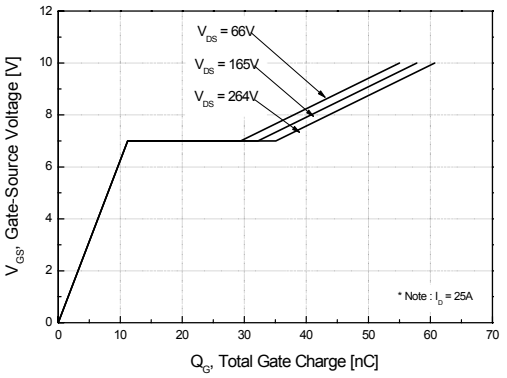


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

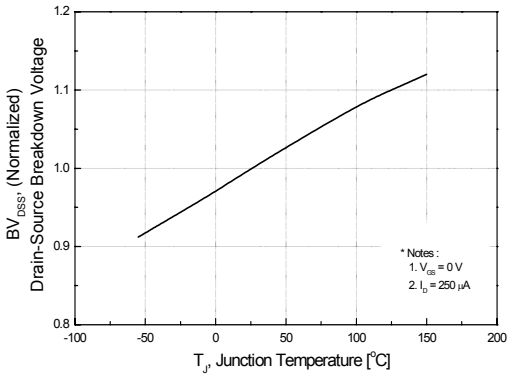


Figure 8. On-Resistance Variation vs. Temperature

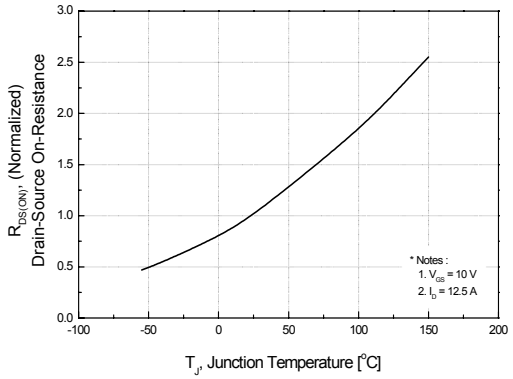


Figure 9. Maximum Safe Operating Area

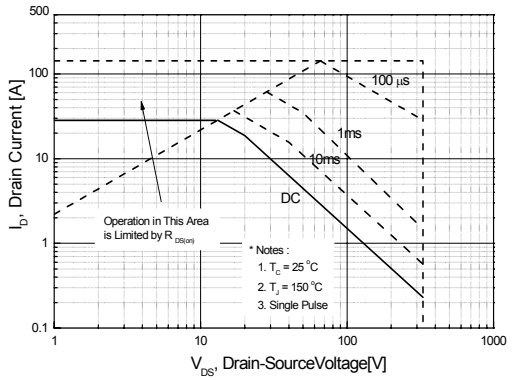


Figure 10. Maximum Drain Current vs. Case Temperature

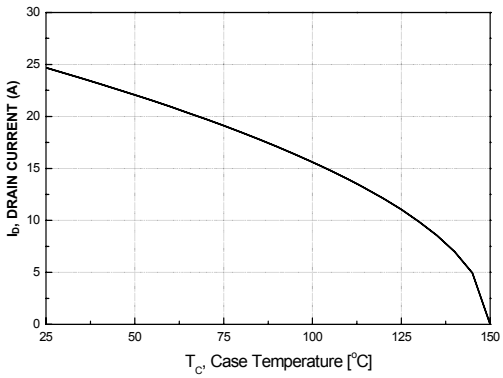
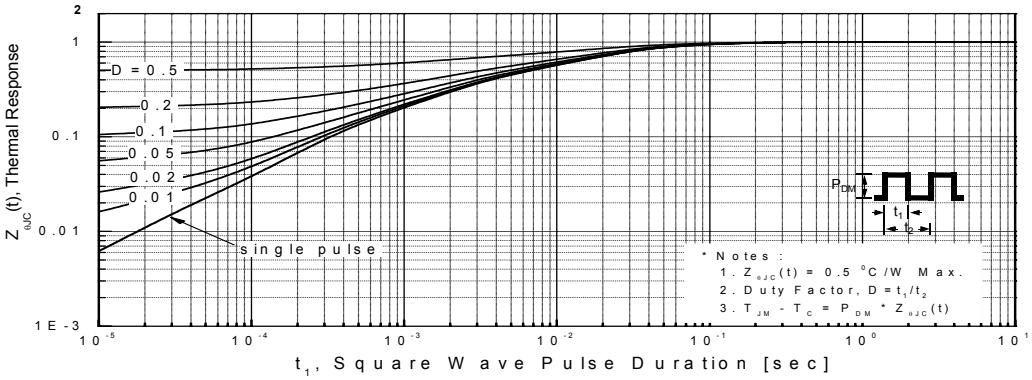
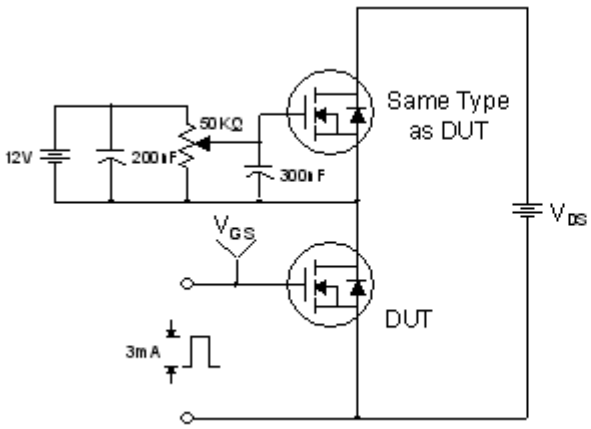
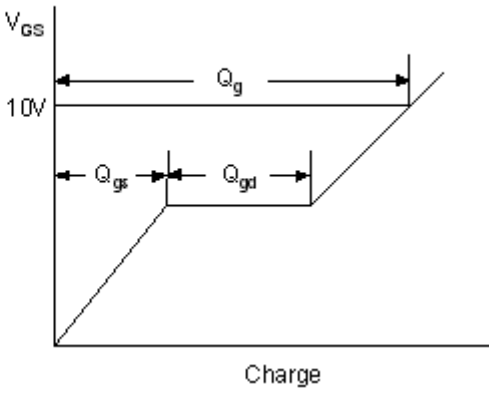


Figure 11. Transient Thermal Response Curve

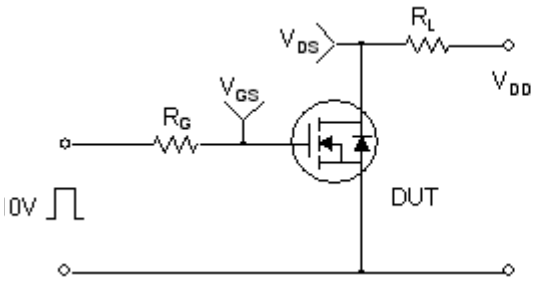


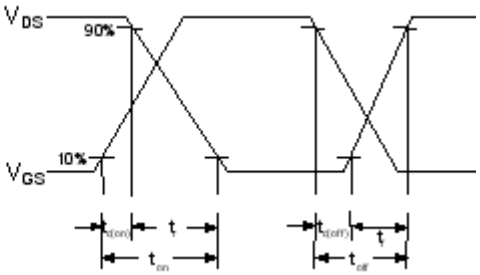
Gate Charge Test Circuit & Waveform



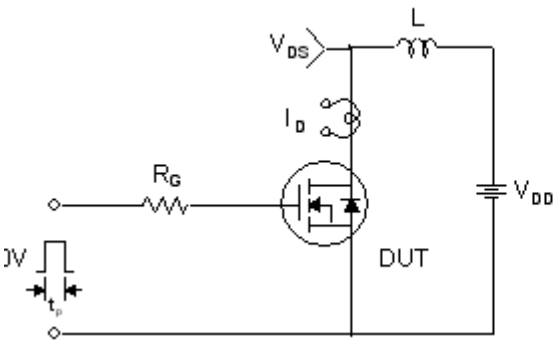


Resistive Switching Test Circuit & Waveforms

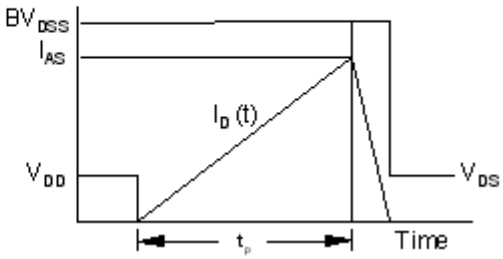




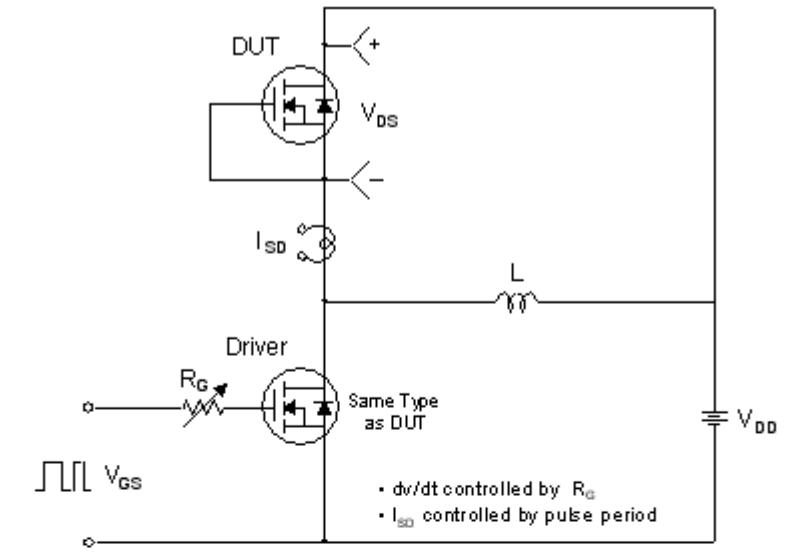
Unclamped Inductive Switching Test Circuit & Waveforms

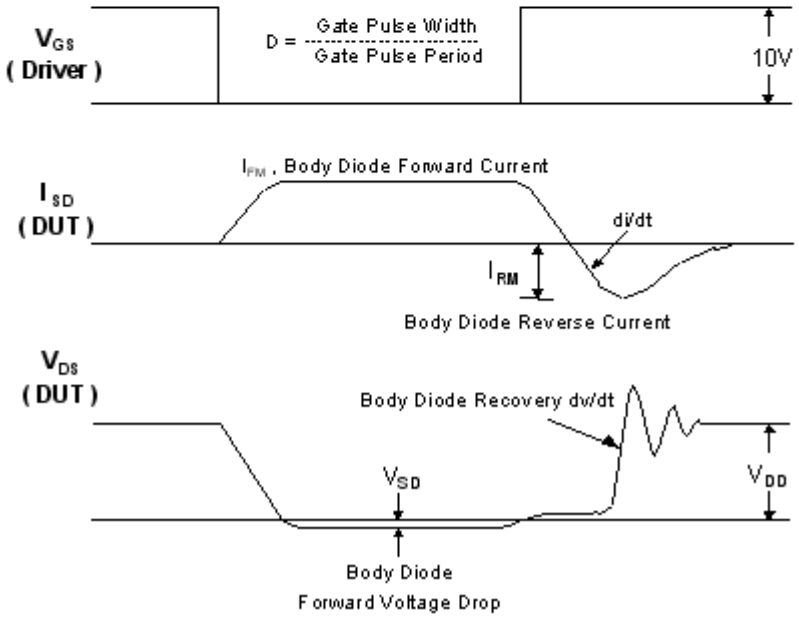


$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



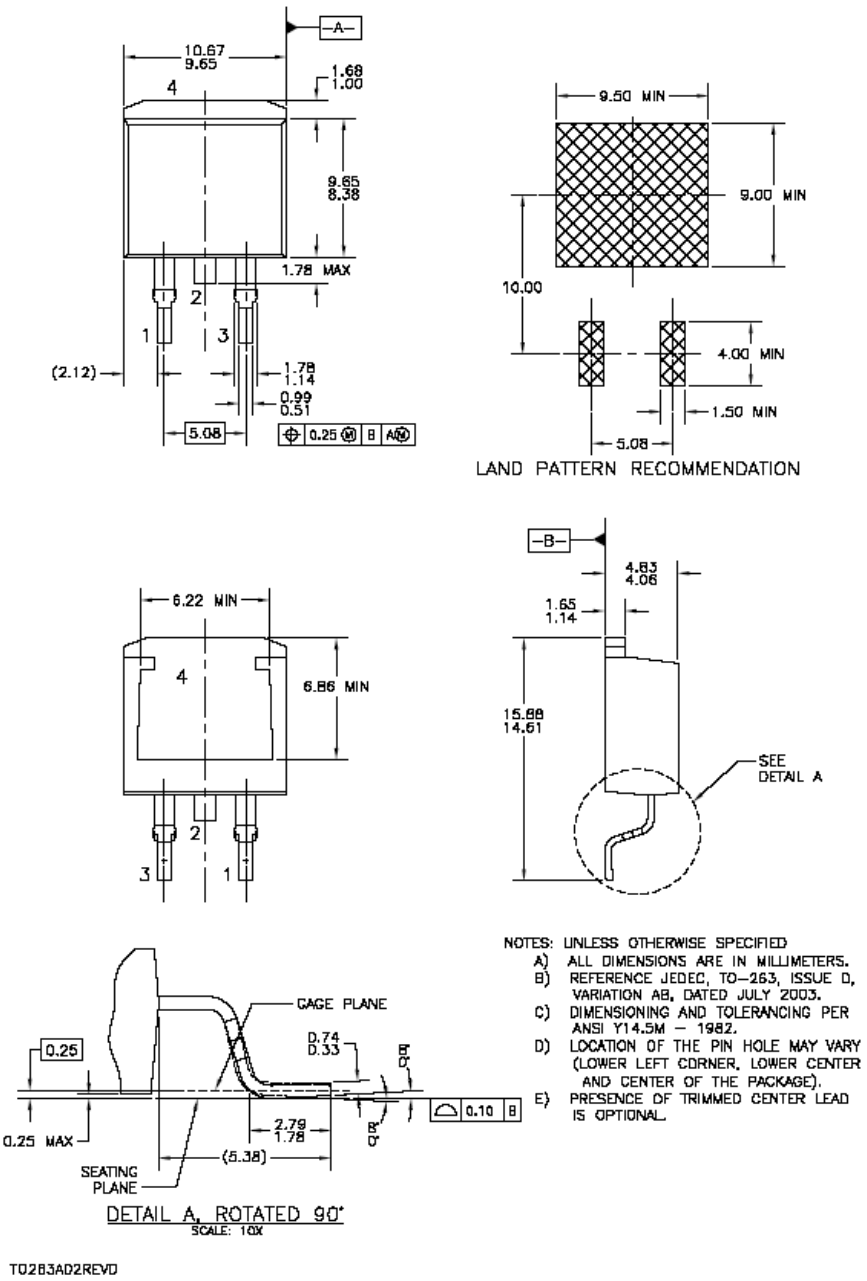
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Package Demensions

D2-PAK



Dimensions in Millimeters

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