

# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	ES1A	ES1B	ES1C	ES1D	ES1G	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage (Note 6)	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	50	100	150	200	400	٧
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	105	140	280	V
Average Rectified Output Current @ T <sub>T</sub> = +110°C	lo			1.0			Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>			30			Α

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Terminal (Note 5)	$R_{\theta JT}$	25	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

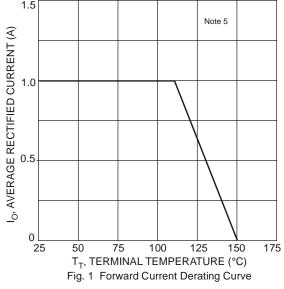
### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

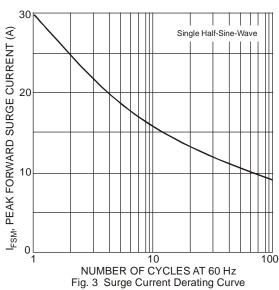
Characteristic		Symbol	ES1A	ES1B	ES1C	ES1D	ES1G	Unit
Minimum Reverse Breakdown Voltage (Note 6)	$I_R = 5\mu A$	$V_{(BR)R}$	50	100	150	200	400	V
Maximum Forward Voltage Drop	I <sub>F</sub> = 0.6A I <sub>F</sub> = 1.0A	V <sub>FM</sub>		_	90 92		 1.25	٧
Peak Reverse Current at Rated DC Blocking Voltage (Note 6)	$T_A = +25^{\circ}C$ $T_A = +125^{\circ}C$	I <sub>RM</sub>			5.0 200			μA
Maximum Reverse Recovery Time (Note 7)		t <sub>RR</sub>			25			ns
Typical Total Capacitance (Note 8)		Ст			20			pF

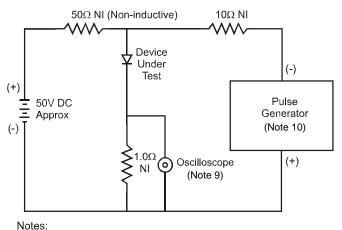
Notes:

- 5. Unit mounted on PC board with 5.0 mm² (0.013 mm thick) copper pad as heat sink.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Measured with  $I_F$  = 0.5A,  $I_R$  = 1.0A,  $I_{RR}$  = 0.25A. See figure 5. 8. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

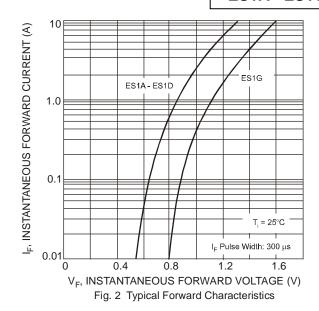


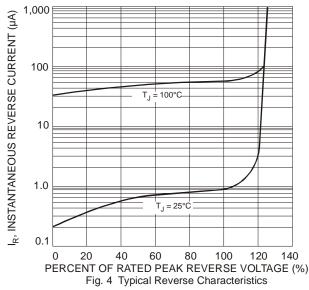


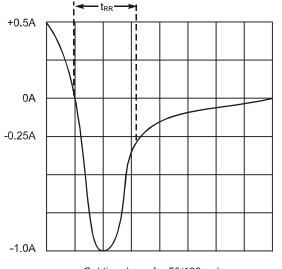




9. Rise Time = 7.0ns max. Input Impedance =  $1.0M\Omega$ , 22pF. 10.Rise Time = 10ns max. Input Impedance =  $50\Omega$ .







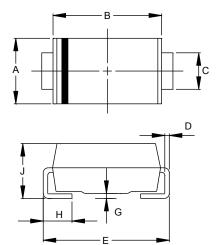
Set time base for 50/100 ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit



### **Package Outline Dimensions**

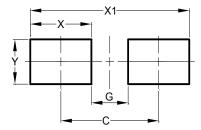
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SMA					
Dim	Min	Max			
Α	2.29	2.92			
В	4.00	4.60			
C	1.27	1.63			
D	0.15	0.31			
Е	4.80	5.59			
G	0.05	0.20			
I	0.76	1.52			
7	1.96	2.40			
All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value			
Dimensions	(in mm)			
С	4.00			
G	1.50			
Х	2.50			
X1	6.50			
γ	1.70			



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