

# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			$V_{GSS}$	±8	V
Continuous Drain Current	Steady State	$T_A = +25$ °C (Note 6) $T_A = +85$ °C (Note 6) $T_A = +25$ °C (Note 5)	I <sub>D</sub>	-0.67 -0.48 -0.53	А
Pulsed Drain Current (Note 7)			I <sub>DM</sub>	2.5	Α

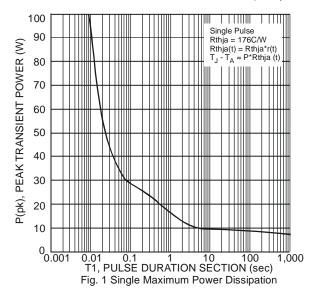
### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Bower Dissinction	(Note 5)	D	0.45	W
Power Dissipation	(Note 6)	PD	0.71	W
Thermal Desistance, Junction to Ambient	(Note 5)	Б	275	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	177	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout

6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

7. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.



R(t), TRANSIENT THERMAL RESISITANCE r(t) @ D=0.9 r(t) @ D=0.1 r(t) @ D=0.05 r(t) @ D=0.01 0.01 r(t) @ D=0.01 r(t) @ D=0.005  $R_{\theta JA}(t) = r(t)^*R_{\theta JA}$  $R_{\theta JA} = 176C/W$ 001 r(t) @ D=Single Pulse 0.000001 0.000001 0.00001 Duty Cycle, D = t1/t2 0.0001 100 1,000 Fig. 2 Transient Thermal Resistance



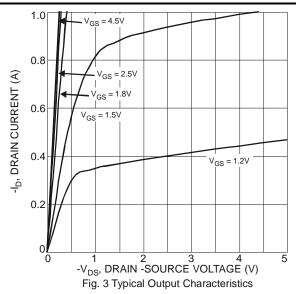
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

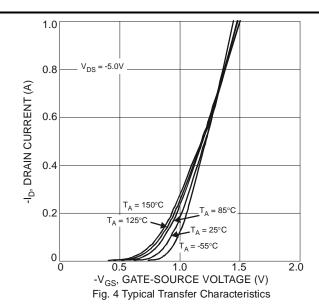
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	1		-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±3	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.5	_	-1.1	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
				1	Ω	$V_{GS} = -4.5V, I_D = -400mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>			1.5		$V_{GS} = -2.5V, I_D = -200mA$	
				2		$V_{GS} = -1.8V, I_D = -100mA$	
Forward Transfer Admittance	Y <sub>FS</sub>	50	_		mS	$V_{DS} = -3V, I_{D} = -300 \text{mA}$	
Diode Forward Voltage	$V_{SD}$	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -300mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>ISS</sub>	_	76	150	pF	\/ 45\/ \/ O\/	
Output Capacitance	Coss	_	9	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>RSS</sub>	_	6.43	_	pF		
Gate Resistance	R <sub>G</sub>	-	167	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	$Q_G$	-	0.9	_	nC	$V_{GS} = -4.5V$ , $V_{DS} = -15V$ , $I_{D} = -1A$	
Total Gate Charge	$Q_{G}$	l	1.5	_	nC	V <sub>GS</sub> = -8V, V <sub>DS</sub> = -15V, -1 <sub>D</sub> = -1A	
Gate-Source Charge	Q <sub>GS</sub>	_	0.1	_	nC		
Gate-Drain Charge	$Q_{GD}$	_	0.2	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.0	_	ns		
Turn-On Rise Time	t <sub>R</sub>	1	5.9	_	ns	$V_{DD} = -10V, R_L = 10\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	35.7	_	ns	$V_{GS} = -4.5V$ , $R_G = 6\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	16.7	_	ns		

Notes:

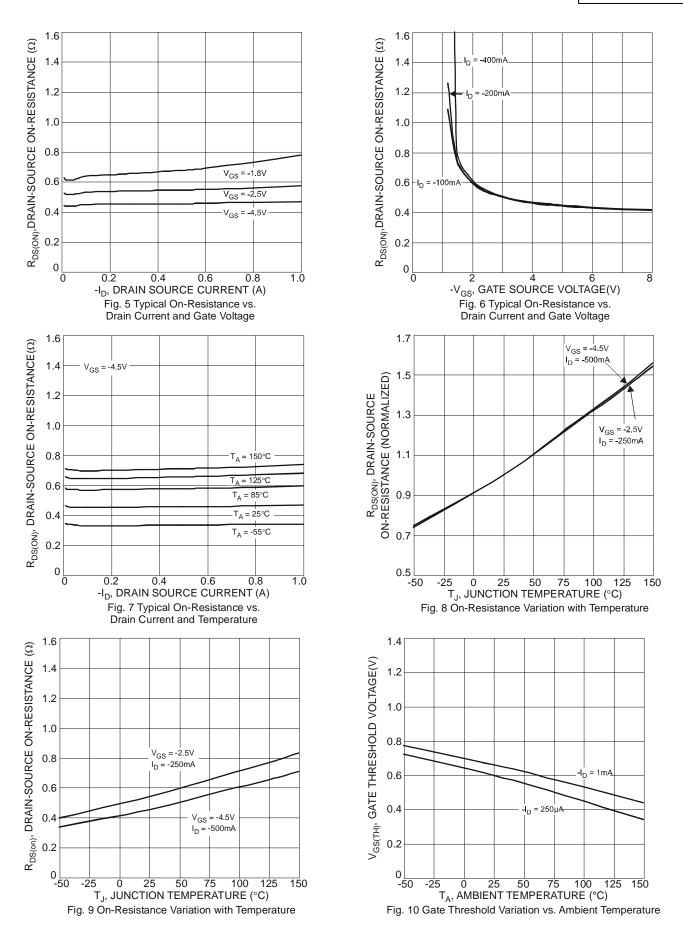
- 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.

# **Typical Electrical Characteristics**

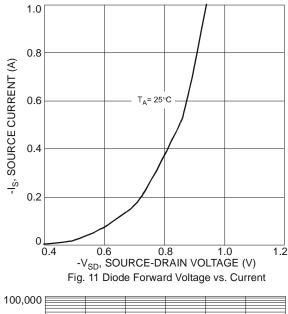












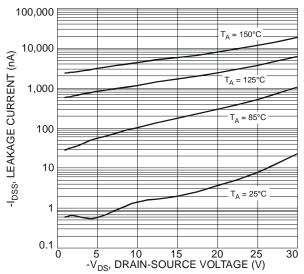
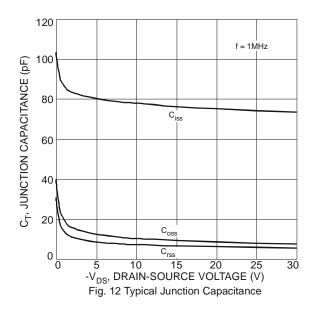
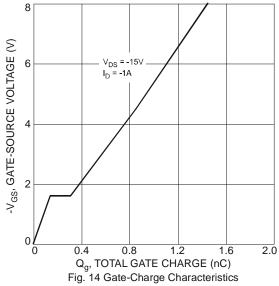


Fig. 13 Typical Drain-Source Leakage Current vs. Voltage



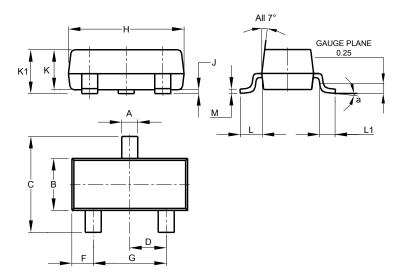




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

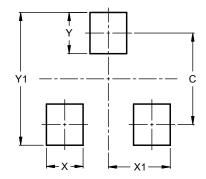


SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
а	0°	8°	_		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



#### IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

www.diodes.com