

### **Marking Information**

#### Site 1



P9 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2011		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Y		Н		J	К	L	М	N	0	Р	R
	-				-					-	-	
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



P9 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Data	Code	Ko
Date	Code	nev

Year	2011		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	1		0	1	2	3	4	5	6	7	8	9
Week 1-26			27-52				53					
Code	A-Z			a-z			Z					
Internal Code	Sun	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	١	V	Х		Y		Z



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage		VDSS	-25	V	
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lo	-6.7 -5.3	А
Continuous Drain Current (Note 5) $V_{GS} = -4.5V$	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lo	-8.3 -6.6	А
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lo	-5.4 -4.3	А
Continuous Drain Current (Note 5) $V_{GS} = -1.8V$	ID	-6.6 -5.2	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%		Idм	-60	А	
Continuous Source-Drain Diode Current			ls	-2.0	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	D-	0.8	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	1.2	vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	160	°C/W	
memai Resistance, sunction to Ambient (Note 5)	t<5s	R <sub>0</sub> JA	104		
Total Dower Discipation (Note 6)	$T_A = +25^{\circ}C$	D-	2.0	W	
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	2.9		
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	P	63	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	RθJA	42	°C/vv	
Thermal Resistance, Junction to Case (Note 6)	Rejc	10.8	°C/W		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C		

### Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			- 76			
Drain-Source Breakdown Voltage	BVDSS	-25	_		V	Vgs = 0V, Id = -250µA
Zero Gate Voltage Drain Current	IDSS		_	-1	μA	$V_{DS} = -25V, V_{GS} = 0V$
Gate-Source Leakage	lgss		_	±10	μA	V <sub>GS</sub> = ±8.0V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.4	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			20	27		VGS = -4.5V, ID = -6.4A
Static Drain-Source On-Resistance	Descer	_	24	34	mΩ	$V_{GS} = -2.5V, I_D = -4.8A$
Static Drain-Source On-Resistance	RDS(ON)	_	28	40	11122	V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.5A
		_	33	70		Vgs = -1.5V, ID = -1.5A
Forward Transfer Admittance	Y <sub>fs</sub>	_	16	_	S	$V_{DS} = -5V, I_{D} = -4A$
Diode Forward Voltage	Vsd	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		2530		pF	
Output Capacitance	Coss		203	_	pF	Vps = -15V, Vgs = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		177	_	pF	
Gate Resistance	Rg		9.1	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg		28.2	_	nC	
Total Gate Charge (V <sub>GS</sub> = -8V	Qg		48.7	_	nC	
Gate-Source Charge	Qgs	_	3.2	_	nC	$V_{DS} = -15V, I_D = -4.0A$
Gate-Drain Charge	Qgd	_	5.0		nC	7
Turn-On Delay Time	tD(ON)	_	15.1		ns	
Turn-On Rise Time	tR		23.5		ns	$V_{DD} = -15V, V_{GS} = -4.5V, R_G = 1\Omega,$
Turn-Off Delay Time	tD(OFF)		137.6		ns	I <sub>D</sub> = -4.0A
Turn-Off Fall Time	tF	_	80.5	_	ns	

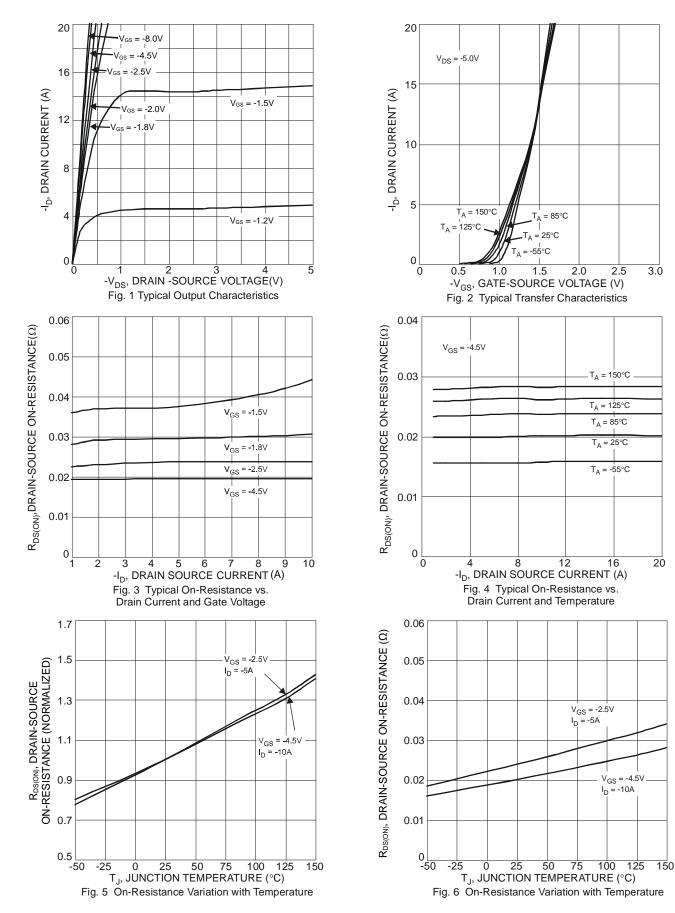
Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

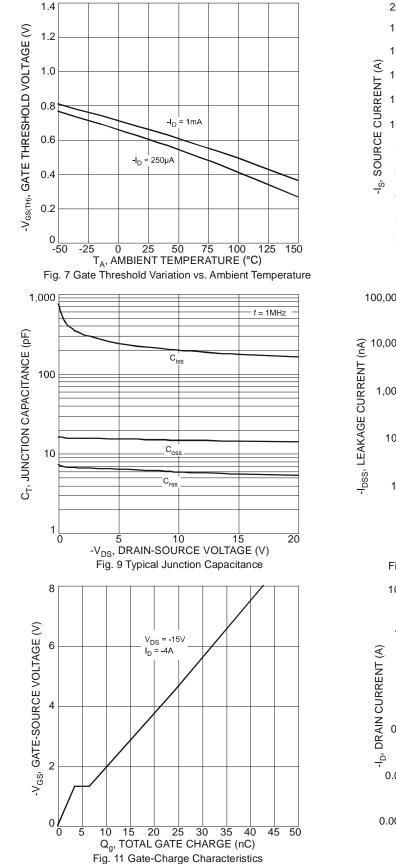


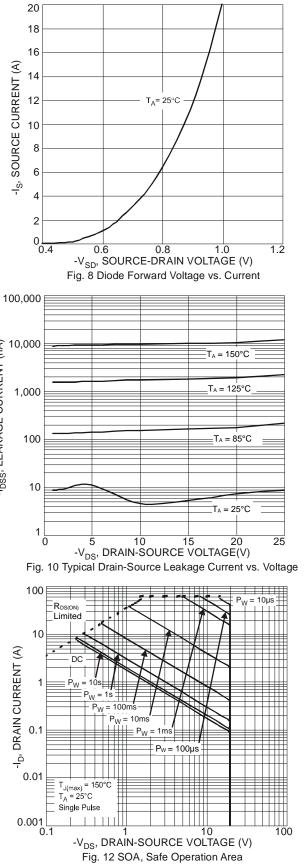


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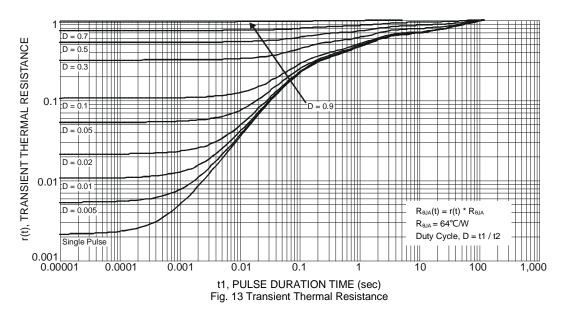


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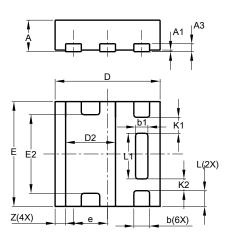






### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

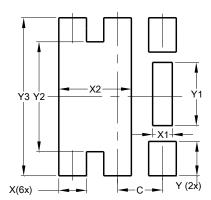


U-DFN2020-6	(Type E)
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	U-DFN2020-6							
	Туре Е							
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
b1	0.185	0.285	0.235					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
E	1.95	2.05	2.00					
E2	1.40	1.60	1.50					
е	-	-	0.65					
L	0.25	0.35	0.30					
L1	0.82	0.92	0.87					
K1	-	-	0.305					
K2	_	_	0.225					
Z	_	-	0.20					
All	Dimen	isions i	in mm					

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### U-DFN2020-6 (Type E)

Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300



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