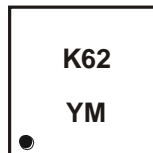


## Marking Information

Site 1:



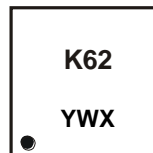
K62 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: I = 2021)  
 M = Month (ex: 9 = September)

Date Code Key

Year	2011	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Y	...	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Site 2:



K62 = Product Type Marking Code  
 YWX = Date Code Marking  
 Y = Year (ex: 1 = 2021)  
 W = Week (ex: a = Week 27; z Represents Week 52 and 53)  
 X = Internal Code (ex: U = Monday)

Date Code Key

Year	2011	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	1	...	1	2	3	4	5	6	7	8	9	0

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	540 430	mA
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	630 500	mA
Continuous Drain Current (Note 6) V <sub>GS</sub> = 5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	430 340	mA
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	510 410	mA
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	1.0	A
Maximum Body Diode Forward Current (Note 6)			I <sub>S</sub>	540	mA

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P <sub>D</sub>	0.43	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>θJA</sub>	260	°C/W
	t < 10s		182	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	0.89	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>θJA</sub>	140	°C/W
	t < 10s		98	°C/W
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	112	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 10μA
Zero Gate Voltage Drain Current @ T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	100	nA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	10	μA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	1.6	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	—	2	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 500mA
		—	—	3		V <sub>GS</sub> = 5V, I <sub>D</sub> = 50mA
Forward Transfer Admittance	Y <sub>fs</sub>	—	130	—	mS	V <sub>DS</sub> = 3V, I <sub>D</sub> = 30mA
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 300mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	30.2	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	4.4	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	2.8	—	pF	
Gate Resistance	R <sub>g</sub>	—	131	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	0.39	—	nC	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1A
Total Gate Charge (V <sub>GS</sub> = 10.0V)	Q <sub>g</sub>	—	0.87	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	0.14	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	0.09	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	3.95	—	ns	V <sub>DS</sub> = 30V, I <sub>D</sub> = 200mA V <sub>GS</sub> = 10V, R <sub>G</sub> = 25Ω
Turn-On Rise Time	t <sub>r</sub>	—	3.81	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	16.0	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	9.04	—	ns	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

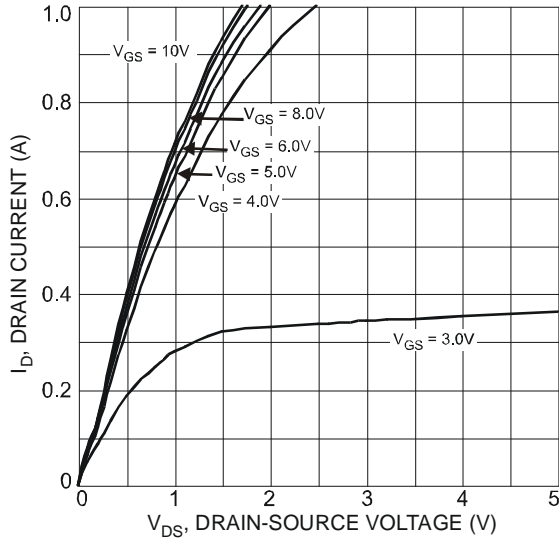


Fig. 1 Typical Output Characteristic

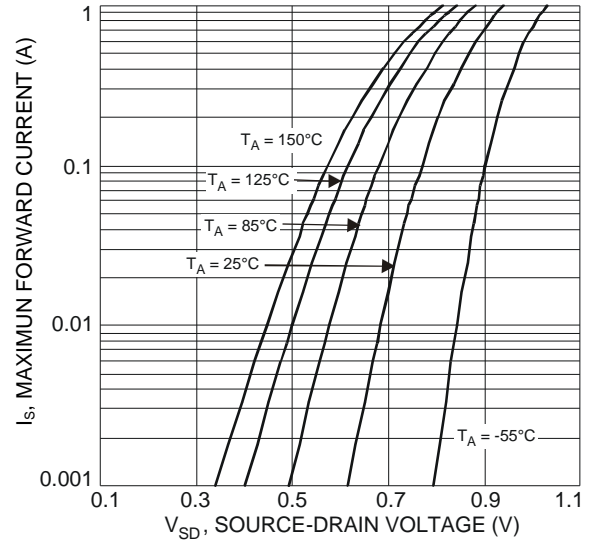


Fig. 2 Maximum Forward Current vs. Source-Drain Voltage

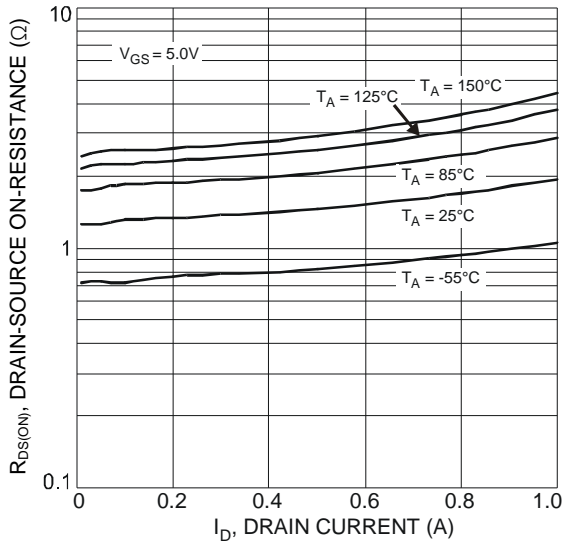


Fig. 3 Typical On-Resistance vs. Drain Current and Temperature

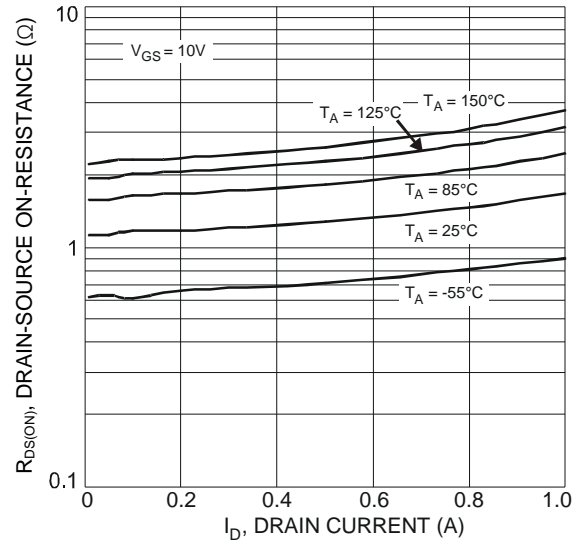


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

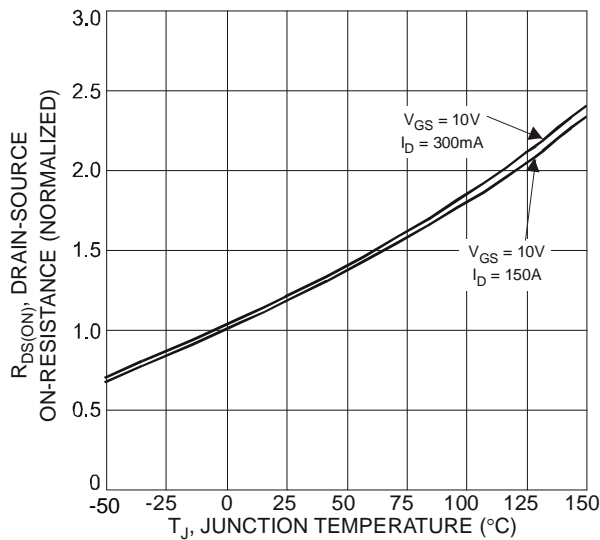


Fig. 5 On-Resistance Variation with Temperature

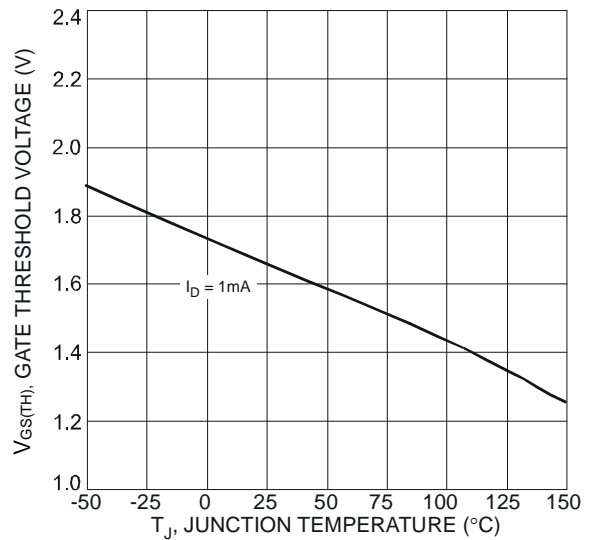


Fig. 6 Gate Threshold Variation vs. Junction Temperature

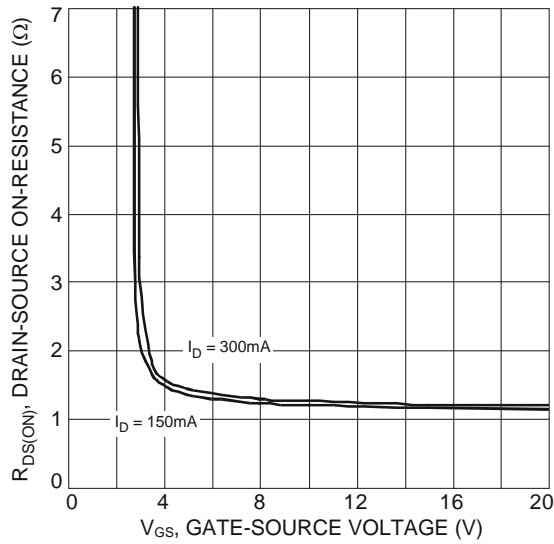


Fig. 7 Typical Transfer Characteristics

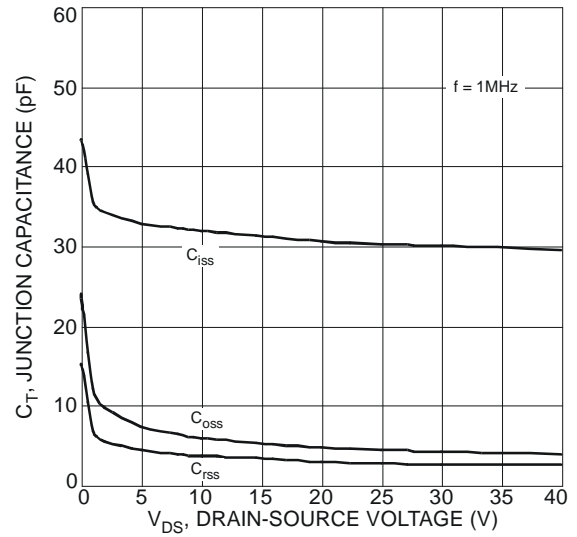


Fig. 8 Typical Junction Capacitance

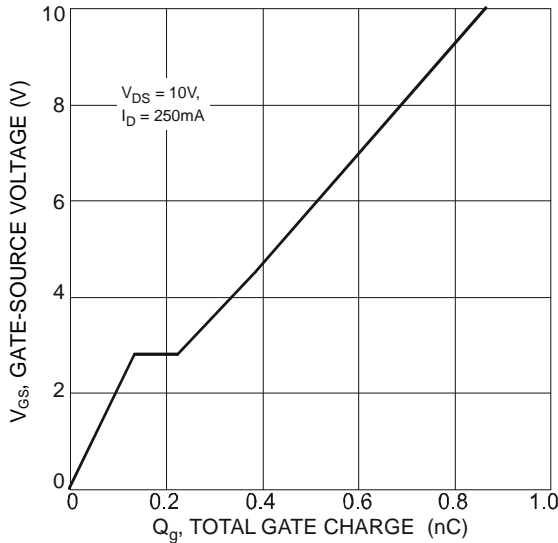


Fig. 9 Gate Charge

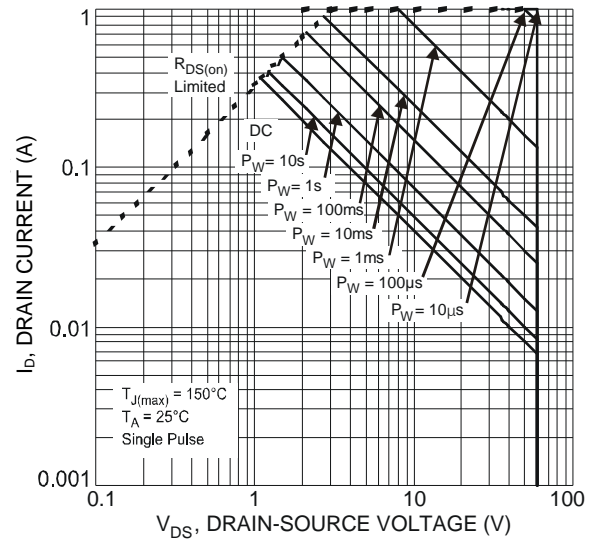


Fig. 10 SOA, Safe Operation Area

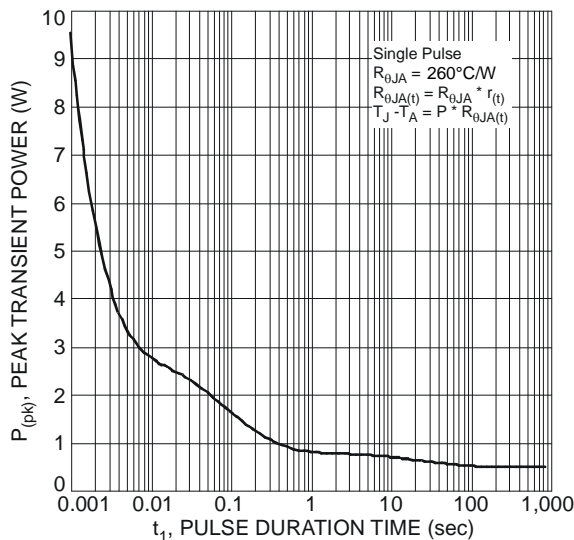
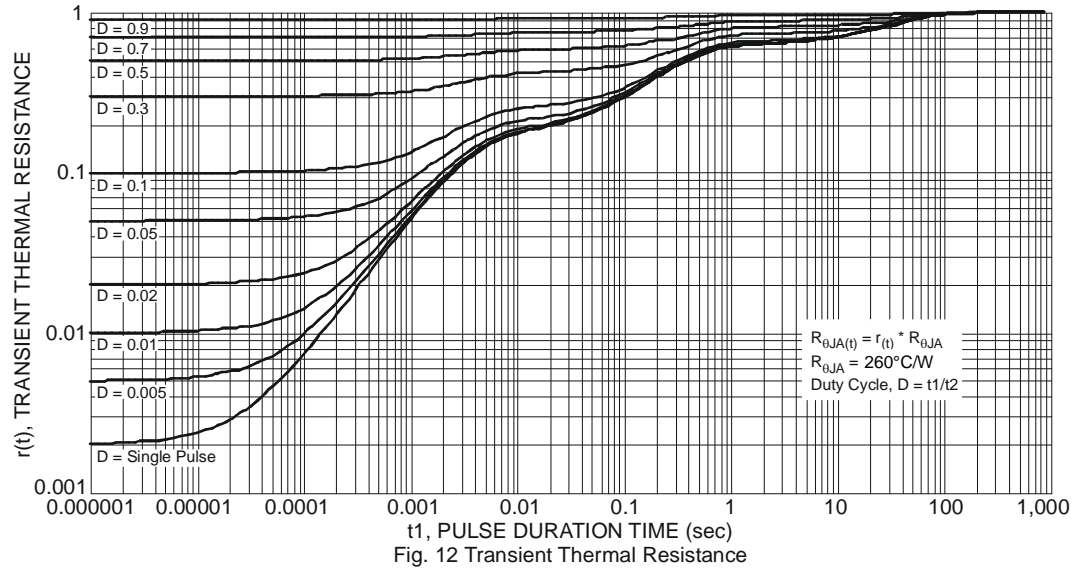


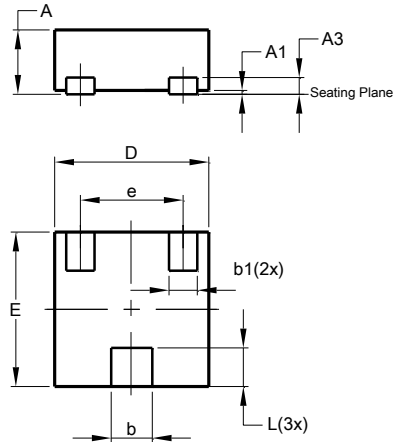
Fig. 11 Single Pulse Maximum Power Dissipation



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1212-3

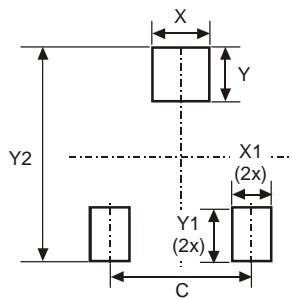


X1-DFN1212-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.02
A3	-	-	0.13
b	0.27	0.37	0.32
b1	0.17	0.27	0.22
D	1.15	1.25	1.20
E	1.15	1.25	1.20
e	-	-	0.80
L	0.25	0.35	0.30
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1212-3



Dimensions	Value (in mm)
C	0.80
X	0.42
X1	0.32
Y	0.50
Y1	0.50
Y2	1.50

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