

Fig. 3 Gate Threshold Voltage vs. Ambient Temperature

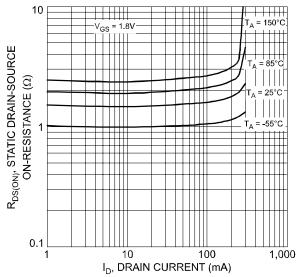
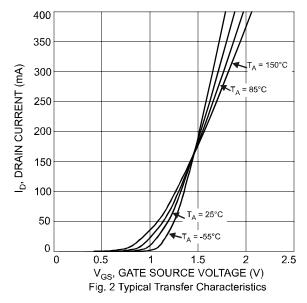


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



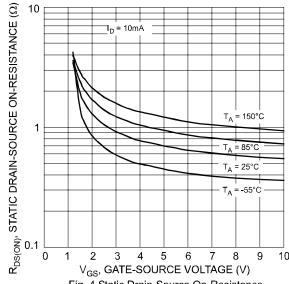


Fig. 4 Static Drain-Source On-Resistance vs. Gate-Source Voltage

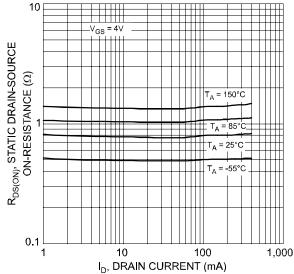


Fig. 6 Static Drain-Source On-Resistance vs. Drain Current



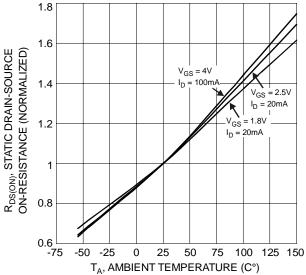


Fig. 7 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

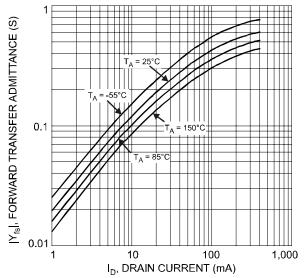


Fig. 9 Forward Transfer Admittance vs. Drain Current

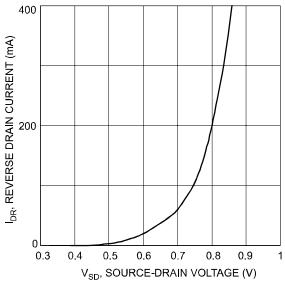
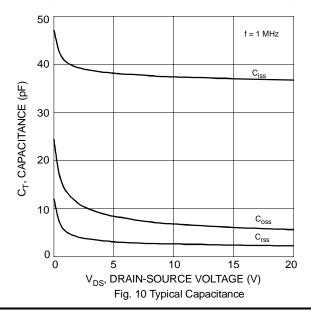


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

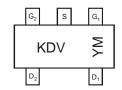


#### Ordering Information (Note 5)

Part Number	Case	Packaging		
DMN32D2LDF-7	SOT-353	3000/Tape & Reel		

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### Marking Information (Note 6)



KDV = Product Type Marking Code (See Note 6) YM = Date Code Marking

YM = Date Code Marking Y = Year ex: U = 2007

M = Month ex: 9 = September

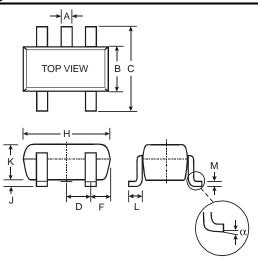
Notes: 6. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).

Date Code Key

Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	Ų	J	\	/	V	V		(	`	<b>′</b>	2	7
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

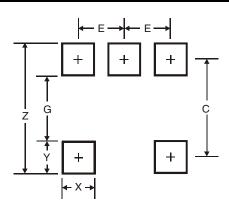


# **Package Outline Dimensions**



SOT-353				
Dim	Min	Max		
Α	0.10	0.30		
В	1.15	1.35		
С	2.00	2.20		
D	0.65 Nominal			
F	0.30	0.40		
Н	1.80	2.20		
J	_	0.10		
K	0.90	1.00		
L	0.25	0.40		
М	0.10	0.25		
α	0°	8°		
All Dimensions in mm				

## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Υ	0.6
С	1.9
E	0.65

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