

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-160	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-150	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector Current	lc	-200	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation Total Device	(Notes 6 & 7)	PD	300	mW
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>0JA</sub>	417	°C/W
Operating and Storage Temperature Range		TJ, T <sub>STG</sub>	-55 to +150	С°

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-160			V	$I_{\rm C} = -100 \mu A, I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-150			V	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5			V	$I_{E} = -10\mu A$ , $I_{C} = 0$
Collector-Base Cutoff Current	I <sub>CBO</sub>	_	_	-50	nA µA	V <sub>CB</sub> = -120V, I <sub>E</sub> = 0 V <sub>CB</sub> = -120V, I <sub>E</sub> = 0, T <sub>A</sub> = +100°C
Emitter-Base Cutoff Current	I <sub>EBO</sub>		_	-50	nA	$V_{EB} = -3V$ , $I_B = 0$
ON CHARACTERISTICS (Note 8)						
DC Current Gain (Note 9)	h <sub>FE</sub>	50 60 50	_	240	_	$I_{C} = -1mA, V_{CE} = -5V$ $I_{C} = -10mA, V_{CE} = -5V$ $I_{C} = -50mA, V_{CE} = -5V$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	-0.2 -0.5	V	$I_{C} = -10mA$ , $I_{B} = -1mA$ $I_{C} = -50mA$ , $I_{B} = -5mA$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	_	-1	V	$I_{C} = -10mA$ , $I_{B} = -1mA$ $I_{C} = -50mA$ , $I_{B} = -5mA$
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f <sub>T</sub>	100		300	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -10mA, f = 100MHz
Output Capacitance	C <sub>OBO</sub>	_		6	pF	$V_{CB} = -10V$ , f = 1.0MHz, I <sub>E</sub> = 0mA
Small Signal Current Gain	h <sub>fe</sub>	40		260		$V_{CE} = -10V, I_C = -1mA, f = 1.0kHz$
Noise Figure	NF	_	_	8	dB	$\label{eq:Vce} \begin{array}{l} V_{CE} = -5V, \ I_C = -200 \mu A, \ R_S = 10 \Omega, \\ f = 1.0 k Hz \end{array}$

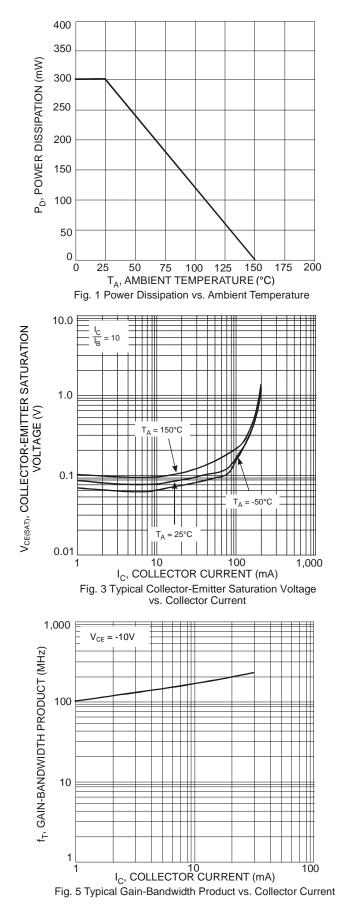
For a device mounted on minimum recommended pad layout with 1oz copper that is on a single-sided 1.6mm FR-4 PCB; the device is measured under still air conditions whilst operating in a steady-state.
Maximum combined dissipation.
Sheat duration while to be a finite or the strength of the strengt

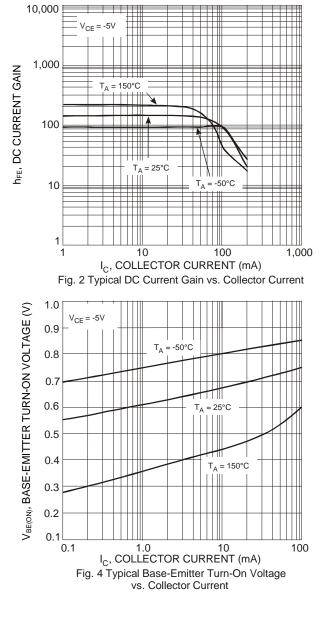
8. Short duration pulse test used to minimize self-heating effect.

9. The DC Current Gain, h<sub>FE</sub>, (matched at I<sub>C</sub> = -10mA and V<sub>CE</sub> = -5V) Collector Emitter Saturation Voltage, V<sub>CE(SAT)</sub>, and Base Emitter Saturation Voltage,  $V_{\text{BE(SAT)}}$  are matched with typical matched tolerances of 1% and maximum of 2%.







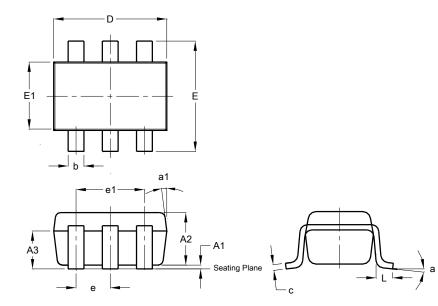




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26 (SC74R)

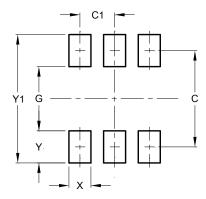


	SOT26 (SC74R)					
Dim	Min	Max	Тур			
A1	0.013	0.10	0.05			
A2	1.00	1.30	1.10			
A3	0.70	0.80	0.75			
b	0.35	0.50	0.38			
С	0.10	0.20	0.15			
D	2.90	3.10	3.00			
е	-	-	0.95			
e1	-	-	1.90			
Е	2.70	3.00	2.80			
E1	1.50	1.70	1.60			
L	0.35	0.55	0.40			
а	-	-	8°			
a1	-	-	7°			
All Dimensions in mm						

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26 (SC74R)



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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