

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +85$ °C	I _D	8 6.4	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t≤10 sec	$T_A = +25$ °C $T_A = +85$ °C	I _D	10.7 8.6	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t ≤ 10 sec	$T_A = +25$ °C $T_A = +85$ °C	I _D	9.6 7.7	Α
Pulsed Drain Current (Note 7)			I _{DM}	45	Α
Avalanche Current (Notes 7 & 8)			I _{AR}	13	Α
Repetitive Avalanche Energy (Notes 7 & 8) L = 0.3mH			E _{AR}	25.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.54	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{0JA}	81	°C/W
Power Dissipation (Note 6)	P _D	2.8	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	Reja	45	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

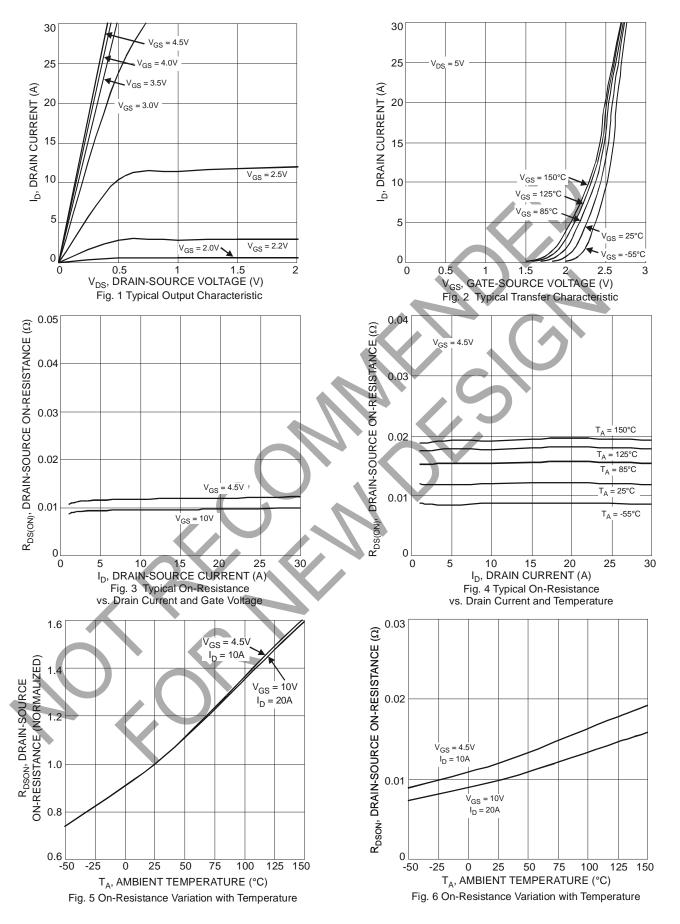
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	Symbol	IVIIII	тур	IVIAX	Ollit	Test Condition	
Drain-Source Breakdown Voltage	BV _{DSS}	30			V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	150	μA	$V_{DS} = 30V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	1 - 1	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)	1.033					1.63 =1.21, 1.63 =1	
Gate Threshold Voltage	V _{GS(th)}	1.0	_	2.3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Ctatic Dusin Course On Desistants		_	11	15	15	$V_{GS} = 10V, I_D = 10.7A$	
Static Drain-Source On-Resistance	RDS (ON)	_	16.5	18.5	mΩ	V _{GS} = 4.5V, I _D = 9.6A	
Forward Transfer Admittance	Y _{fs}	_	20	_	S	$V_{DS} = 5V, I_{D} = 10.7A$	
Diode Forward Voltage	V _{SD}	_	0.36	0.5	V	$V_{GS} = 0V$, $I_S = 1A$	
Maximum Body-Diode + Schottky Continuous Current	Is	_	_	5	Α	_	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	_	1849	_	pF	V _{DS} =15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	158	_	pF		
Reverse Transfer Capacitance	Crss	_	123	_	pF	1 = 1.0MHZ	
Gate Resistance	Rq	0.54	2.0	4.0	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Qq	_	18.5	_	nC		
Total Gate Charge V _{GS} = 10V	Q_g	_	43	_	nC	$V_{DS} = 15V, V_{GS} = 10V,$	
Gate-Source Charge	Q _{qs}	_	4.7	_	nC	$I_D = 9.6A$	
Gate-Drain Charge	Q _{qd}	_	4.0	_	nC		
Turn-On Delay Time	t _{D(on)}	_	6.62	_	ns		
Turn-On Rise Time	t _r	_	8.73	_	ns	$V_{GS} = 10V, V_{DS} = 15V,$	
Turn-Off Delay Time	t _{D(off)}	_	36.41	_	ns	$R_G = 3\Omega, R_L = 15\Omega, I_D = 1A$	
Turn-Off Fall Time	t _f	_	4.69	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout. The value in any given application depends on the user's specific board design. 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 1 oz. Copper, single sided, device is measured at t ≤ 10 sec.
- 7. Repetitive rating, pulse width limited by junction temperature.
- 8. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = +25°C 9. Short duration pulse test used to minimize self-heating effect.
- 10. Guaranteed by design. Not subject to production testing.







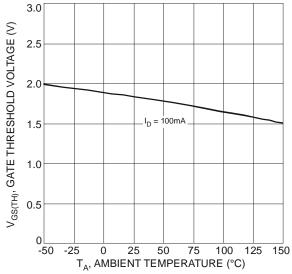
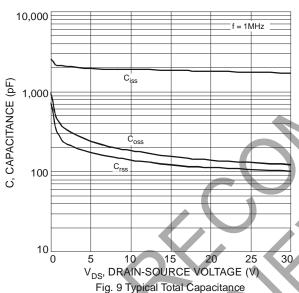
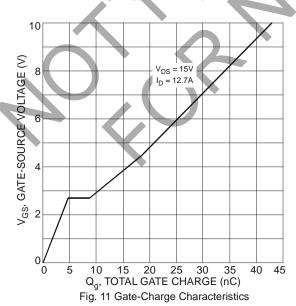


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





30 25 QUANDO 15 15 0 0 0 0.2 0.4 0.6 0.8 1.0 1.2 V_{SD}, SOURCE-DRAIN VOLTAGE (V) Fig. 8 Diode Forward Voltage vs. Current

10,000

T_A = 125°C

T_A = 85°C

T_A = 85°C

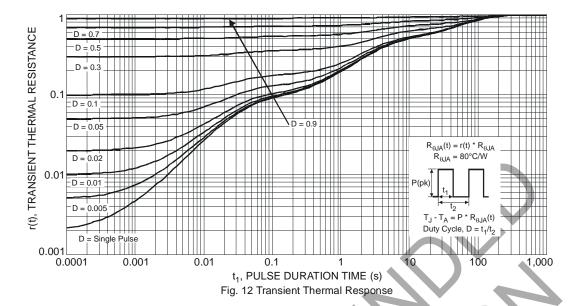
T_A = 85°C

V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Fig. 10 Typical Leakage Current

vs. Drain-Source Voltage

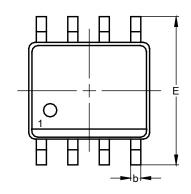


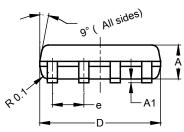


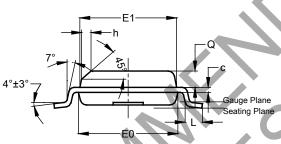


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version







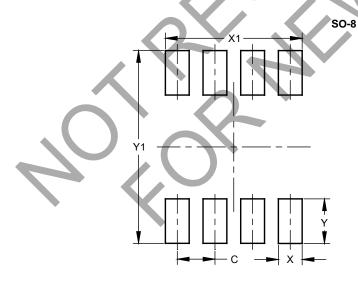
SO-8

SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е	_	_	1.27		
h	1	_	0.35		
L	0.62	0.82	0.72		
Q	0.60	0.70	0.65		

All Dimensions in mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50



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