

Electrical Characteristics - MOSFET (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 1)								
Drain-Source Breakdown Voltage	BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I _{DSS} —		_	10	μΑ	$V_{DS} = 50V, V_{GS} = 0V$		
Gate-Body Leakage	I _{GSS}	_	_	1.0 5.0	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 1)								
Gate Threshold Voltage	$V_{GS(th)}$	0.7	0.8	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$		
Static Drain-Source On-Resistance	D		3.1	4	Ω	$V_{GS} = 4V, I_D = 100mA$		
Static Drain-Source On-Resistance	R _{DS(ON)}		4	5		$V_{GS} = 2.5V, I_D = 80mA$		
Forward Transconductance	g FS	180	_	_	ms	$V_{DS} = 10V, I_D = 100mA,$ f = 1.0kHz		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C _{iss}		25	_	pF	101/11/101/		
Output Capacitance	Coss	_	5	_	pF pF	$V_{DS} = 10V, V_{GS} = 0V,$ -f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	2.1			T = 1.0IVII IZ		

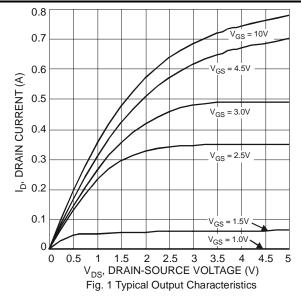
Electrical Characteristics - NPN Transistor (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage (Note 5)		V _{(BR)CBO}	50	_	_	V	$I_C = 10 \mu A, I_B = 0$
Collector-Emitter Breakdown Voltage	(Note 5)	V _{(BR)CEO}	45	_	_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	(Note 5)	$V_{(BR)EBO}$	6	_	_	V	$I_E = 1\mu A, I_C = 0$
DC Current Gain	(Note 5)	h _{FE}	200	290	450	_	$V_{CE} = 5.0V, I_{C} = 2.0mA$
Collector-Emitter Saturation Voltage	(Note 5)	V _{CE(SAT)}	_		100 300	mV	$I_C = 10mA, I_B = 0.5mA$ $I_C = 100mA, I_B = 5.0mA$
Base-Emitter Saturation Voltage	(Note 5)	V _{BE(SAT)}	_	700 900	_	mV	$I_C = 10mA$, $I_B = 0.5mA$ $I_C = 100mA$, $I_B = 5.0mA$
Base-Emitter Voltage	(Note 5)	V_{BE}	580 —	660 —	700 770	mV	$V_{CE} = 5.0V, I_{C} = 2.0mA$ $V_{CE} = 5.0V, I_{C} = 10mA$
Collector Cut-Off Current	(Note 5)	I _{CBO}	_	_	15 5.0	nΑ μΑ	V _{CB} = 30V V _{CB} = 30V, T _A = +150°C
Collector-Emitter Cut-Off Current	(Note 5)	I _{CES}	_	_	-100	nA	V _{CE} = -45V
Gain Bandwidth Product		f _T	100	_	_	MHz	$V_{CE} = 5.0V, I_{C} = 10mA,$ f = 100MHz
Output Capacitance	•	C _{OBO}	_	_	4.5	pF	V _{CB} = 10V, f = 1.0MHz
Noise Figure		NF	_	_	10	dB	$V_{CE} = 5V, R_S = 2.0k\Omega,$ f = 1.0kHz, BW = 200Hz

Note: 5. Short duration pulse test used to minimize self-heating effect.



MOSFET



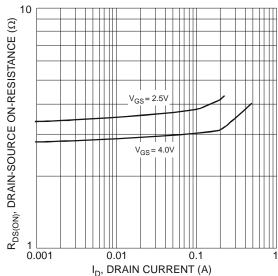
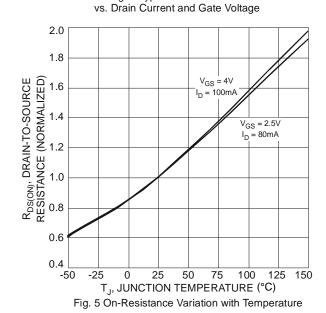


Fig. 3 Typical On-Resistance



0.5

0.4

V_{DS} = 10V

0.4

T_A = 85°C

T_A = 25°C

T_A = 125°C

T_A = 125°C

T_A = 125°C

0.1

0
0
1
2
3
V_{GS}, GATE SOURCE VOLTAGE (V)

Fig. 2 Typical Transfer Characteristics

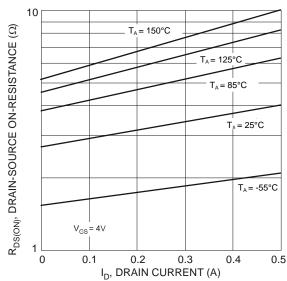
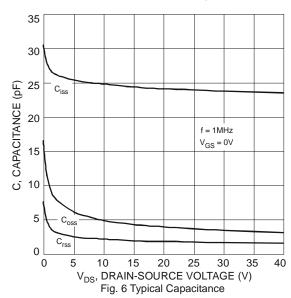


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature





MOSFET (continued)

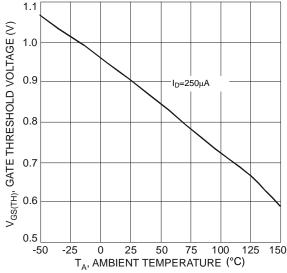


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

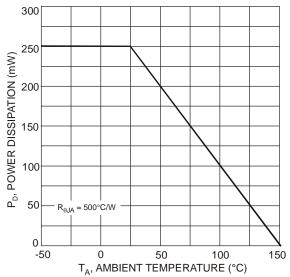
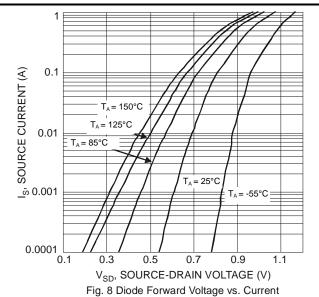


Fig. 9 Derating Curve - Total Package Power Dissipation





NPN Transistor

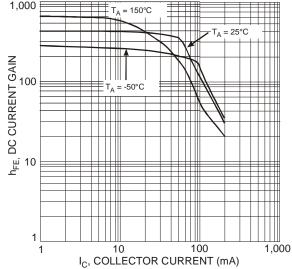


Fig. 10 Typical DC Current Gain vs. Collector Current

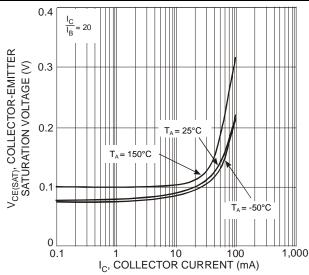


Fig. 11 Typical Collector-Emitter Saturation Voltage vs. Collector Current

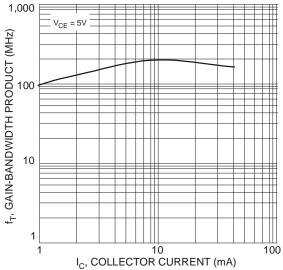


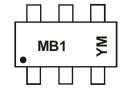
Fig. 12 Typical Gain-Bandwidth Product vs. Collector Current

Ordering Information (Note 6)

Part Number	Case	Packaging
DMB53D0UDW-7	SOT-363	3000/Tape & Reel

Note: 6. For packaging details, go to our website at https://www.diodes.com/assets/Packaging-Support-Docs/Ap02007.pdf.

Marking Information



MB1 = Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

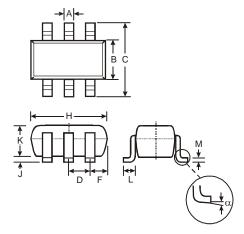
Date Code Key

Year	2008		2009	2010		2011	2012		2013	2014	ļ.	2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	q	0	N	D



Package Outline Dimensions

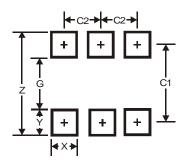
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT-363						
Dim	Min	Max				
Α	0.10	0.30				
В	1.15	1.35				
С	2.00 2.20					
D	0.65 Typ					
F	0.40	0.45				
Н	1.80	2.20				
J	0	0.10				
K	0.90	1.00				
L	0.25 0.40					
M	0.10	0.22				
α	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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