

## Absolute Maximum Ratings @ 25°C

| Parameter                              | Ratings     | Units            |
|--|-------------|------------------|
| Blocking Voltage                       | 600         | V <sub>P</sub>   |
| Reverse Input Voltage                  | 5           | V                |
| Input Control Current                  | 50          | mA               |
| Peak (10ms)                            | 1           | A                |
| Input Power Dissipation <sup>1</sup>   | 150         | mW               |
| Total Package Dissipation <sup>2</sup> | 800         | mW               |
| Isolation Voltage, Input to Output     | 3750        | V <sub>rms</sub> |
| Operational Temperature                | -40 to +85  | °C               |
| Storage Temperature                    | -40 to +125 | °C               |

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

## Electrical Characteristics @ 25°C

| Parameter   | Conditions                            | Symbol            | Min   | Typ  | Max | Units            |
|---|---------------------------------------|-------------------|-------|------|-----|------------------|
| <b>Output Characteristics</b>                         |                                       |                   |       |      |     |                  |
| Operating Voltage Range                               | V <sub>T</sub>                        | -                 | 20    | -    | 260 | V <sub>rms</sub> |
| Load Current, Continuous                              | V <sub>L</sub> =120V <sub>rms</sub>   | I <sub>L</sub>    | 0.005 | -    | 0.5 | A <sub>rms</sub> |
| Off State Leakage Current                             | V = 600V                              | I <sub>LEAK</sub> | -     | -    | 1   | mA               |
| On-State Voltage Drop                                 | I <sub>L</sub> =500 mA <sub>rms</sub> | V <sub>ON</sub>   | -     | -    | 1.4 | V <sub>rms</sub> |
| Critical Rate of Rise <sup>3</sup>                    | -                                     | dV/dt             | 1000  | -    | -   | V/μs             |
| Switching Speeds                                      | I <sub>F</sub> =5 mA                  | t <sub>on</sub>   | -     | -    | 0.5 | cycles           |
| Turn-on   |                                       |                   |       |      |     |                  |
| Turn-off  |                                       | t <sub>off</sub>  | -     | -    | 0.5 |                  |
| Zero-Cross Turn-On Voltage <sup>1</sup>               | 1st half-cycle                        | -                 | -     | 2    | 10  | V                |
|   | Subsequent half-cycle                 | -                 | -     | 1    | -   |                  |
| Operating Frequency                                   | -                                     | -                 | 20    | -    | 500 | Hz               |
| Load Power Factor for Guaranteed Turn-On <sup>2</sup> | -                                     | PF                | 0.25  | -    | -   | -                |
| <b>Input Characteristics</b>                          |                                       |                   |       |      |     |                  |
| Input Control Current to Activate <sup>4</sup>        | -                                     | I <sub>F</sub>    | -     | 0.23 | 5   | mA               |
| Input Drop-out Voltage                                | -                                     | -                 | 0.8   | -    | -   | V                |
| Input Voltage Drop                                    | I <sub>F</sub> =5mA                   | V <sub>F</sub>    | 0.9   | 1.2  | 1.5 | V                |
| Reverse Input Current                                 | V <sub>R</sub> =5V                    | I <sub>R</sub>    | -     | -    | 10  | μA               |
| <b>Common Characteristics</b>                         |                                       |                   |       |      |     |                  |
| Input to Output Capacitance                           | V <sub>IO</sub> =0V, f=1MHz           | C <sub>IO</sub>   | -     | 3    | -   | pF               |

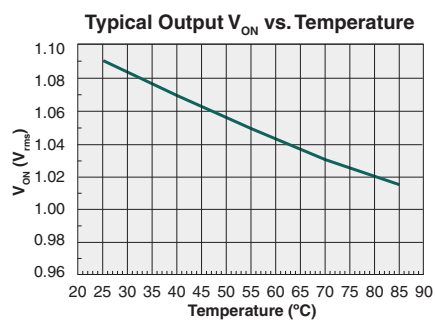
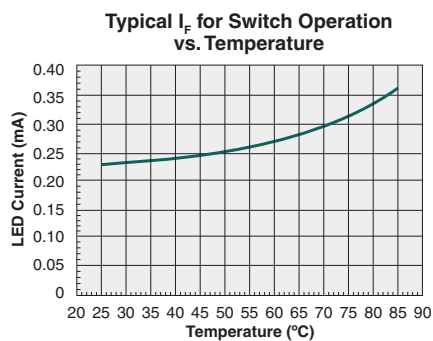
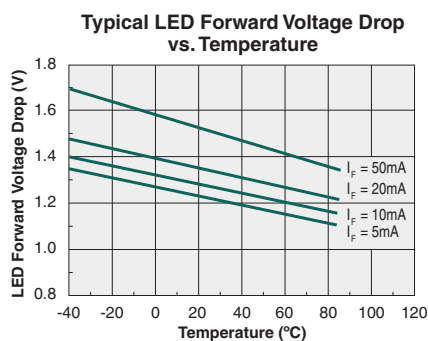
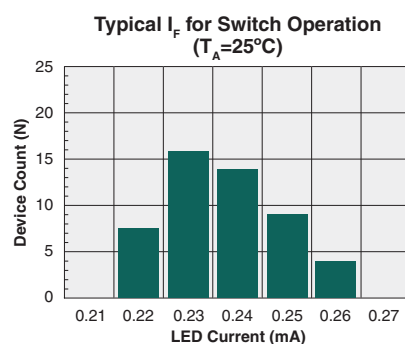
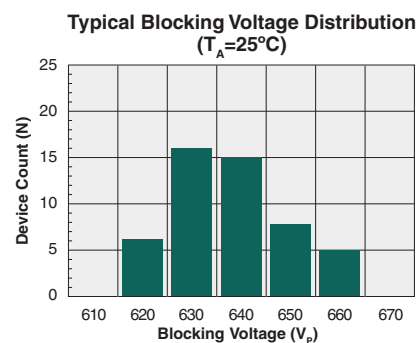
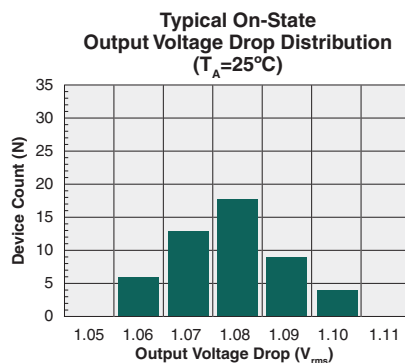
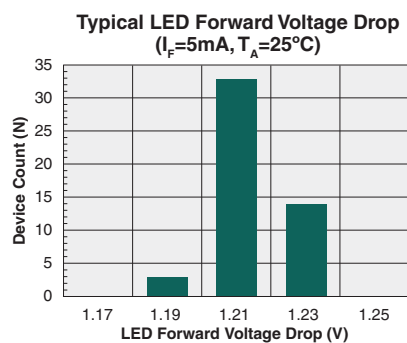
<sup>1</sup> Zero Cross 1st half-cycle @ <100Hz

<sup>2</sup> Snubber circuits may be required at low power factors.

<sup>3</sup> Tested in accordance with EIA/NARM standard RS-443.

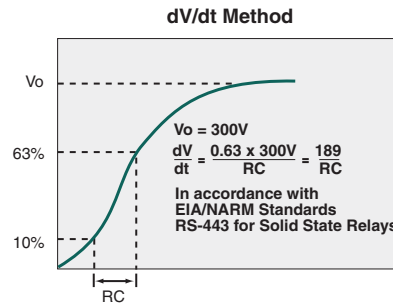
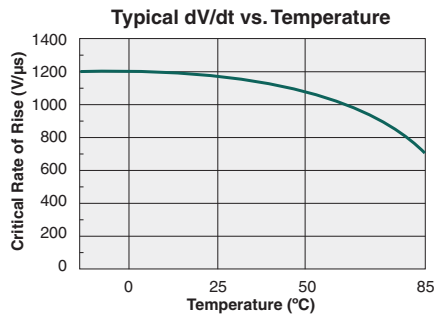
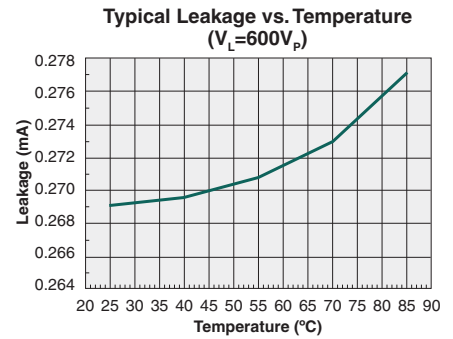
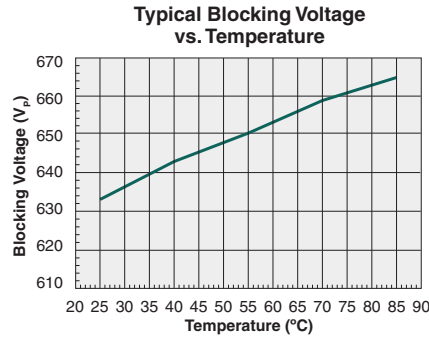
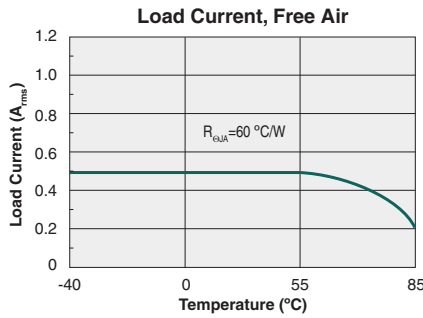
<sup>4</sup> For high noise environments use at least 10mA LED drive current.

# PERFORMANCE DATA\*



\*Unless otherwise noted, data presented in these graphs is typical of device operation at  $25^\circ\text{C}$ .  
For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA\*



\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.  
For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

| Device               | Moisture Sensitivity Level (MSL) Classification |
|----------------------|---|
| CPC1963G / CPC1963GS | MSL 1   |

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Soldering Profile

Provided in the table below is the Classification Temperature ( $T_C$ ) of this product and the maximum dwell time the body temperature of this device may be ( $T_C - 5$ )°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

| Device    | Classification Temperature ( $T_C$ ) | Dwell Time ( $t_p$ ) | Max Reflow Cycles |
|-----------|--------------------------------------|----------------------|-------------------|
| CPC1963G  | 250°C                                | 30 seconds           | 1                 |
| CPC1963GS | 250°C                                | 30 seconds           | 3                 |

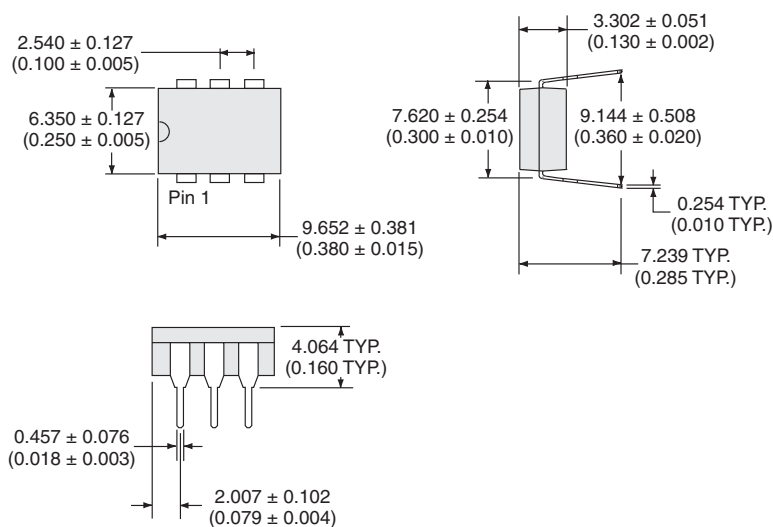
### Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

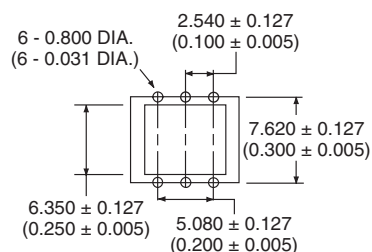


## MECHANICAL DIMENSIONS

### CPC1963G

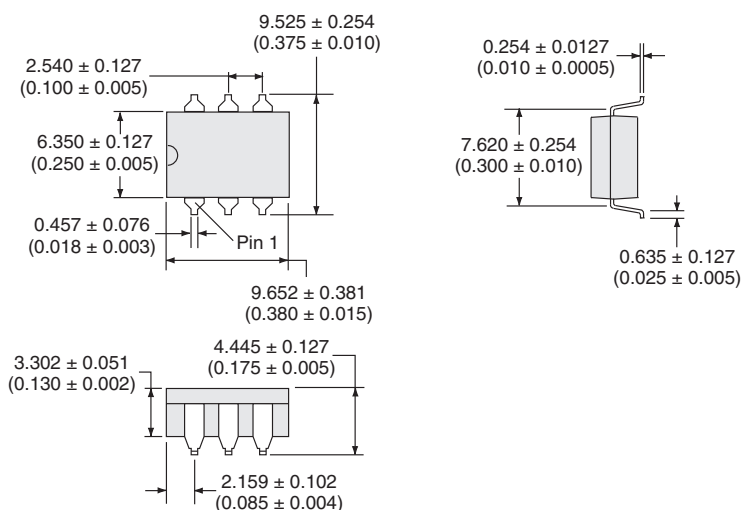


#### PCB Hole Pattern

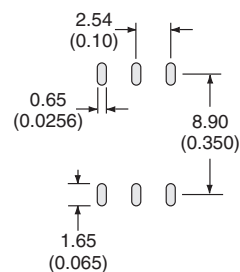


Dimensions  
mm  
(inches)

### CPC1963GS

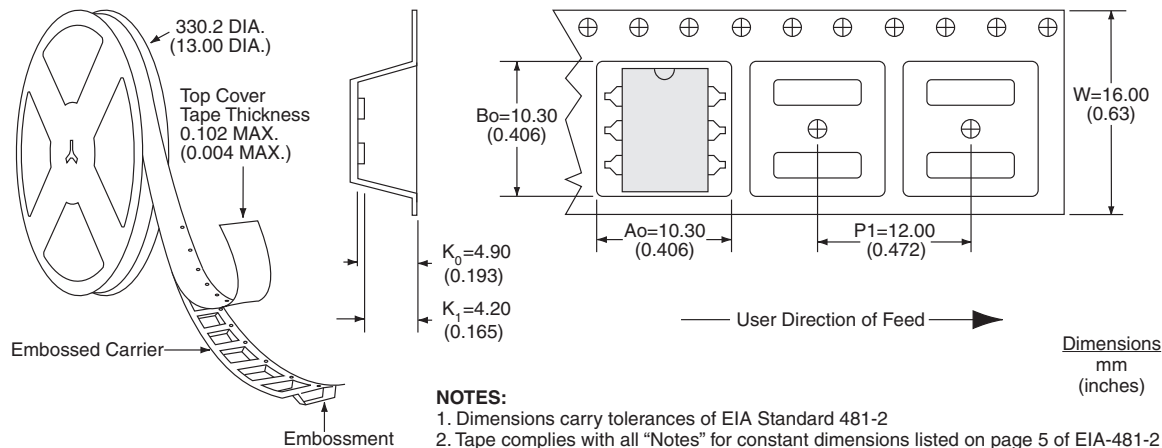


#### PCB Land Pattern



Dimensions  
mm  
(inches)

## CPC1963GS Tape & Reel



**For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)**

IXYS Integrated Circuits makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in IXYS Integrated Circuits' Standard Terms and Conditions of Sale, IXYS Integrated Circuits assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of IXYS Integrated Circuits' product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. IXYS Integrated Circuits reserves the right to discontinue or make changes to its products at any time without notice.

Specification: DS-CPC1963-R07  
©Copyright 2018, IXYS Integrated Circuits  
OptoMOS® is a registered trademark of IXYS Integrated Circuits  
All rights reserved. Printed in USA.  
6/20/2018